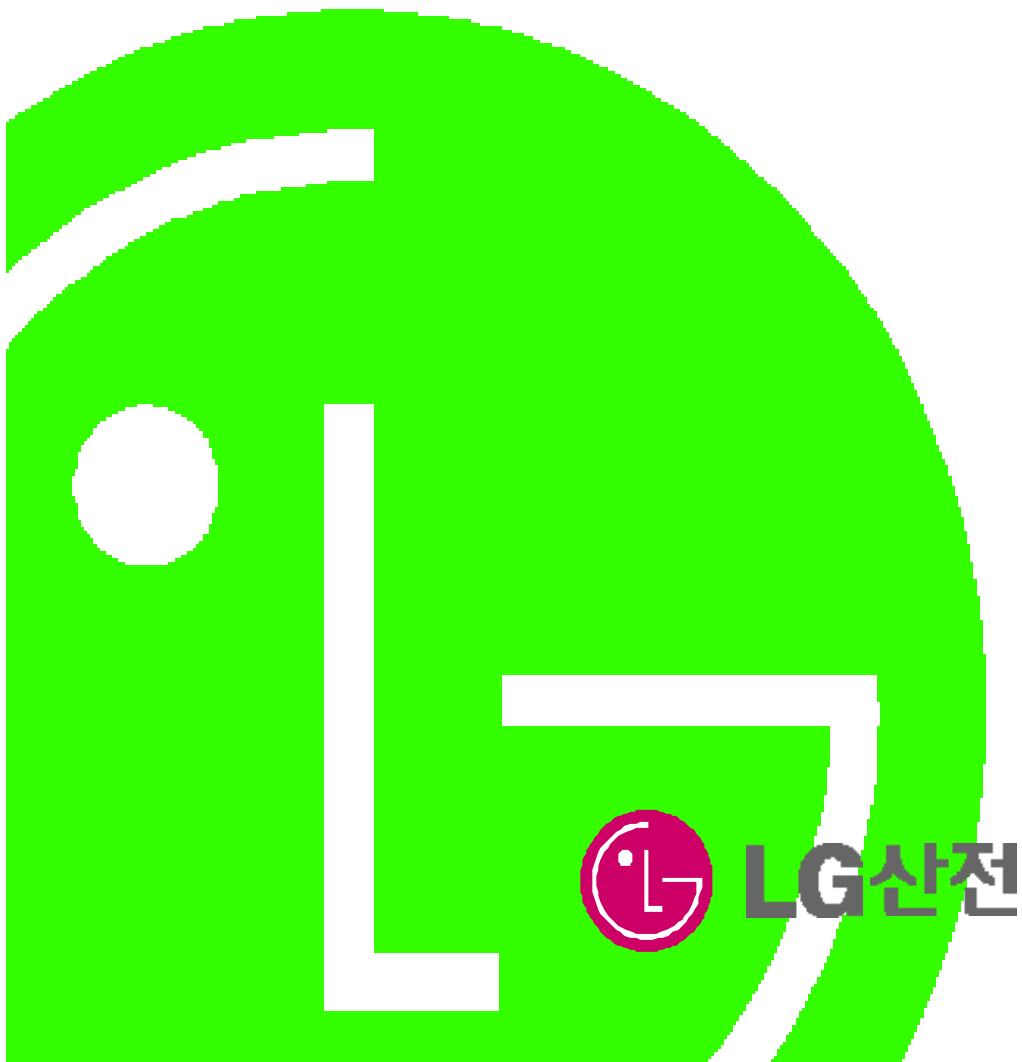


LG

Smart I/O

GLOFA-GM / MASTER-K

PROFIBUS-DP
DEVICENET
FIELDBUS
MODBUS





' 02. 03.		I/O



SMART I/O

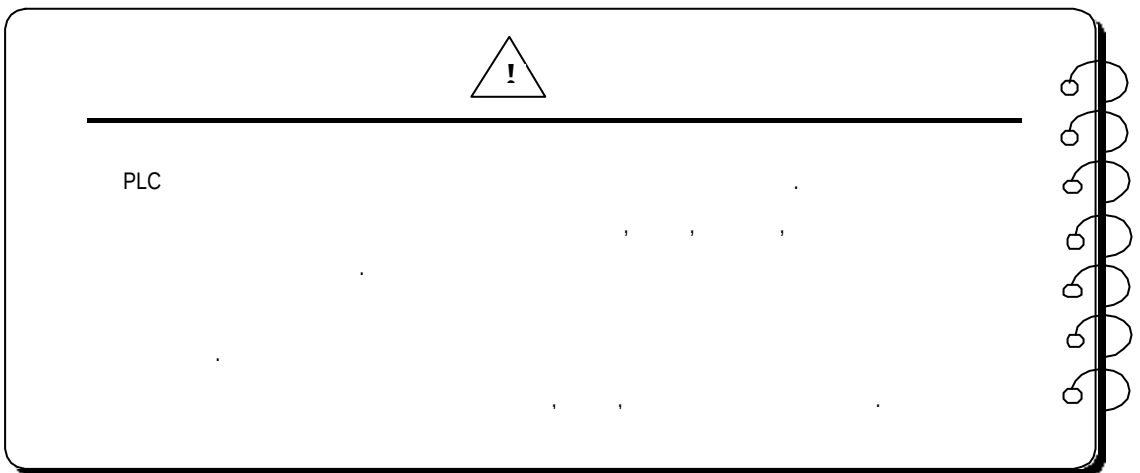
PLC

GLOFA-GM/MATER-K CPU



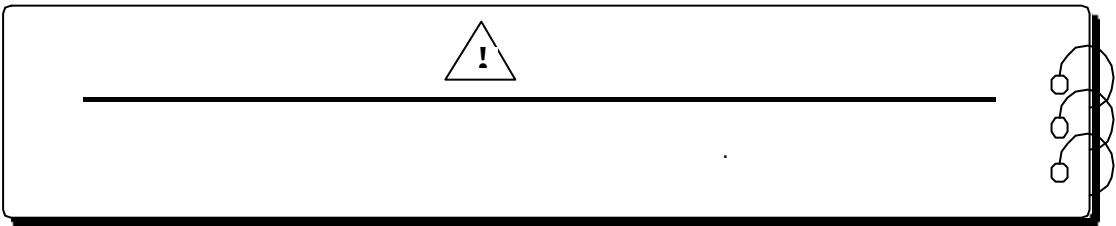
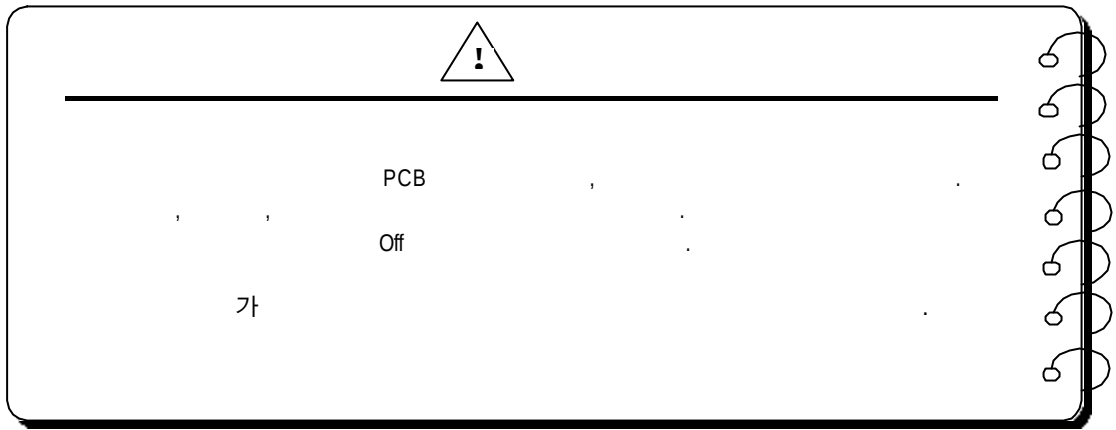
2

가



PLC





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SMART I/O

PLC

*CHAP.1**CHAP.2*

SMART I/O

CHAP.3

SMART I/O

CHAP.4

SMART I/O

CHAP.5 PROFIBUS-DP

PROFIBUS-DP(Pnet)

CHAP.6 DEVICENET

DEVICENET(Dnet)

CHAP.7 FIELDBUS

FIELDBUS(RNET)

CHAP.8 MODBUS

MODBUS(Snet)

CHAP.9

PLC

CHAP.10

PLC

가

CHAP.11

1.2 SMART I/O

1) SMART I/O

(1) (IEC 61131) (GLOFA)

- IEC 61131-3 (IL / LD / SFC)

(2)

(3) /CPU 가 .

(4) 32 ~ 64 가 .

- 64 (Rnet,Dnet)

- 32 (Pnet,Snet)

(5)

(6) , 가 .

(7)

(8) 가 .

- SMART I/O 가

(9) 가 .

(10)

- GMWIN/KGLWIN

- , Snet

(11)

- DC 16/32 ,TR 16/32 , 16

(12)

OPEN
- FIELDBUS,MODBUS,DEVICNET,PROFIBUS-DP

(13)

PLC

- CPU,

(14)

-

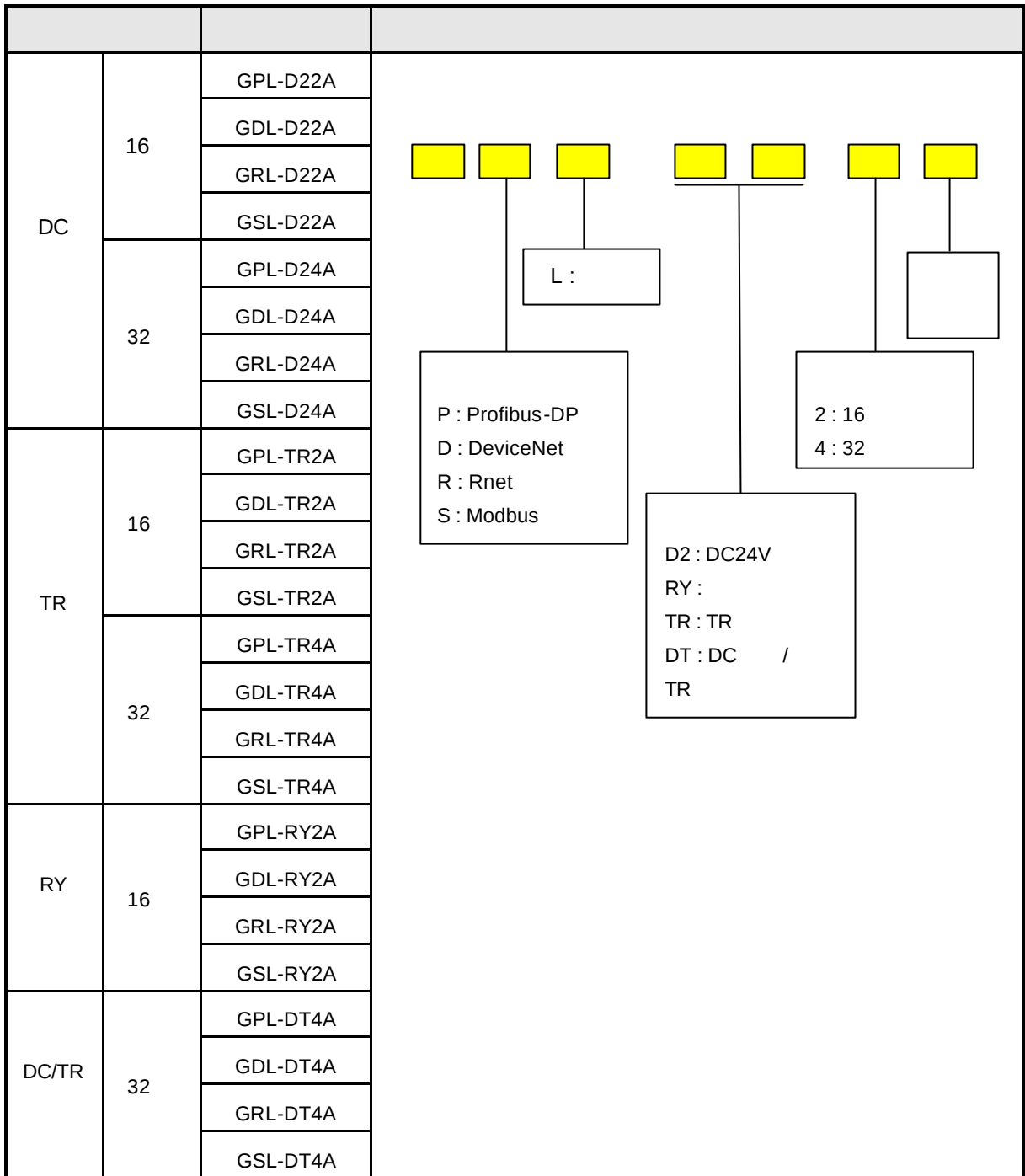
(15)

(16) 가 가 .

- PROFIBUS-DP, DEVICENET

1.3 SMART I/O

1.3.1



1.3.2

SMART I/O	PFOFIBUS-DP	GPL-TR2A	• () : DC24V() • : 16
		GPL-TR4A	• () : DC24V() • : 32
		GPL-DT4A	• () : DC24V() • () : DC 16 /TR 16
		GPL-RY2A	• () : DC24V() • : 16
		GPL-D22A	• () : DC24V() • : DC 16
		GPL-D24A	• () : DC24V() • : DC 32
	DEVICENET	GDL-TR2A	• () : DC24V() • : 16
		GDL-TR4A	• () : DC24V() • : 32
		GDL-DT4A	• () : DC24V() • () : DC 16 /TR 16
		GDL-RY2A	• () : DC24V() • : 16
		GDL-D22A	• () : DC24V() • : DC 16
		GDL-D24A	• () : DC24V() • : DC 32
	MODBUS	GSL-TR2A	• () : DC24V() • : 16
		GSL-TR4A	• () : DC24V() • : 32
		GSL-DT4A	• () : DC24V() • () : DC 16 /TR 16
		GSL-RY2A	• () : DC24V() • : 16
		GSL-D22A	• () : DC24V() • : DC 16
		GSL-D24A	• () : DC24V() • : DC 32

SMART I/O	FIELD BUS	GRL-TR2A	() : DC24V() : 16
		GRL-TR4A	() : DC24V() : 32
		GRL-DT4A	() : DC24V() () : DC 16 /TR 16
		GRL-RY2A	() : DC24V() : 16
		GRL-D22A	() : DC24V() : DC 16
		GRL-D24A	() : DC24V() : DC 32

2.1

SMART I/O

No							
1		0 ~ 55 °C					
2		-25 ~ +70 °C					
3		5 ~ 95%RH,					
4		5 ~ 95%RH,					
5					-	IEC6 1131-2	
			가		X, Y, Z 10		
		10 ≤ f < 57Hz	-	0.075mm			
		57 ≤ f ≤ 150Hz	9.8m/s ² {1G}	-			
			가				
		10 ≤ f < 57Hz	-	0.035mm			
		57 ≤ f ≤ 150Hz	4.9m/s ² {0.5G}	-			
6		- 가 : 147 m/s²{15G} - 가 : 11ms : (X, Y, Z 3 3)				IEC 61131-2	
7			AC : ± 1,500 V DC : ±900 V			LG	
			: 4kV ()			IEC 61131-2, IEC 801-2	
			27 ~ 500 MHz, 10 V/m			IEC 61131-2, IEC 801-3	
		/			(24V)	(24V)	IEC 61131-2 IEC 801-4
				2kV	1kV	0.25kV	
8		가 , 가					
9		2,000m					
10		2					
11							

1) IEC(International Electrotechnical Commission :)

: 가

2)

: 2 ,

2.2

SMART I/O

2.2.1

SMART I/O Profibus-DP(Pnet)

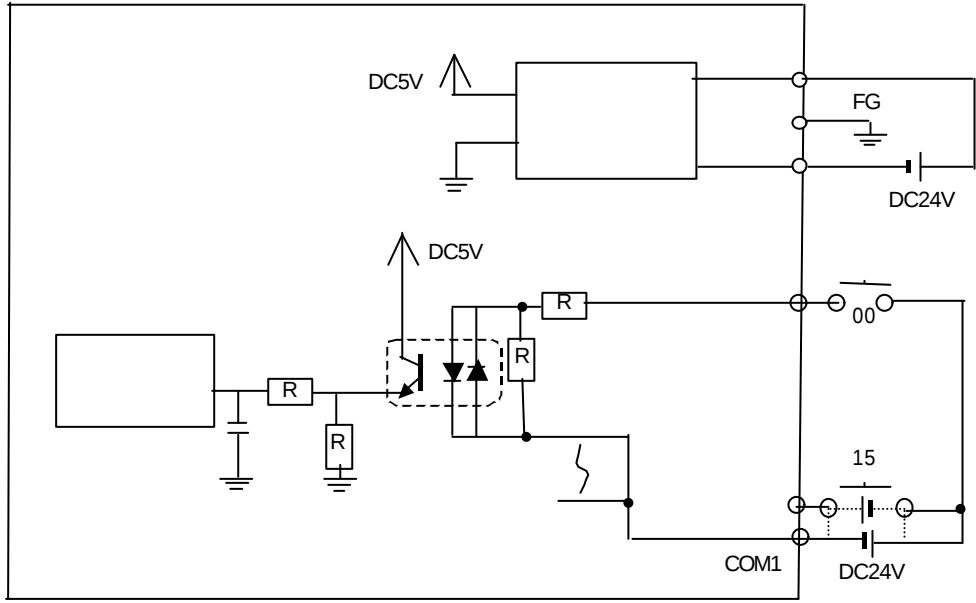
	GPL- TR2A	GPL- TR4A	GPL- RY2A	GPL- DT4A	GPL- D22A	GPL- D24A
	DC +24V (Max +28V, Min +19V)					
	0.4A(+24VDC)					
	40A : (24VDC)					
(+5V)	0.2~0.6A					
(+5V, Aux)	0.02~0.1A					
	60% (Full Load)					
	LED ON					
	150ms (DC19~24V , Full Load)					
	1.5 ~ 2.5mm ² (AWG16 ~ 22)					
	12kg · cm					

SMART I/O DeviceNet, Rnet, Modbus

	GD/R/SL- TR2A	GD/R/SL- TR4A	GD/R/SL- RY2A	GD/R/SL- DT4A	GD/R/SL- D22A	GD/R/SL- D24A
	DC +24V (Max +28V, Min +19V)					
	0.4A(+24VDC)					
	40A : (24VDC)					
(+5V)	0.2~0.6A					
	60% (Full Load)					
	LED ON					
	150ms (DC19~24V , Full Load)					
	1.5 ~ 2.5mm ² (AWG16 ~ 22)					
	12kg · cm					

2.3

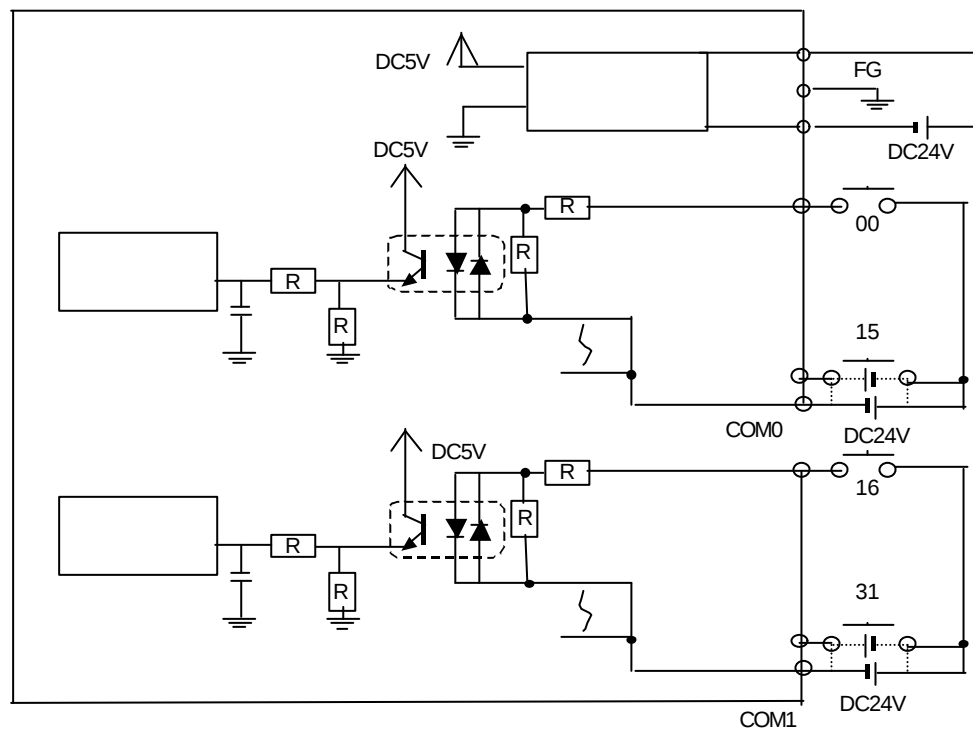
2.3.1 DC16 : GPL/GDL/GRL/GSL-D22A

		DC
		16
		DC24V
		7 mA
		DC20.4 ~ 28.8V (5%)
		100% (16 /1COM) On
On / On		DC19V / 3.5 mA
Off / Off		DC6V / 1.5 mA
		3.3 kΩ
	Off → On	3 ms
	On → Off	3 ms
		16 / COM
		200mA
		On LED
		(M3 X 6)
		160g
		

2.3.2 DC32

: GPL/GDL/GRL/GSL-D24A

		DC
		16
		DC24V
		7 mA
		DC20.4 ~ 28.8V (5%)
		100% (16 /1COM) On
On / On		DC19V / 3.5 mA
Off / Off		DC6V / 1.5 mA
		3.3 k Ω
	3 ms	3 ms
	3 ms	3 ms
		16 / COM
		300 mA
		On LED
		(M3 X 6)
		240g

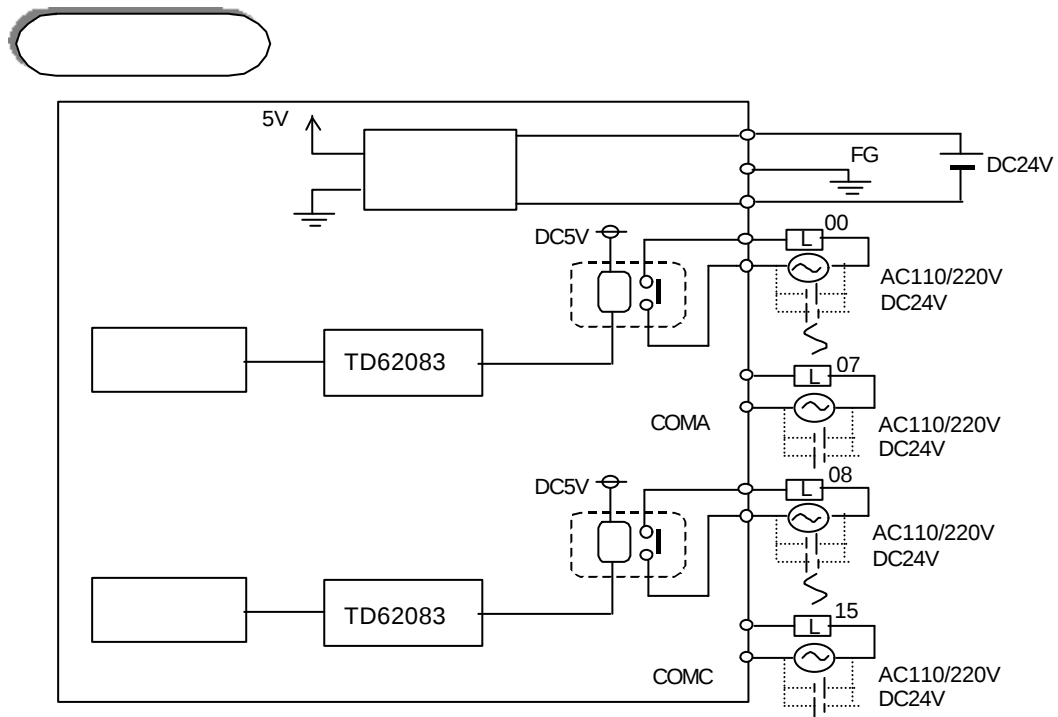


2.4

2.4.1 16

: GPL/GDL/GRL/GSL-RY2A

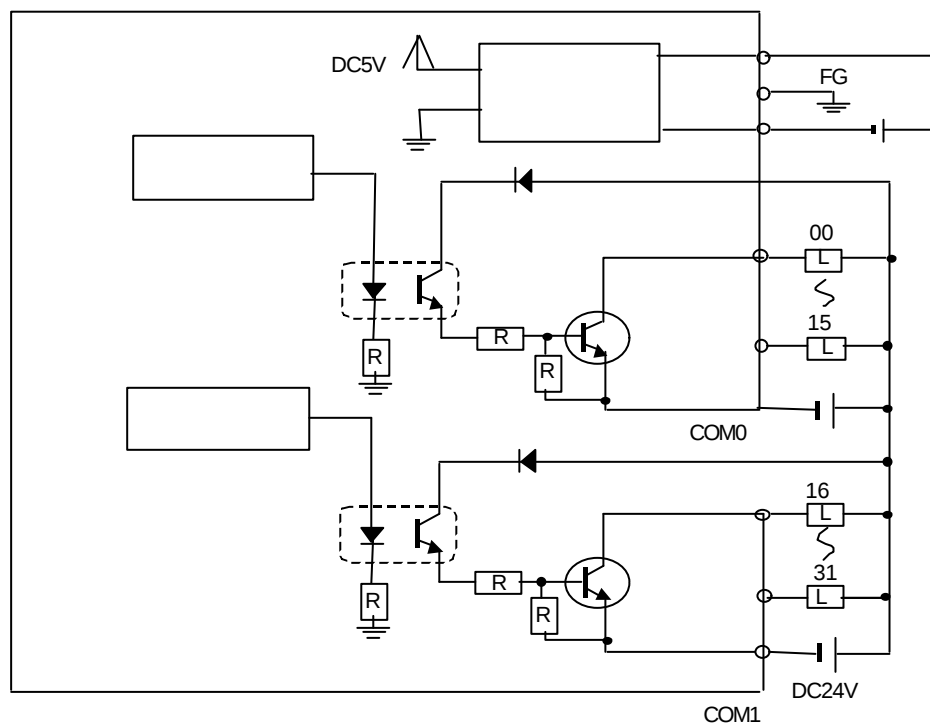
		16
/		DC24V 2A() / 1 , AC220V 2A(COS Ψ = 1)
() /		DC5V / 1mA, AC250V, DC110V
		1,200 /
		2,000
		/ 10
		AC200V / 1.5A, AC240V / 1A (COS Ψ = 0.7) 10
		AC200V / 1A, AC240V / 0.5A (COS Ψ = 0.35) 10
		DC24V / 1A, DC100V / 0.1A (L / R = 7ms) 10
	Off → On	10 ms
	On → Off	12 ms
		8 / COM
		550 mA (On)
		On LED
		(M3 X 6)
		300g



2.4.3 32

: GPL/GDL/GRL/GSL-TR4A

		32
		DC 24V
		DC 20.4 ~ 26.4V
		0.1A / 1 , 2A / 1COM
Off		0.1 mA
		0.4 A / 10 ms
On		DC 1.0 V
	Off → On	2 ms
	On → Off	2 ms
		16 / 1 COM
		380 mA (On)
		DC 24V ± 10 % (4Vp-p)
		40 mA (DC 24V 1 COM)
		On LED (16)
		(M3 X 6)
		240g

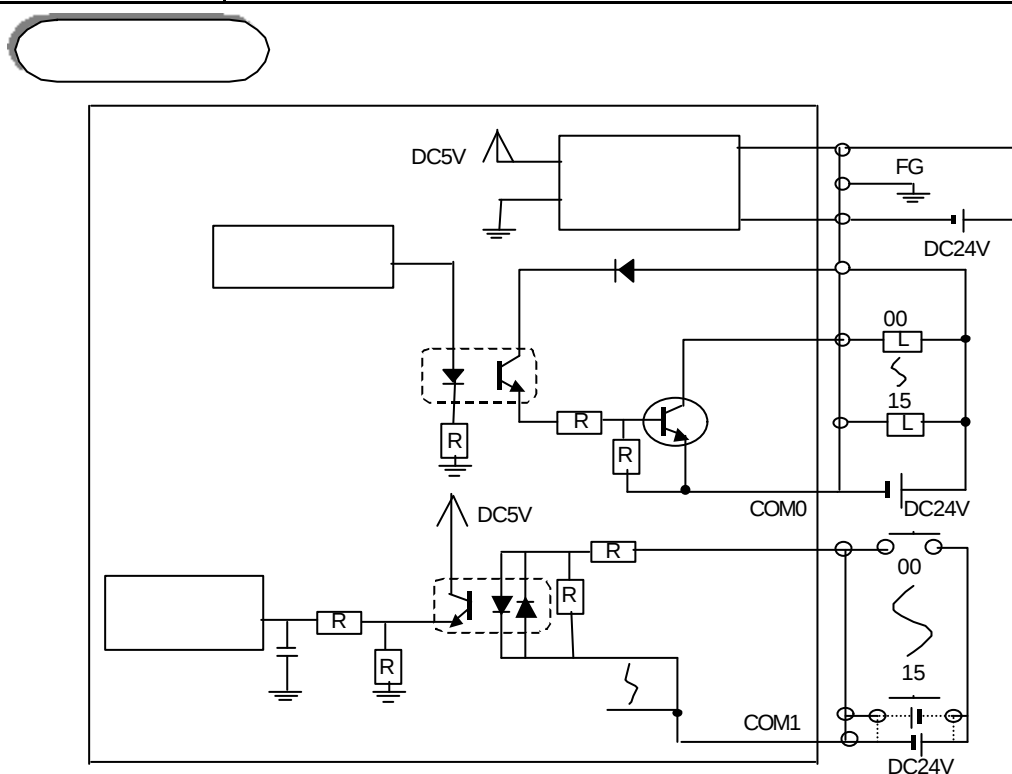


2.5

2.5.1 32

(DC16/TR16) : GPL/GDL/GRL/GSL-DT4A

	16		16
	DC 24V		DC24V
	7 mA		0.1A/1 , 2A/1COM
	DC20.4~26.4V (5%)	/	AC250V, DC125V
	100% On	Off	0.1 mA
On /On	DC19V / 3.0 mA		4A/10ms
Off /Off	DC6V / 1.5 mA		
	3.3 kΩ		
	Off→ On	Off → On	2 ms
	3 ms	2 ms	2 ms
	On → Off		
	3 ms		
	16 / COM		16 / 1COM
	On LED		On LED
	(M3 × 6)		
	350 mA		
	240g		



2.6

2.6.1 PROFIBUS-DP

	Profibus-DP
Standard	EN 50170 / DIN 19245
	RS-485(Electric)
Medium Access	POLL
	BUS
	NRZ
	Shielded Twisted Pair
	1200m (9.6K ~187Kbps)
	400m (500 Kbps)
	200m (1.5 Mbps)
	100m (3M ~ 12Mbps)
	126
()	32
I/O	64Byte

2.6.2 DEVICENET

	DeviceNet
	CAN Protocol
Medium Access	POLL
	BUS
	Class 2 Thick/Thin Cable(Allen-Bradley)
	125/250/500 kbps
(Thick)	500/250/100 m
	125 kbps 6m(156m)
	250 kbps 6m(78m)
	500 kbps 6m(39m)
	0 ~ 8 Byte(64 Bits)
	• / • /
	• / • Peer-to-Peer • Strobe,Poll,COS/Cyclic
	64 MAC I/MAC Identifier
	32 I/O(2,048 I/O)
	On
	DC 24V

2.6.3 FIELDBUS

	Fnet
	20ms
	1Mbps
	2
	BUS
	750m
	64 ()
	Manchester Biphase-L
	CRC-CCITT Time Over Retry
	9-PIN
	TWISTED PAIR SHIELDED CABLE
	3,840 Word()
	1,920 Word()
	63
	60 Word

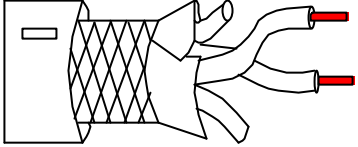
2.6.4 MODBUS

	RS-422/485
	MODBUS-RTU
	8 Byte
	BUS
	TWISTED PAIR SHIELDED CABLE
	2400 ~ 38,400 BPS
	500m
Medium Access	POLL
	32
	32

2.7

2.7.1 PROFIBUS-DP

- Belden Network
 - Type : Network Components
 - Protocol : FMS-DP-PA
 - Certification : No
 - Order No. : 3076F, 3077F, 3079A

	Twinax	
AWG	22	
	BC-Bare Copper	
	PE-Polyethylen	
	0.035 (Inch)	
Shield	Aluminum Foil- Polyester Tape/Braid Shield	
	8500 pF/ft	
	150 Ω	
	2 (Core)	

2.7.2 DEVICENET

	Class 2 Thick/Thin Cable	
	Allen-Bradley	/
	Round	
	30V/100VA	
	100VA/24V or 4A	
	12.2mm/6.9mm	
	5	

Class 2 Thick Cable
Spool Size
50m
150m
300m
500m

Class 2 Thin Cable
Spool Size
50m
150m
300m
600m

SMART I/O Dnet I/F

5 가 . DC 24V

Thick

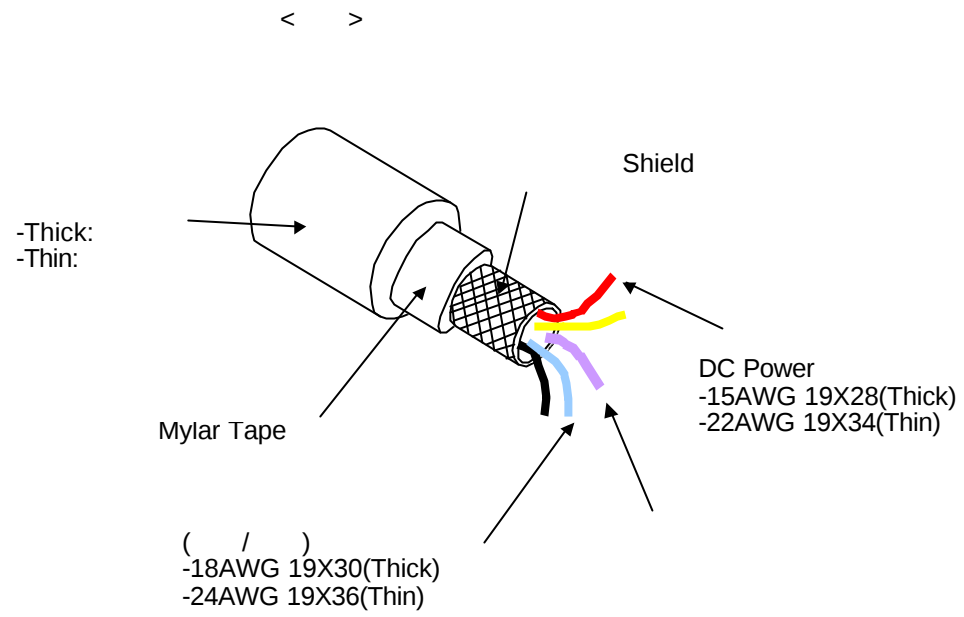
Thin

/

가

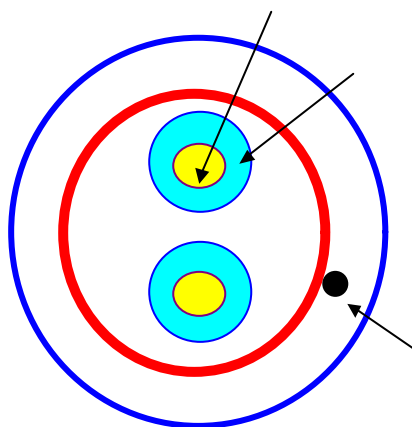
	CAN_H	
	CAN_L	
Bare	Drain	
	V-	
	V+	

	Thick	Thin
125kbps	500m	100m
250kbps	250m	100m
500kbps	100m	100m



2.7.3 FIELDBUS

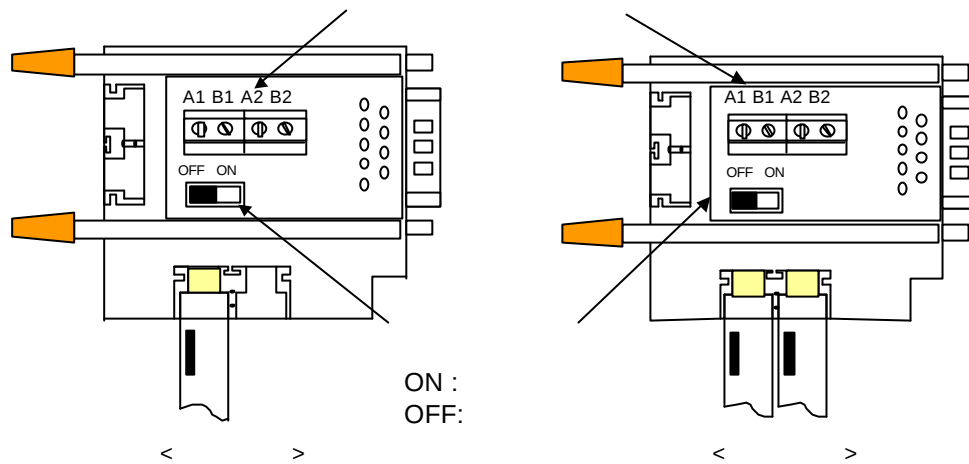
	Low Capacitance Lan Interface Cable		
	LIREV-AMESB		
	2*0.64 mm (GS 92-3032,22 AWG)		
	LG		
	Ω/km	59	
(DC)	V/min	500V 1	
	MEGA Ω-km	1,000	
	pF/m	45	1 kHz
	Ω	120 ± 12	10MHz
		CORE	2
		AWG	18
		NO./mm	1/1.0
		mm	1.0
		mm	0.9
		mm	2.8



2.8

2.8.1 PROFIBUS-DP

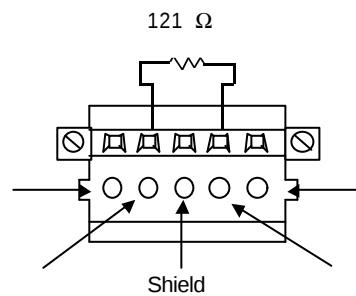
•



2.8.2 DEVICENET

•

- 121Ω, 1%, 1/4W
- CAN_H CAN_L



1)

2.8.3 FIELDBUS

SMART I/O FIELDBUS(Rnet)

6 , 7

, SMART I/O

Rnet

8 , 9

6

SMART I/O

8

, 7

9

가

9

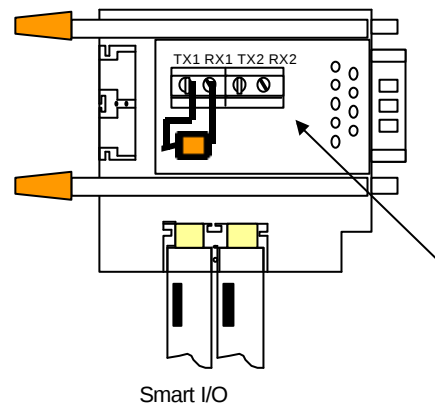
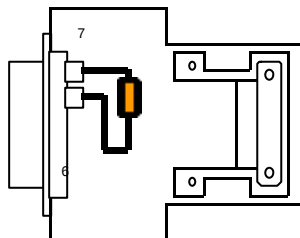
가

- : 110Ω , 1/2W
- - : Pin 6 , 7
- Smart I/O : Pin 8 , 9

□

(110Ω , 1/2W)

□

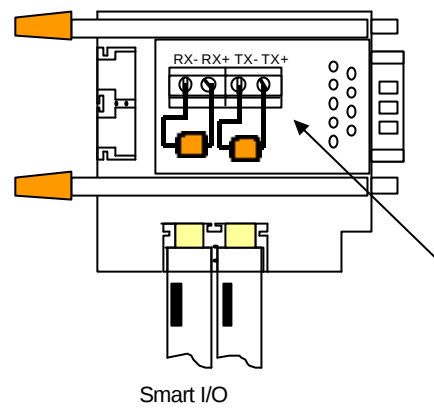
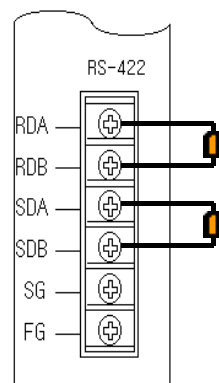


2.8.4 MODBUS

RS-422

(1/2W)
120Ω

1/2W



SMART I/O

3.1

I/O

1)

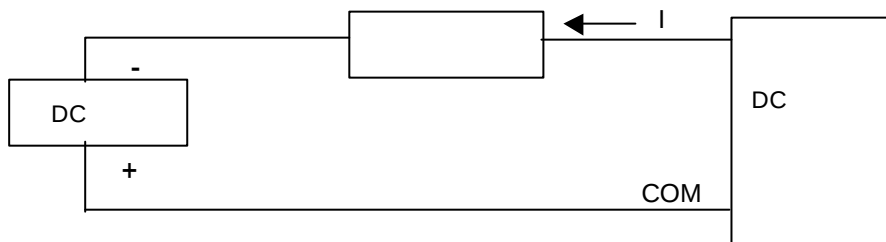
DC

I/O

/

(1)

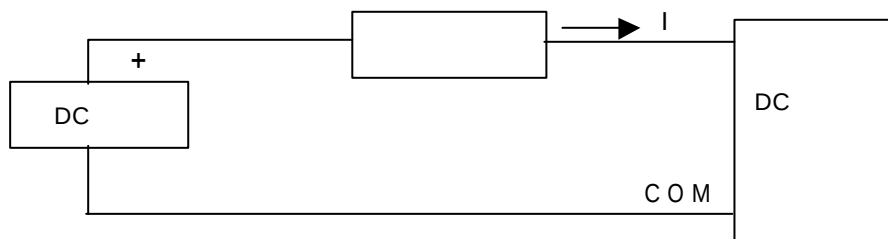
DC



- 가 DC DC (-)
- On DC 가

(2)

DC



- 가 DC DC (+)
- On DC 가 DC

2)

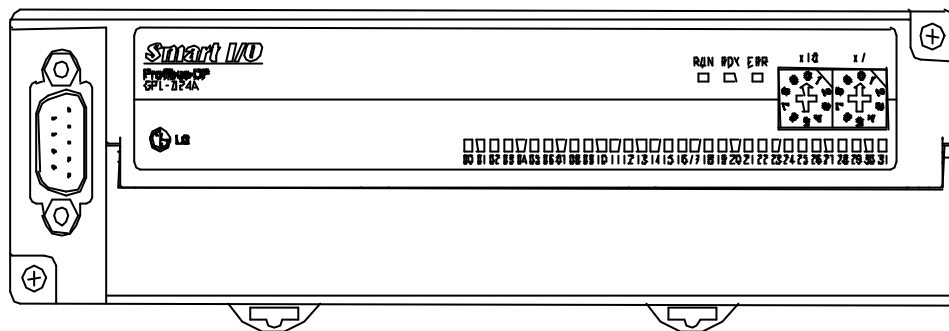
가

3.2

3.2.1

SMART I/O 4 가 가 . 가 가
 . SMART I/O 가 OPEN (FIELD BUS)

SMART I/O



SMART I/O 가 ()

가		16	32
	PFOFIBUS-DP	• GPL-TR2A / GPL-TR4A / GPL-RY2A • GPL-D22A / GPL-D24A • GPL-DT4A	
	DEVICENET	• GDL-TR2A / GDL-TR4A / GDL-RY2A • GDL-D22A / GDL-D24A • GDL-DT4A	
	FIELD BUS	• GRL-TR2A / GRL-TR4A / GRL-RY2A • GRL-D22A / GRL-D24A • GRL-DT4A	
	MODBUS	• GSL-TR2A / GSL-TR4A / GSL-RY2A • GSL-D22A / GSL-D24A • GSL-DT4A	

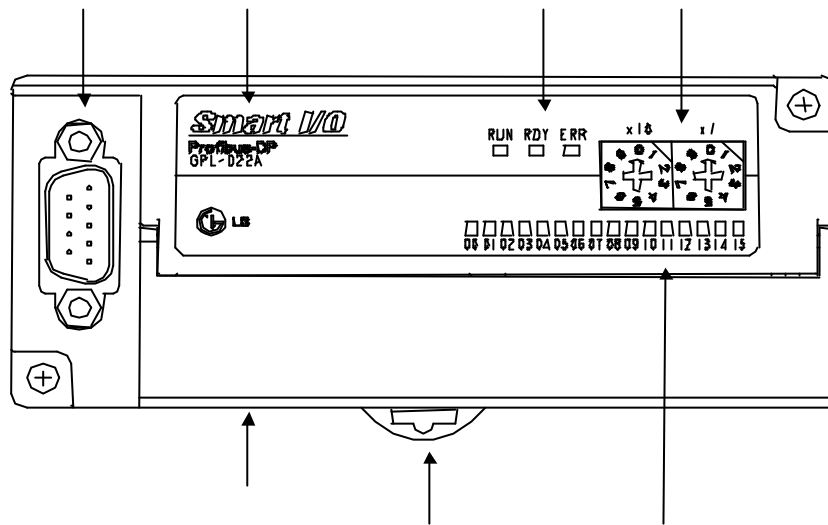
3.2.2 SMART I/O

3.2.2.1 PROFIBUS-DP, FIELDBUS, MODBUS

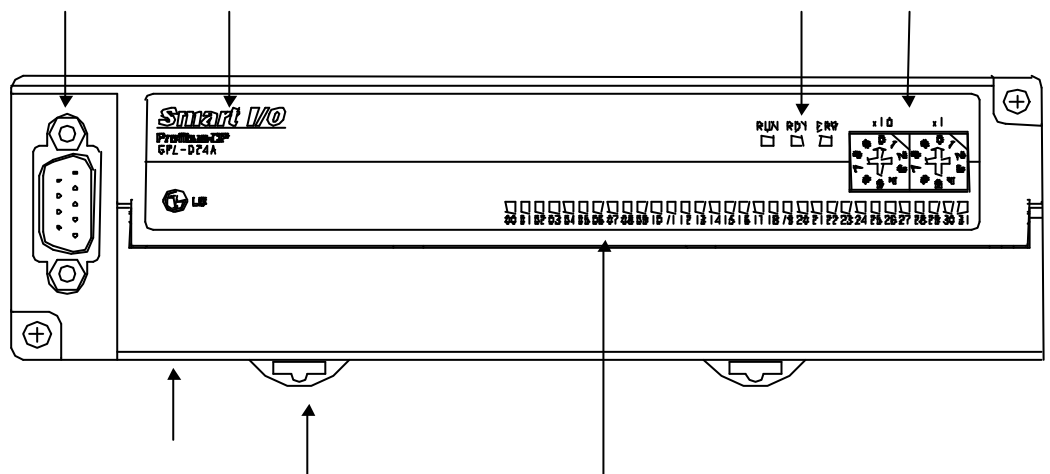
PROFIBUS-DP, FIELDBUS, MODBUS

가

16



32



1) PROFIBUS-DP

No.			
			/
			• 9
	SMART I/O		- PROFIBUS-DP - GPL-D22A : DC 16 - GPL-D24A : DC 32 - GPL-TR2A : TR 16 - GPL-TR4A : TR 32 - GPL-RY2A : 16 - GPL-DT4A : DC 16 /TR 16
	LED	PWR LED	- On : - Off :
		TX LED	: : LED 가 Off .
		RX LED	: - Off :
			. 0~99 가 (0) - X10 : 10 - X1 : 1
	LED		
	DIN (HOOK)		DIN
			* 3.3

2) FIELDBUS

No.			
			/
			• 9
	SMART I/O		- FIELDBUS - GRL-D22A : DC 16 - GRL-D24A : DC 32 - GRL-TR2A : TR 16 - GRL-TR4A : TR 32 - GRL-RY2A : 16 - GRL-DT4A : DC 16 /TR 16
	LED	PWR LED	- On : - Off :
		TX LED	: (GRL-TR4A) : LED 가 Off
		RX LED	: - Off :
			. 0~63 가 - X16 : 16 - X1 : 1
	LED		
	DIN (HOOK)		DIN
			* 3.3

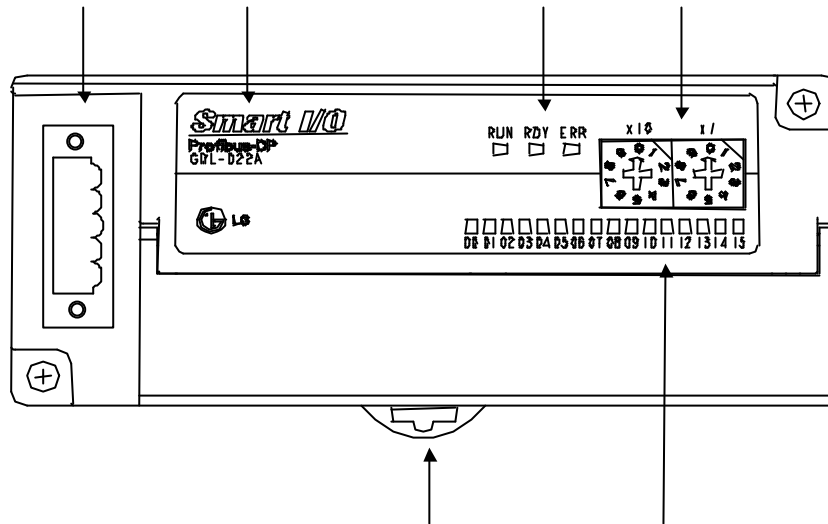
3) MODBUS

No.			
			/
			• 9
	SMART I/O		- MODBUS - GSL-D22A : DC 16 - GSL-D24A : DC 32 - GSL-TR2A : TR 16 - GSL-TR4A : TR 32 - GSL-RY2A : 16 - GSL-DT4A : DC 16 /TR 16
	LED	PWR LED	- On : - Off :
		TX LED	: : LED 가 Off
		RX LED	: - Off :
			. 0~31 가 - X16 : 16 - X1 : 1
	LED		
	DIN (HOOK)		DIN
			* 3.3

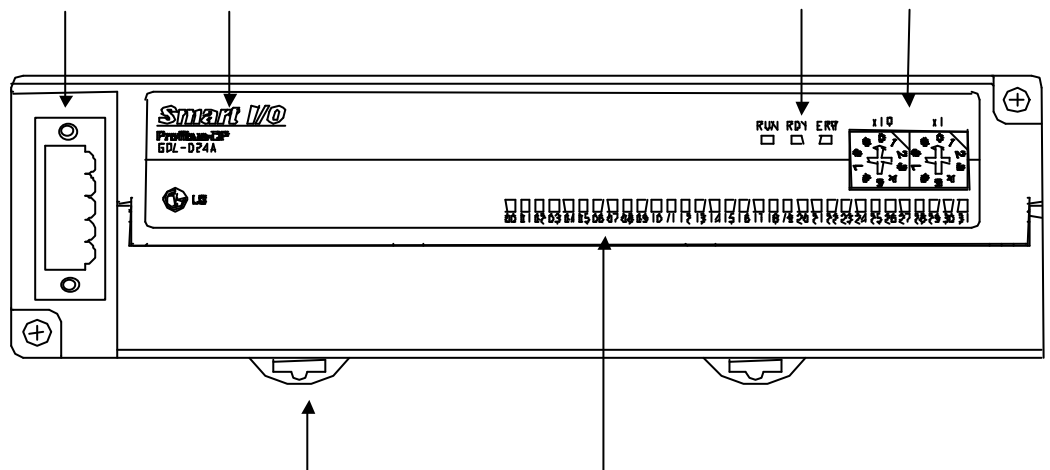
3.2.2.2 DEVICENET

DEVICENET

16



32



1) DEVICENET

No.			
			/
			• 5 OPEN
	SMART I/O		• DEVICENET GDL-D22A : DC 16 GDL-D24A : DC 32 GDL-TR2A : TR 16 GDL-TR4A : TR 32 GDL-RY2A : 16 GDL-DT4A : DC 16 /TR 16
	LED	PWR LED	• On : • Off :
		MS LED	• : CPU • :
		NS LED	• :
			가 • X16 : 16 • X1 : 1
	LED		
	DIN (HOOK)		• DIN
			• * 3.3

3.3

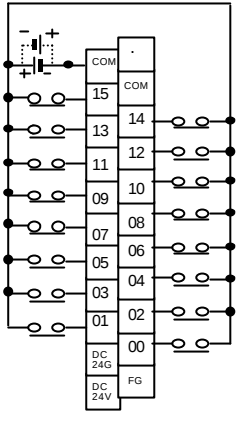
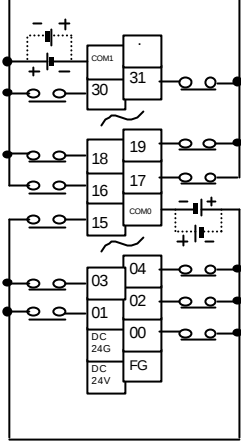
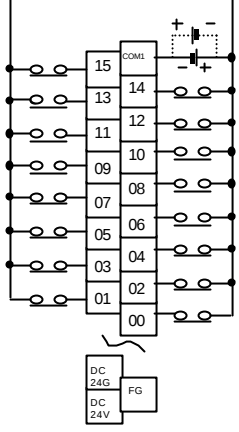
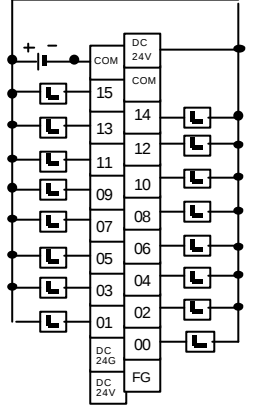
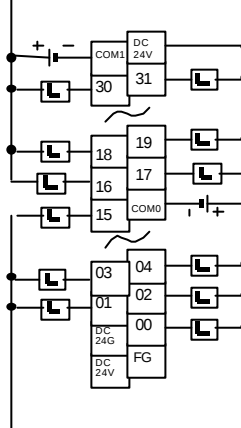
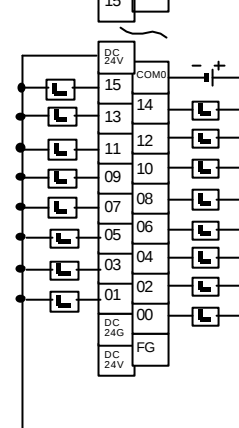
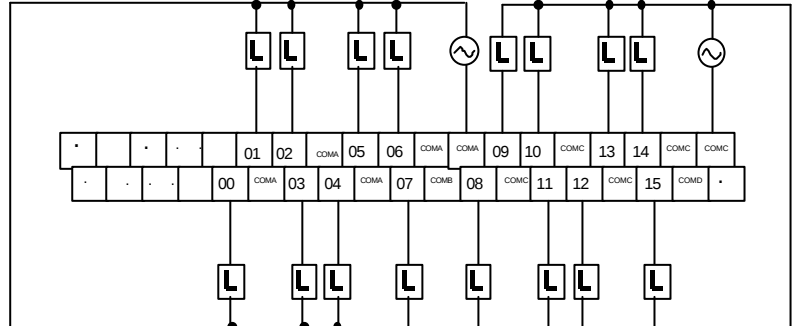
3.3.1 SMART I/O

3.3.1.1 PROFIBUS-DP

1)

GPL-D22A / GPL-D24A	0 ~ 15	(GPL-D22A)
	0 ~ 31	(GPL-D24A)
	COM	Common (16 COM)(GPL-D22A)
	COM0/COM1	Common (16 COM)(GPL-D24A)
	FG	FG
	DC 24V	DC 24V(+)
	DC 24G	DC 24V(-)
GPL-DT4A	0 ~ 15/0 ~ 15	/
	COM0/COM1	Common (16 COM)
	FG	FG
	DC 24V	DC 24V(+)
	DC 24G	DC 24V(-)
GPL-TR2A / GPL-TR4A	0 ~ 15	(GPL-TR2A)
	0 ~ 31	(GPL-TR4A)
	COM	Common (16 COM) (GPL-TR2A)
	COM0/COM1	Common (16 COM) (GPL-TR4A)
	FG	FG
	24V	DC 24V(+)
	24G	DC 24V(-)
GPL-RY2A	0 ~ 15	
	COMA~COMD	Common (8 COM)
	FG	FG
	DC 24V	DC 24V(+)
	DC 24G	DC 24V(-)

2)

DC	[GPL-D22A]  [GPL-D24A]  [GPL-DT4A()] 
TR	[GPL-TR2A]  [GPL-TR4A]  [GPL-DT4A()] 
RELAY	[GPL-RY2A]  * COMA COMB

3.3.1.2 DEVICENET

1)

GDL-D22A / GDL-D24A	0 ~ 15	(GDL-D22A)
	0 ~ 31	(GDL-D24A)
	COM	Common (16 COM)(GDL-D22A)
	COM0/COM1	Common (16 COM)(GDL-D24A)
GDL-DT4A	0 ~ 15/0 ~ 15	/
	COM0/COM1	Common (16 COM)
GDL-TR2A / GDL-TR4A	0 ~ 15	(GDL-TR2A)
	0 ~ 31	(GDL-TR4A)
	COM	Common (16 COM) (GDL-TR2A)
	COM0/COM1	Common (16 COM) (GDL-TR4A)
GDL-RY2A	0 ~ 15	
	COMA~COMD	Common (8 COM)

2)

DC			
	[GDL-D22A]	[GDL-D24A]	[GDL-DT4A()]
TR			
	[GDL-TR2A]	[GDL-TR4A]	[GDL-DT4A()]
RELAY			
	[GDL-RY2A]		

3.3.1.3 FIELD BUS

1)

GRL-D22A / GRL-D24A	0 ~ 15	(GRL-D22A)
	0 ~ 31	(GRL-D24A)
	COM	Common (16 COM)(GRL-D22A)
	COM0/COM1	Common (16 COM)(GRL-D24A)
	FG	FG
	DC 24V	DC 24V(+)
	DC 24G	DC 24V(-)
GRL-DT4A	0 ~ 15/0 ~ 15	/
	COM0/COM1	Common (16 COM)
	FG	FG
	DC 24V	DC 24V(+)
	DC 24G	DC 24V(-)
GRL-TR2A / GRL-TR4A	0 ~ 15	(GRL-TR2A)
	0 ~ 31	(GRL-TR4A)
	COM	Common (16 COM) (GRL-TR2A)
	COM0/COM1	Common (16 COM) (GRL-TR4A)
	FG	FG
	24V	DC 24V(+)
	24G	DC 24V(-)
GRL-RY2A	0 ~ 15	
	COMA~COMD	Common (8 COM)
	FG	FG
	DC 24V	DC 24V(+)
	DC 24G	DC 24V(-)

2)

DC	[GRL-D22A]	[GRL-D24A]	[GRL-DT4A()]
TR	[GRL-TR2A]	[GRL-TR4A]	[GRL-DT4A()]
RELAY	[GRL-RY2A]		

3.3.1.4 MODBUS

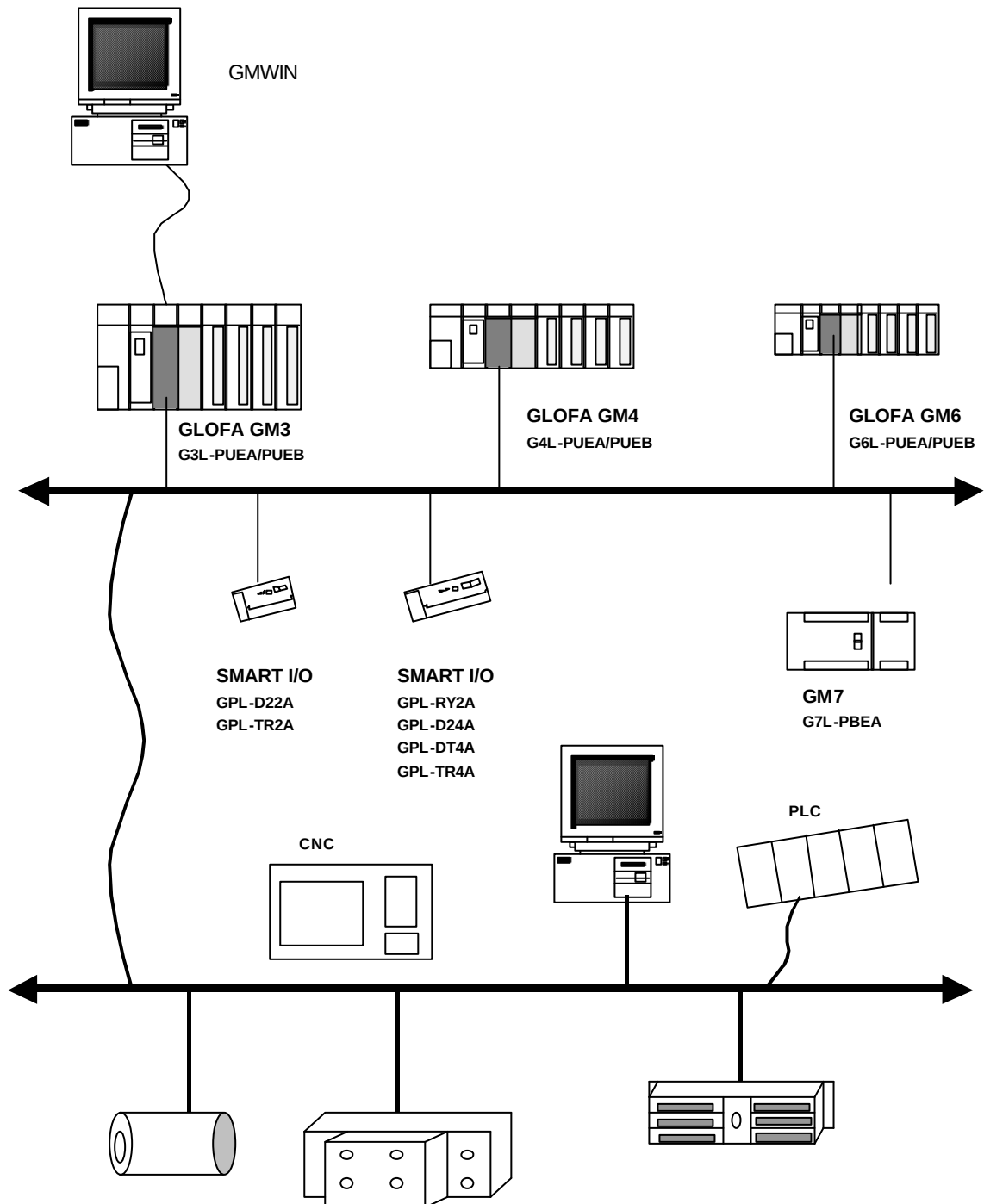
1)

GSL-D22A / GSL-D24A	0 ~ 15	(GSL-D22A)
	0 ~ 31	(GSL-D24A)
	COM	Common (16 COM)(GSL-D22A)
	COM0/COM1	Common (16 COM)(GSL-D24A)
	FG	FG
	DC 24V	DC 24V(+)
	DC 24G	DC 24V(-)
GSL-DT4A	0 ~ 15/0 ~ 15	/
	COM0/COM1	Common (16 COM)
	FG	FG
	DC 24V	DC 24V(+)
	DC 24G	DC 24V(-)
GSL-TR2A / GSL-TR4A	0 ~ 15	(GSL-TR2A)
	0 ~ 31	(GSL-TR4A)
	COM	Common (16 COM) (GSL-TR2A)
	COM0/COM1	Common (16 COM) (GSL-TR4A)
	FG	FG
	24V	DC 24V(+)
	24G	DC 24V(-)
GSL-RY2A	0 ~ 15	
	COMA~COMD	Common (8 COM)
	FG	FG
	DC 24V	DC 24V(+)
	DC 24G	DC 24V(-)

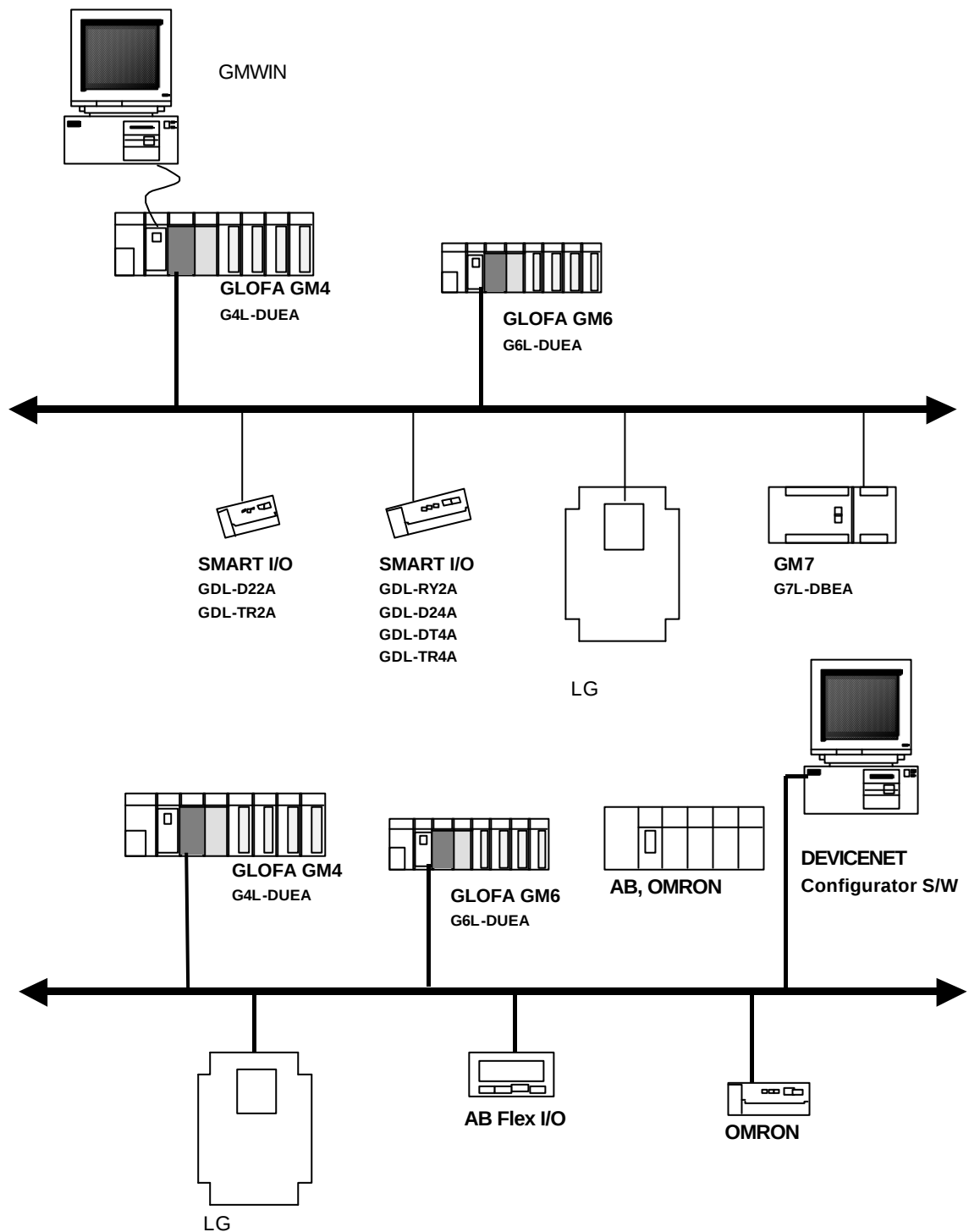
2)

DC	[GSL-D22A]	[GSL-D24A]	[GSL-DT4A()]
TR	[GSL-TR2A]	[GSL-TR4A]	[GSL-DT4A()]
RELAY	[GSL-RY2A] * COMA COMB		

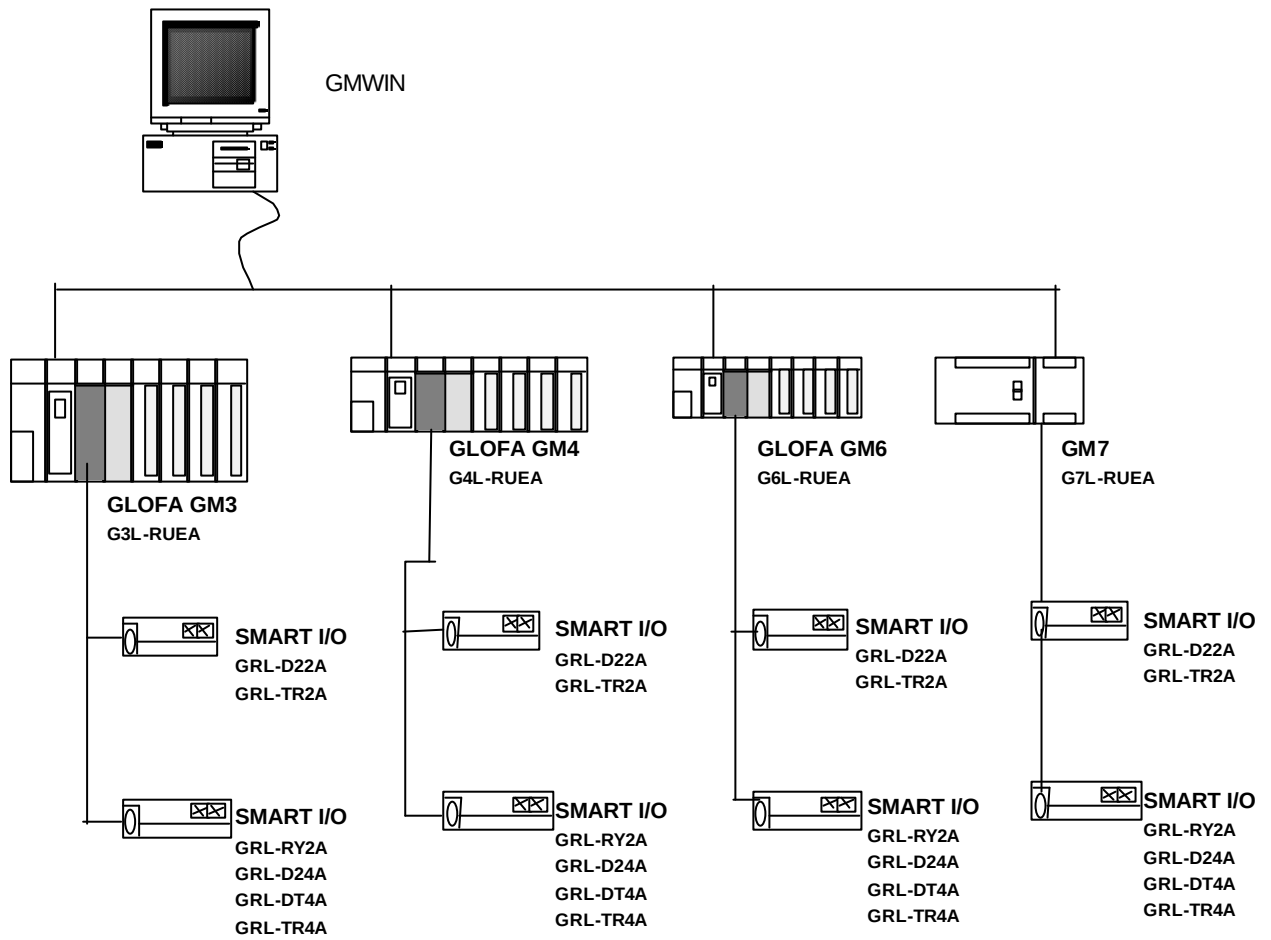
3.4.1 PROFIBUS-DP



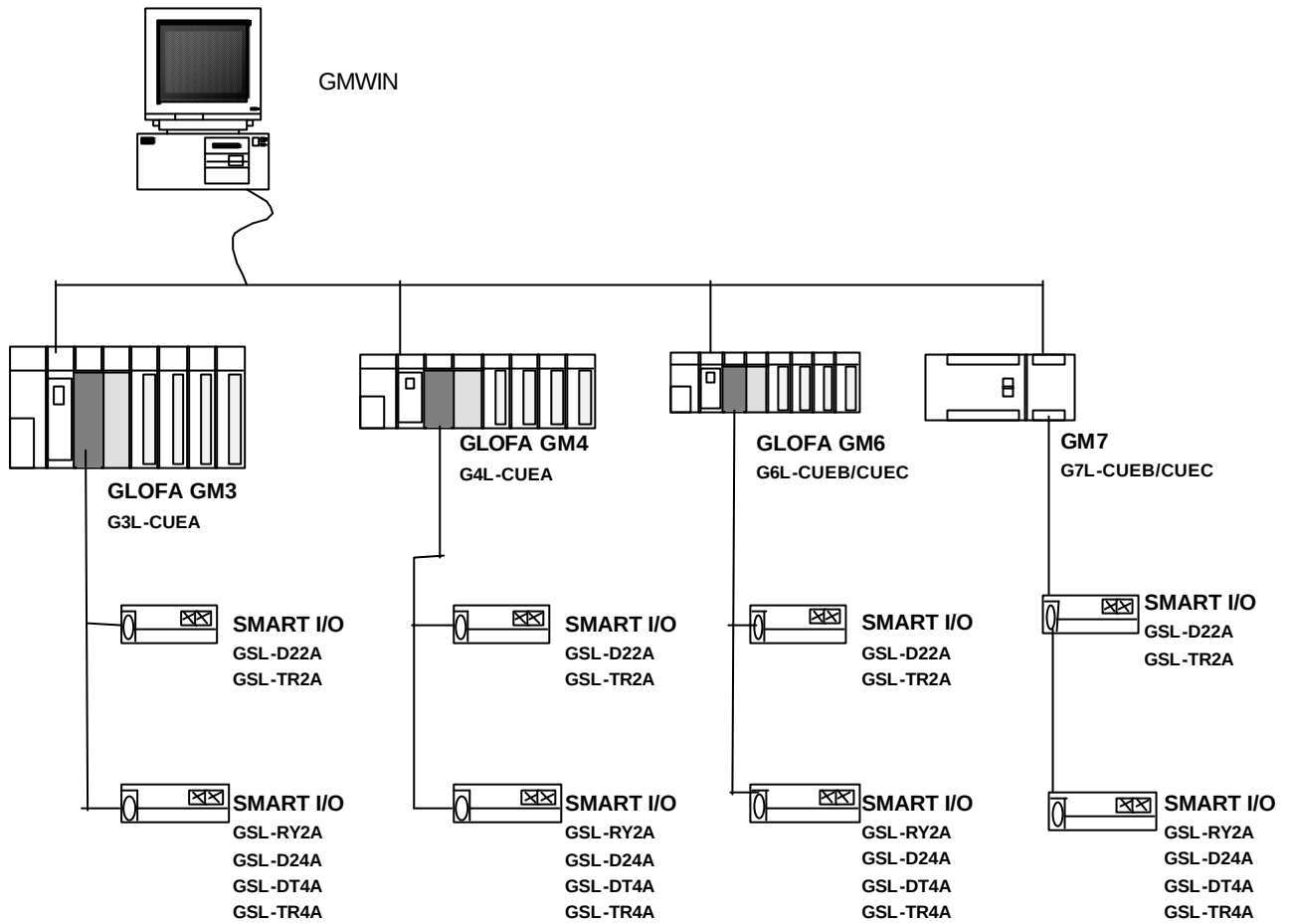
3.4.2 DEVICENET



3.4.3 FIELDBUS



3.4.4 MODBUS



4.1

SMART I/O

2가

1)

()

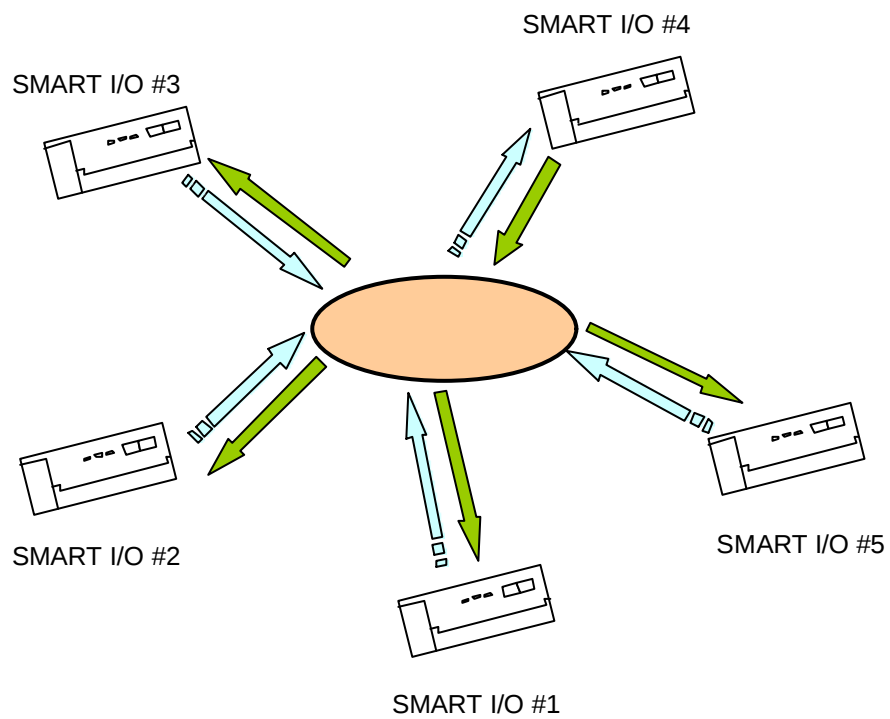
GLOFA

GMWIN, MASTER-K

KGLWIN

가 , 20 ms 1 (16) Rnet 3,840 가

가



2) (GLOFA-GM) / (MASTER-K)

가

가

GLOFA

GMWIN, MASTER-K

KGLWIN

(Enable)

(16)

GLOFA

Bit, Byte, Word
(Word)

MASTER-K

		/
	1 (16)	GLOFA: 가 (Bit, Word) MASTER-K: (Word)
	20ms() ~ 10 (1)	(Enable)
	→ PLC →	GLOFA: → PLC → MASTER-K: PLC →
CPU	CPU RUN, STOP, PAUSE	CPU

1)

SMART I/O

Profibus-DP, DeviceNet, Rnet

2)

Modbus

4.2

4.2.1

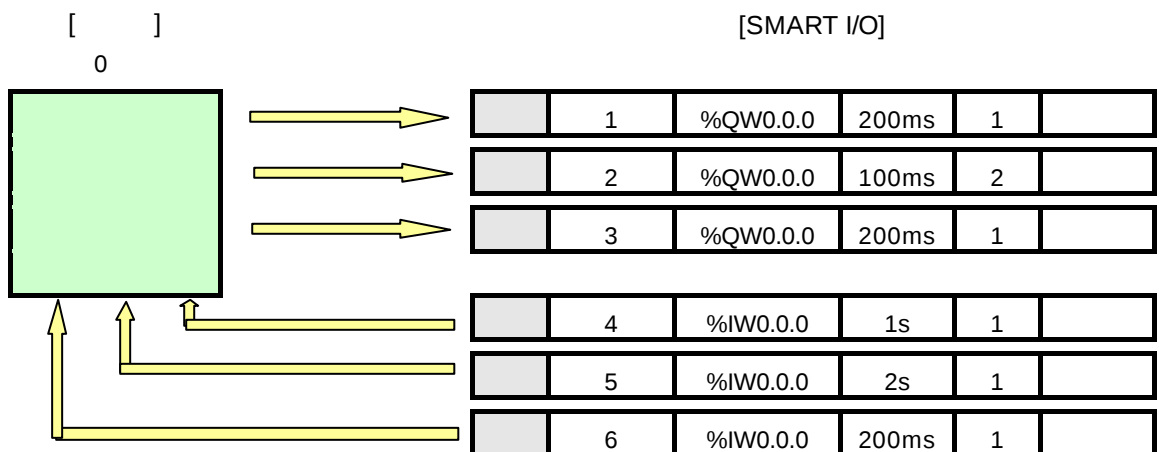
- 가
- ,
- ,
- .
- :
 (1) , 32 64
 (2) 60
 (3) 가 3,840 가
 - :
 가
 가 20ms() 10
 - :
 I/O
 - :
 (Keyword)

()

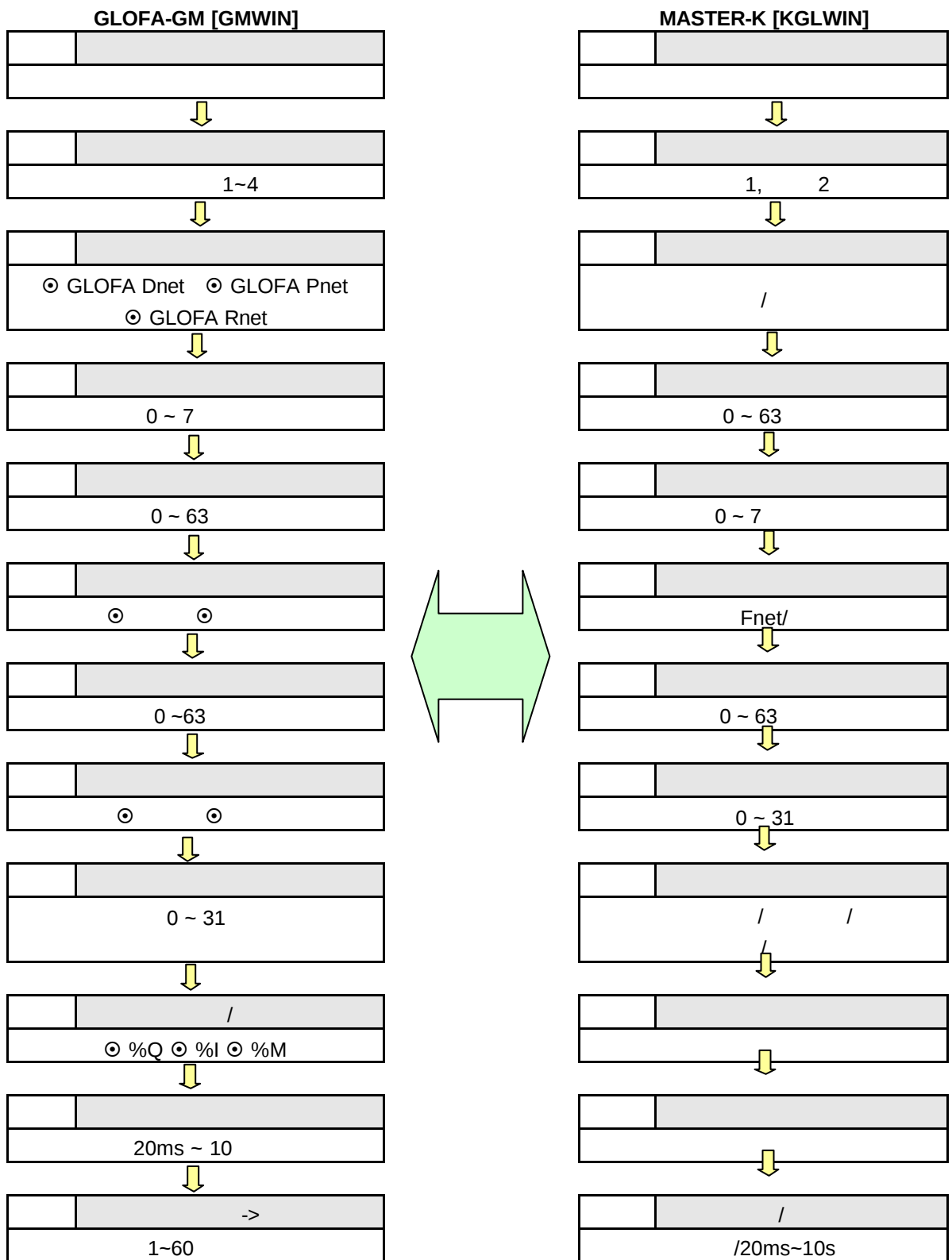
		()	()		()	
SMART I/O	G3/4/6/7L- RUEA	3,840	1,920	64(0-63)	60	Rnet I/F Module
	G3/4/6L- PUEA/PUEB	3,840	1,920	64(0-63)	60	Pnet I/F Module
	G4/6L-DUEA	3,840	1,920	64(0-63)	60	Dnet I/F Module

4.2.2

) (0) SMART I/O
 1,2,3 , 4,5,6
 SMART I/O , ,
 가 32 ,
 32 가 , 0 63
 가 (.
 SMART I/O) ()
 SMART I/O
 . SMART I/O
 . GMWIN KGLWIN
 , , ,
 가 .



4.2.3



4.2.4 GMWIN

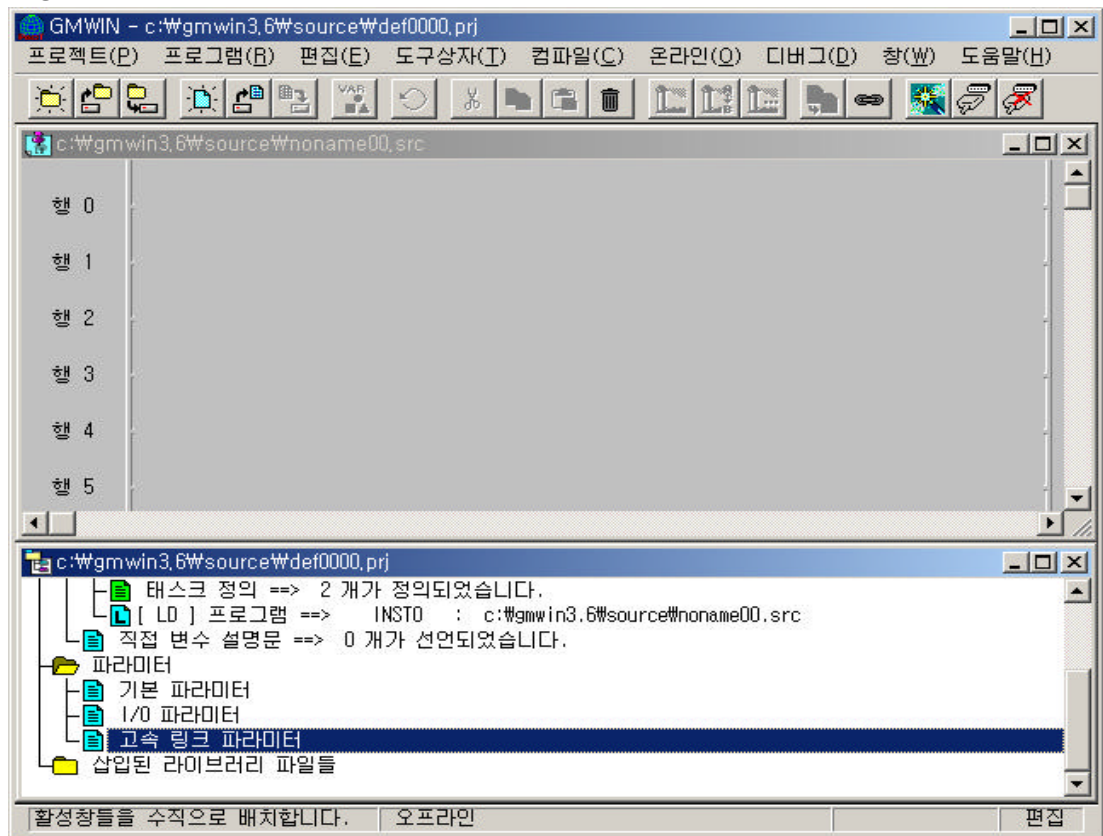
GMWIN

1)

GMWIN

가

GMWIN

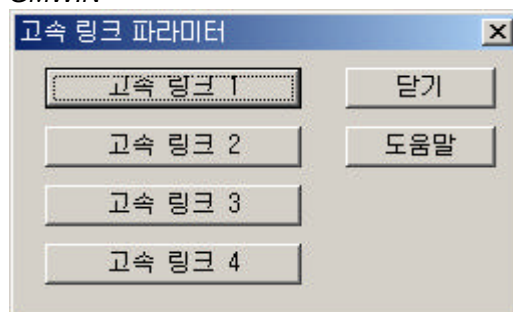


2)

(1)

:

GMWIN

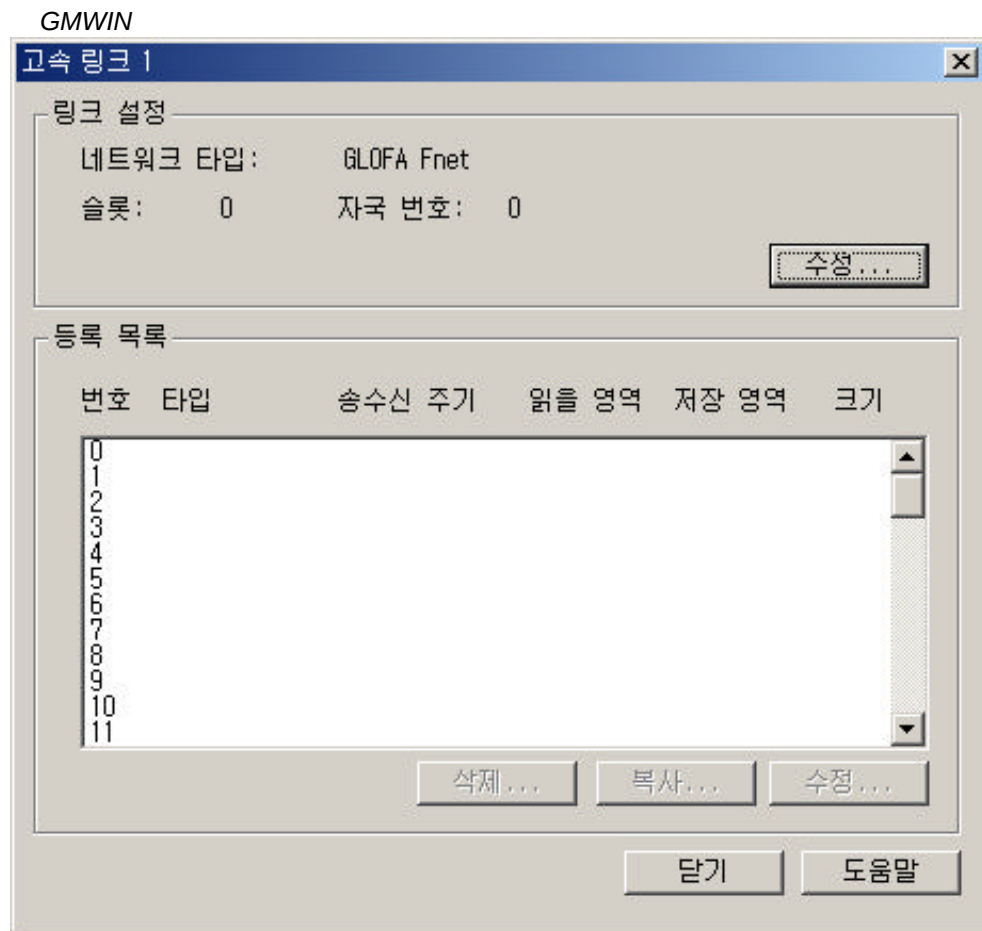


(2) : GMWIN 1~4 PLC CPU

가) 1 가

3)

가 ,



(1)

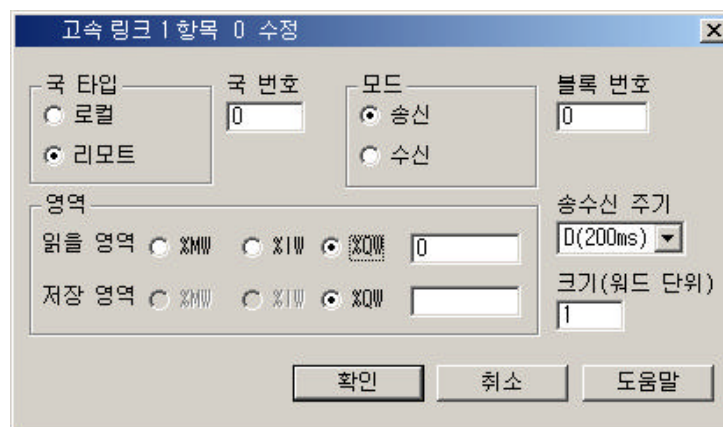


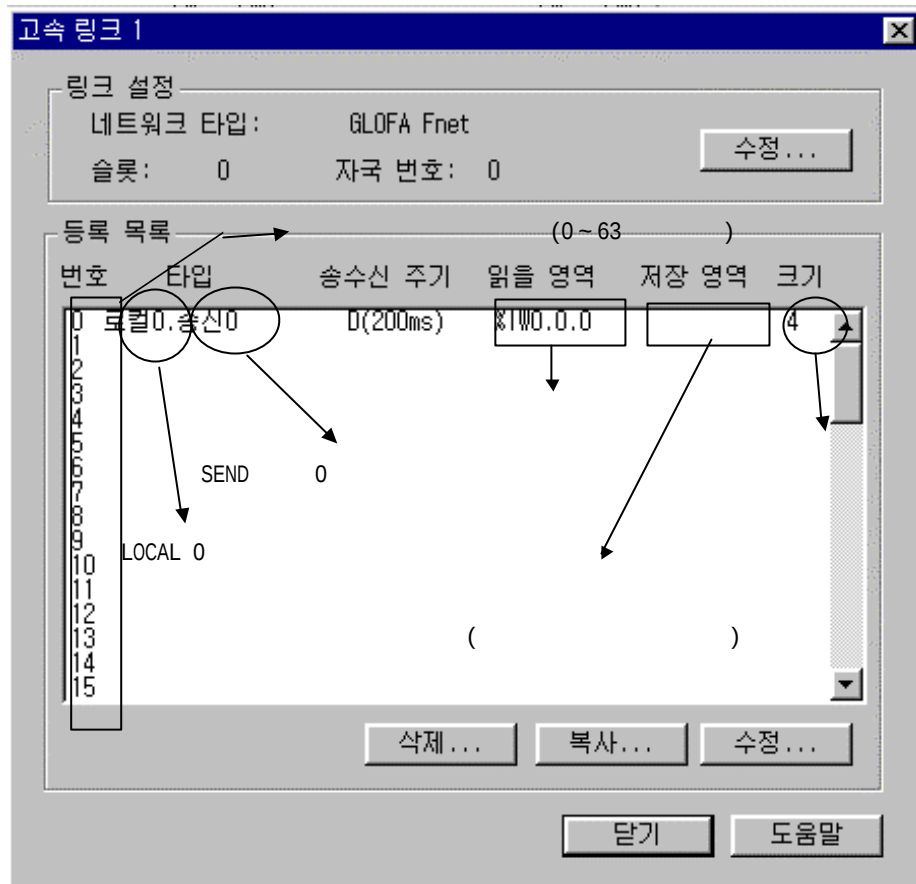
: , '0' '7' (CPU 0).

10 : 0 63 가 ,

(2)

'0' , ...





- : '0' '63' 64

,

- : . SMART I/O

- :

SMART I/O

- :

, 32

가

32

가

- :

32

가

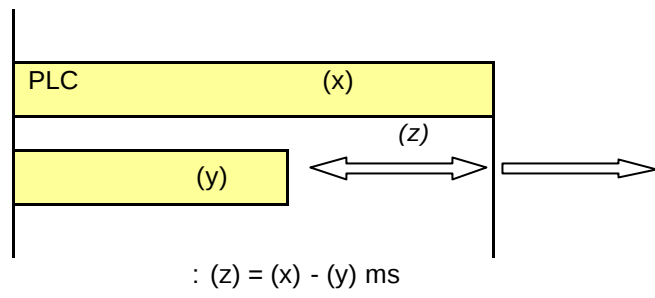
- : (%MW,%IW,%QW) (%QW) 가 .
- : (%IW) 가 .
- %MW,%IW,%QW . CPU
- 가 .

		%IW %QW %MW			%IW %QW %MW			
		○	○	○	○	X	X	
		X	○	X	○	○	○	

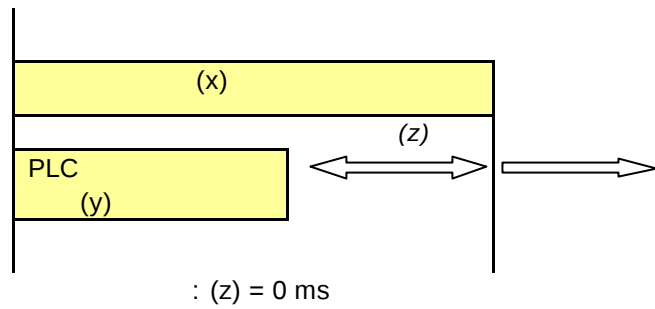
○: 가 X: 가

1) SMART I/O %IW0.0.0 ~ %IW0.0.1,
%QW0.0.0 ~ %QW0.0.1

- : 1 (16) .
- : 가 PLC PLC (Scan) ms , 가 가 20ms 10 가 200ms . PLC PLC PLC PLC (PLC >)



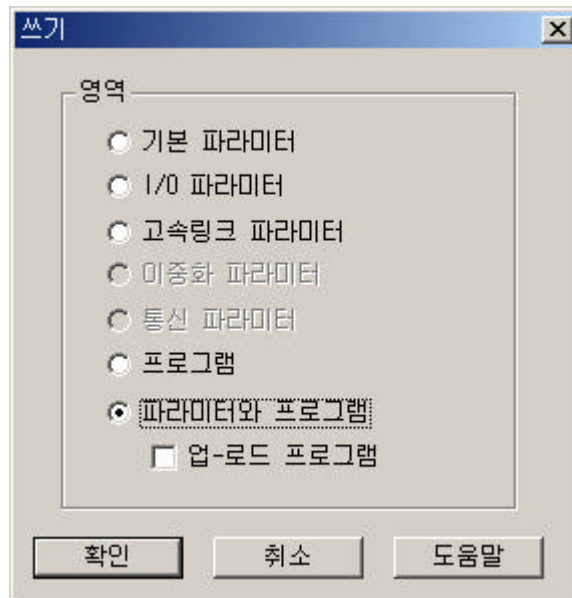
(PLC <)



1)

4)

1)



가

GMWIN

, GMWIN

PLC

(Link Enable)
'On'

1~4

5)



가

PLC
PLC CPU

가

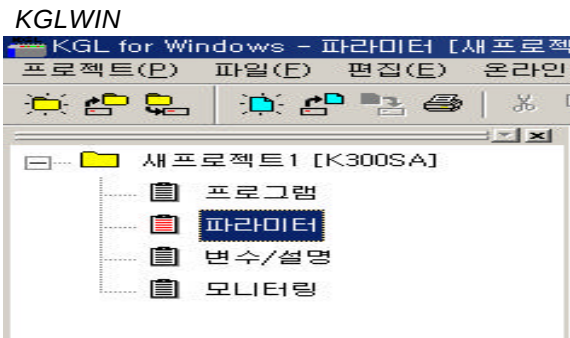
PLC

PLC RUN	X	X	○	
PLC STOP	○	○	○	
PLC PAUSE	X	X	○	
PLC DEBUG	X	X	○	

4.2.5 KGLWIN

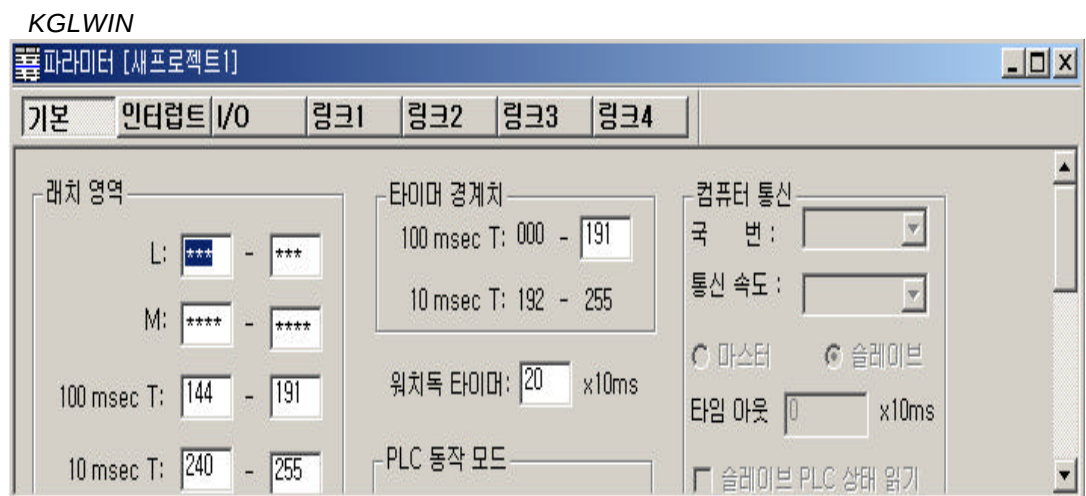
KGLWIN GMWIN 가 가
 . KGLWIN

1)



2)

(1) :

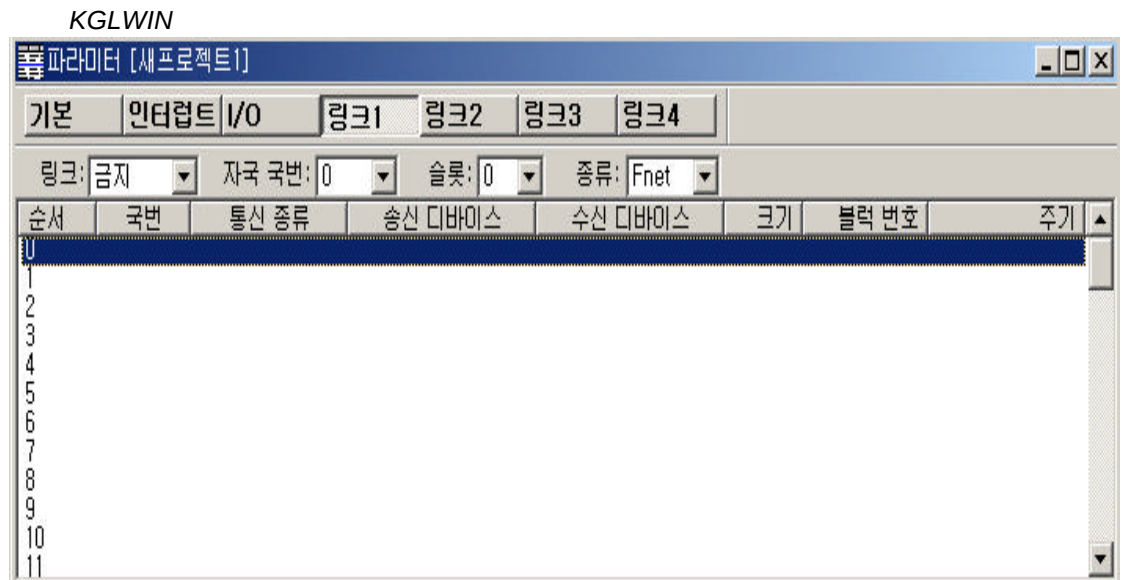


(3) : KGLWIN 1~4 PLC CPU

가) 1 가

3)

KGLWIN
GMWIN



(1)

: On
: Off

(2)

10 0 63 가 ,

(3)

' 0' ' 7'

(4)

SMART I/O

Rnet :
Pnet : Profibus-DP
Dnet : DeviceNet

4)

KGLWIN

(1) SMART I/O ()

(2)

(3)

가

:

: (SMART I/O)
: (SMART I/O)

(4)

/

		가	
		P,M,L,K,F,D,T,C	
		P	
		P	
		P,M,L,K,D,T,C	

(5)

(6)

가

PLC
PLC

ms

(Scan)

가

가

20ms

10

가

4

5)

KGLWIN

4.2.6

1)

64

	-	- LINK_ TROUBLE	TRX_MODE	DEV_MODE	DEV_ERROR	HS_STATE
(□= 1,2,3,4)	_HS□RLINK	_HS□LTRBL	_HS□TRX[n] (n= 0~63)	_HS□MOD[n] (n= 0~63)	_HS□ERR[n] (n= 0~63)	_HS□STATE[n] (n= 0~63)
	BIT	BIT	BIT-ARRAY	BIT-ARRAY	BIT-ARRAY	BIT-ARRAY
	가	가	가	가	가	가
	가	가	가	가	가	가

(1) - (_HS□RLINK)

가 가 가

, ‘ On’ ‘ Off’ ‘ On’

,

‘ On’ .

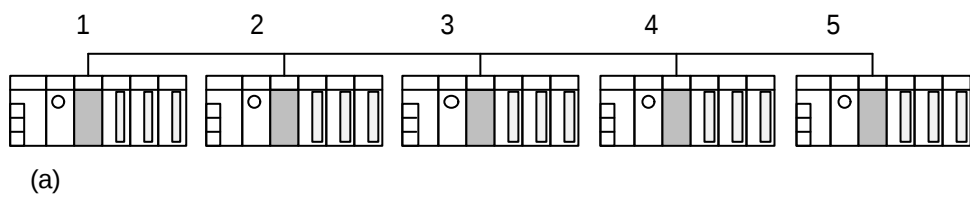
‘ On’ .

.

가

가 (RUN) 가

- On



1	2	3	4	5
:2	:2	:2	:2	:2
:2 (2)	:2 (1)	:2 (1)		
:2 (3)	:2 (4)	:2 (5)		

(b)

- 5 - 가 'On' .
- (a) , 1 - 가 'On' . (b)
- (1) (Link-Enable) 'On' ,
 (1) RUN ,
 (1) 가 ,
 (1) 가 ,
 2,3 가 ,
 (1) (2 ,3) 가 RUN ,
 가 ,
 (1) (2,3) (4,5)
 가 RUN 가 , .
- 7 1 - 'On' . PLC 가
 -
 - 'On' (Link-Enable) 'Off' 'On' .
- (2) - (_HS□LTRBL)
 - 가 'On' - 가 'On' 가
 'On' , 'Off' .
- (3) (_TRX□STATE[0..63])
 (0~63)
 가 'On' , 'Off' .
- (4) (_HS□MODE[0..63])
 (0~63)
 . RUN 가 'On' ,
 STOP/PAUSE/DEBUG 'Off' .
- (5) (_HS□ERR[0..63])
 (0~63)
 PLC 가
 'Off' PLC 가 ,
 'On'

(6) (_HS□STATE[0..63])

가 , (RUN) , 가
 ' On' ' Off' .

(1)~(6)

□: : (1,2,3,4) .

(1 1)

[0.63] : [6.2.2(E) .
 (0~63)

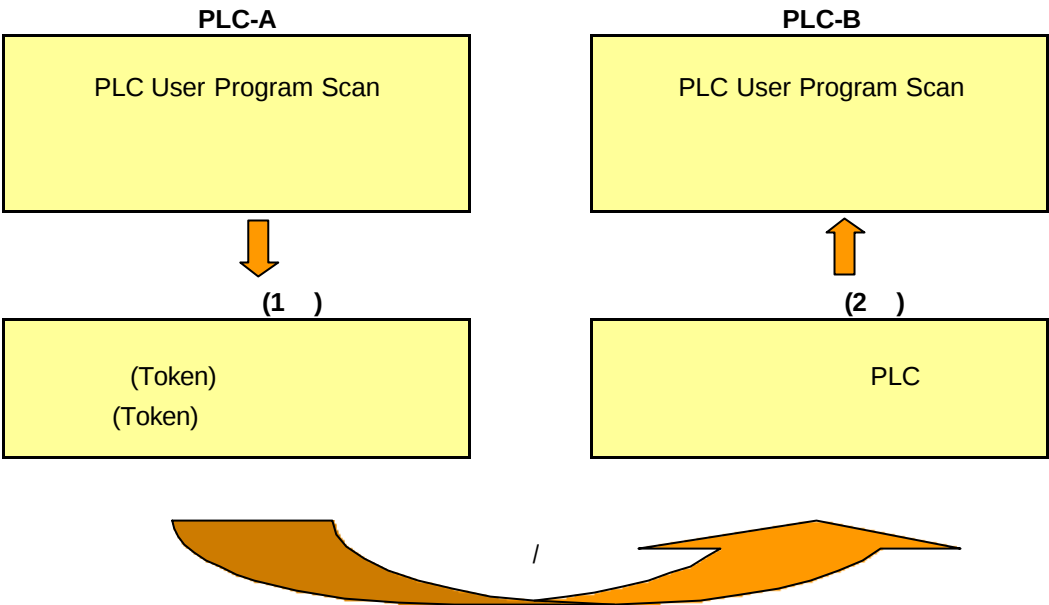
1) Profibus-DP

가 . ' 4.3.13

GMWIN , .

4.2.7

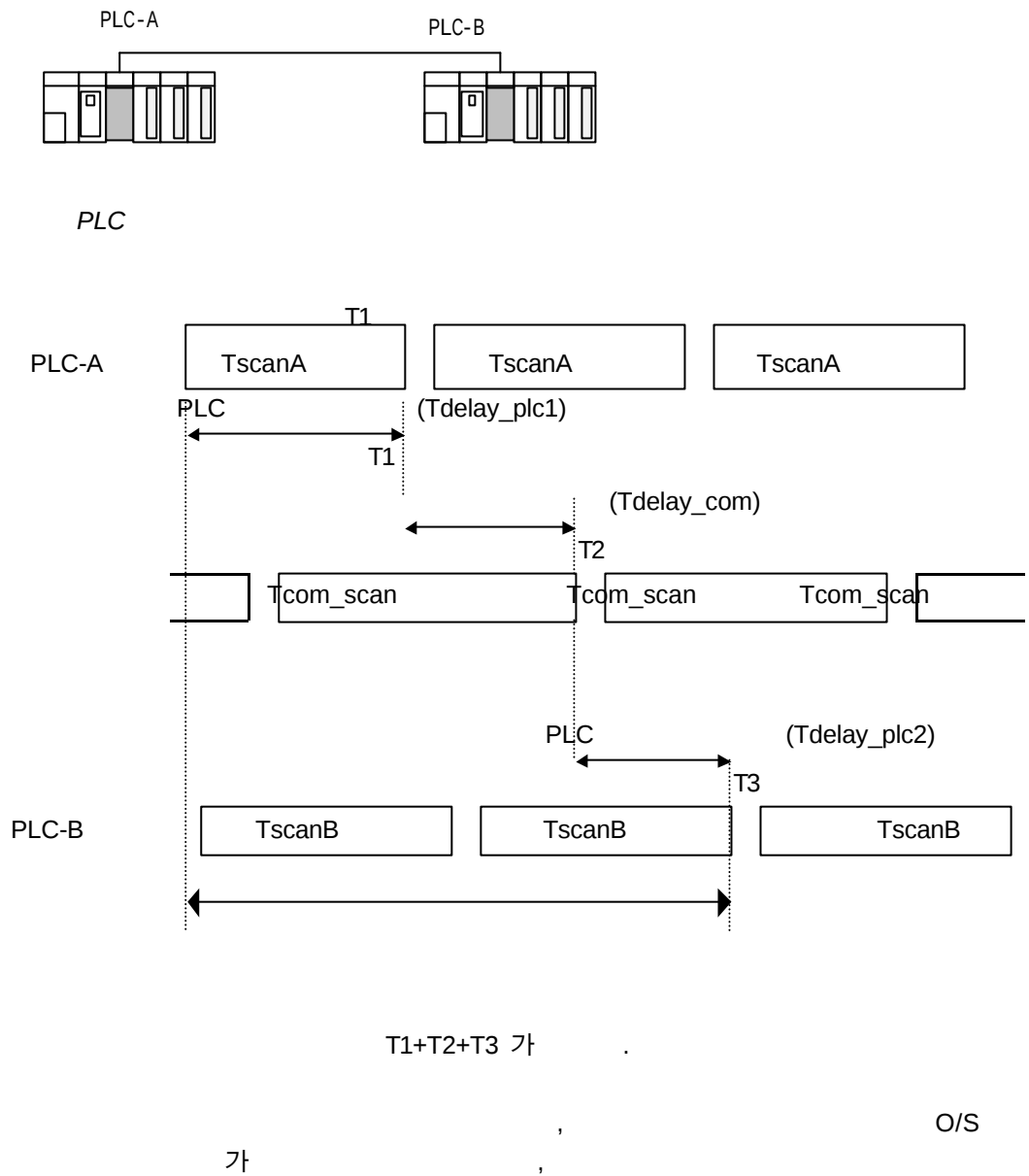
1)



3 가

(Path)	
PLC CPU(A) --> (1)	PLC-A
(1) --> (2)	+ O/S
(2) --> PLC CPU(B)	PLC-B

PLC CPU , PLC 가 .
(Token)
(Token)
PLC



2)

PLC-A

PLC-B

가

가 512 , 가 10 가

(1)

10 가 512

$$St = P_scanA + C_scan + P_scanB \text{ -----} [4-1]$$

St =

P_scanA = PLC A

C_scan =

P_scanB = PLC B

$$C_scan = THT \times Sn \text{ -----} [4-2]$$

THT = Token Hold Time : 1 (Token)

Sn = Total Station Number :

Token Hold Time (THT)= Fnet : 8 ms

(2)

10 가 512

$$St = Et \times To \times Ntx + Mf \text{ -----} [4-3]$$

Et = Effective Tx Ratio()

To = Octet time (1)

Ntx = Total Tx number

Mf = Margin Factor()

$$Et = St \times Nf \text{ -----} [4-4]$$

St = Total

Nf = Factor

$$\begin{aligned}
 & \text{Fnet} & 1.5 \\
 \text{To} &= \text{Octet Time} \\
 & \cdot \\
 & - \text{Fnet} : 8 \mu s \\
 \\
 \text{Ntx} &= & / \\
 & \text{Fnet} & \cdot \\
 & - \text{Fnet} : & + \text{Variable F/B}() \\
 & \times 256 \\
 \\
 \text{Mf} &= & \text{O/S} \\
 & & \text{Fnet} \cdot \\
 & - \text{Fnet} : 16 \text{ ms}
 \end{aligned}$$

4.3

4.3.1

SMART I/O

8

MODBUS

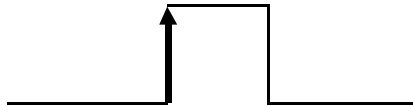
Snet

4.3.2 GMWIN

GLOFA GM7



REQ



'0'

'1'

, NDR

ERR

가

SLV_ADDR :**FUNC :**

8

MODBUS

ADDRH :**ADDRL :****NUMH:****NUML :**



NDR :

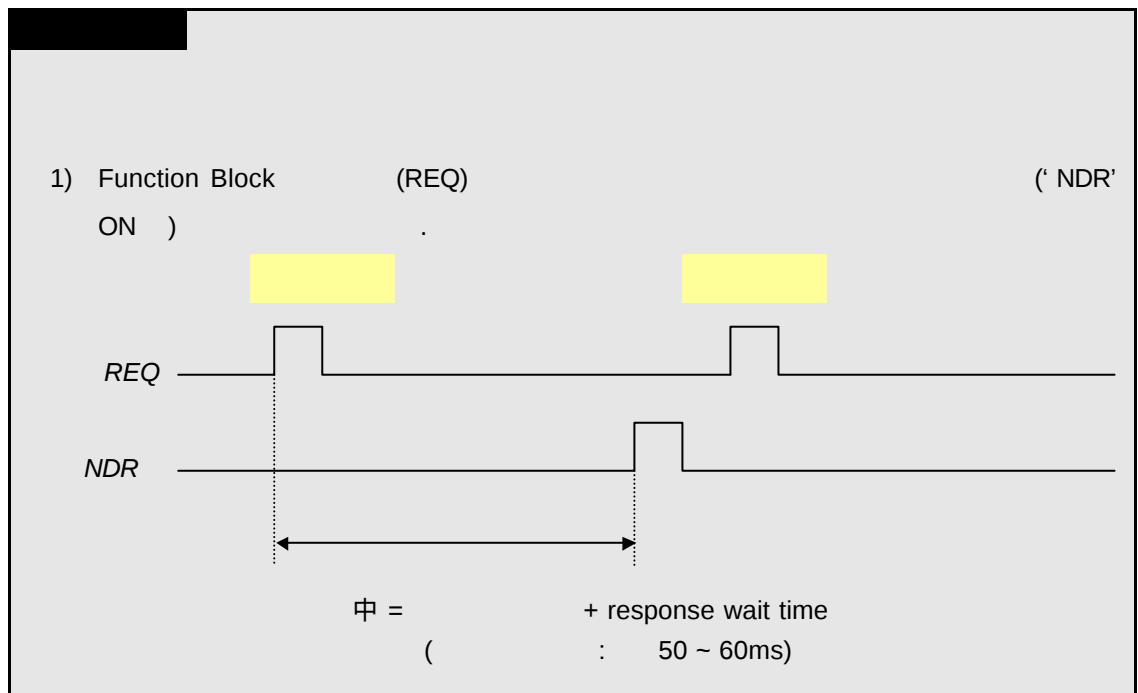
가 On ,
Off .

ERR :

On 가 Off ,
가 On ,
가

STATUS :

가
가 Off .



4.3.3 KGLWIN

MASTER-K

KGLWIN MODBUS

가
(' 8.4.2 MODBUS -MASTER-K)

5 PROFIBUS-DP

5 PROFIBUS-DP

5.1

Profibus 가 (Vendor-independence),
 DP 가
 Communication profile Field Level FA
 I/O 가
 가 DP 24V 4-20mA
 Hart 가
 SMART I/O GM3/4/6 Profibus-DP
 (G3L-PUEA/ G3L-PUEB/G4L-PUEA/ G4L-PUEB/G6L-PUEA/ G6L-PUEB)
 Profibus-DP
<http://www.profibus.co.kr>

5.2

	G3/4/6L-PUEA	G3/4/6L-PUEB
Module Type	Mater	
Network Type	Profibus-DP	
Standard	EN 50170 / DIN 19245	
Medium Access	Logical Token Ring	
Interface	RS-485(Electric)	
Topology	Bus	
Modulation	NRZ	
Cable	Shielded Twisted Pair	
Distance	1200 m (9.6k ~187kbps)	
	400 m (500kbps)	
	200 m (1.5 Mbps)	
	100 m (3M ~ 12Mbps)	
Max Node/Network	126 Station	
Max Node/Segment	32 Station	
Max I/O Data	1kbytes	7kbytes
	GM-WIN	SyCon-PB
Configuration Tool	SyCon-PB	
Configuration port	RS-232C Configuration Port	

5.3

5.3.1

Profibus-DP Master

가

-
- GMWIN/KGLWIN Configuration Tool(Tool :
- SyCon) ..
- GMWIN/KGLWIN
- (MASTER-K
- MAP .)
- SyCon
- Configuration Port
- Daughter board 512bytes/3,584bytes
- 가
- byte (SyCon)
- GMWIN/KGLWIN

5.3.2

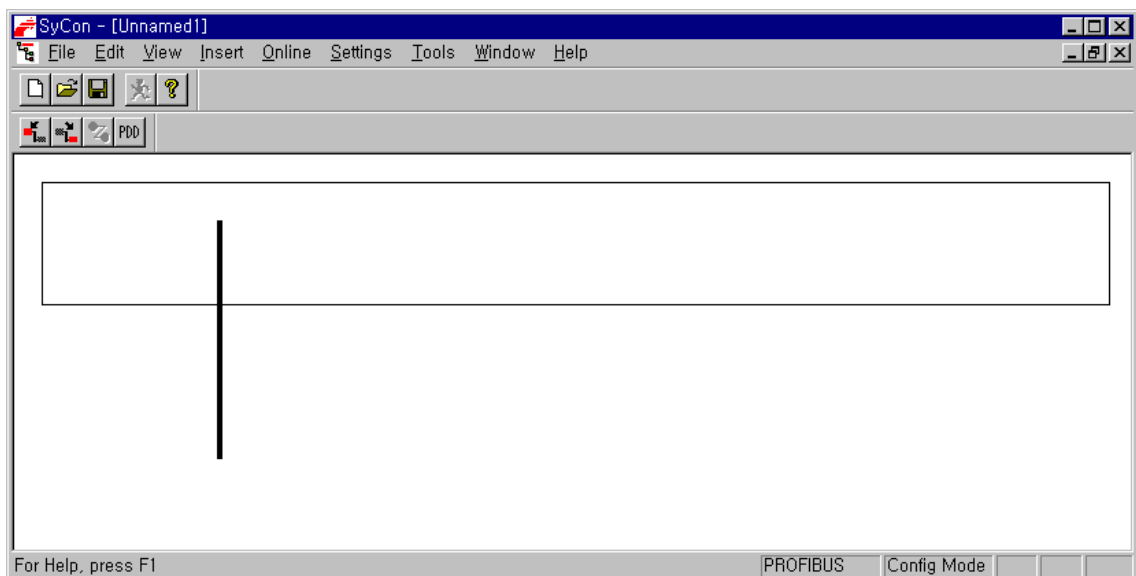
1. Master LG (G3/4/6L-PUEA, G3/4/6L-PUEB) SyCon
Profibus Network Configuration
2. Profibus Network Configuration
3. GMWIN Master
- 4.
5. Master Configuration Tool
Profibus Network Configuration

5.3.3 SyCon

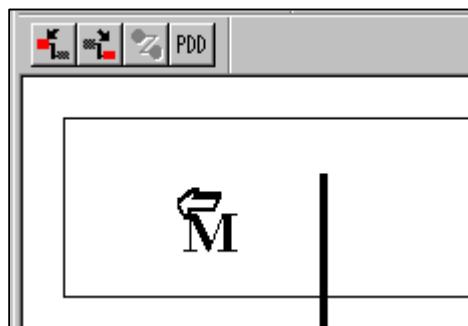
LG (G3/4/6L-PUEA, G3/4/6L-PUEB) SyCon
 Profibus Network Configuration ,
 . Profibus Network Configuration Tool LG
 (G3/4/6L-PUEA, G3/4/6L-PUEB) SyCon
 .



SyCon 가
 가 .



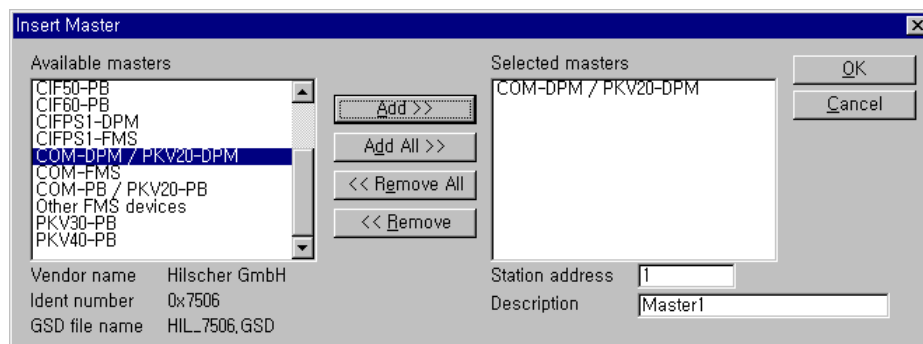
5.3.4

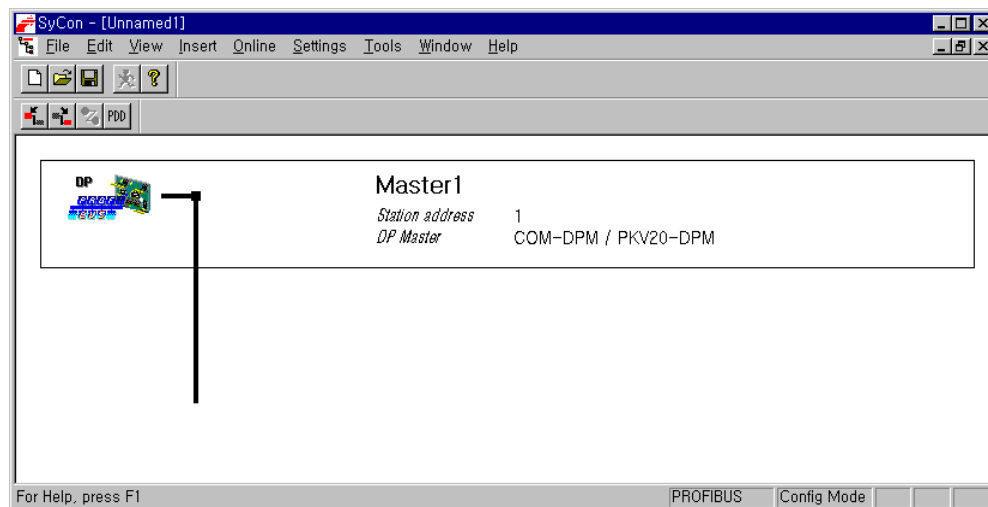


(Insert Master) 가

G3/4/6L-PUEA	COM-DPM/PKV20-DPM	, Add
	G3/4/6L-PUEB	COM-PB/PKV20-PB
, Add	(Station address)	,
(Description)	OK	

Master

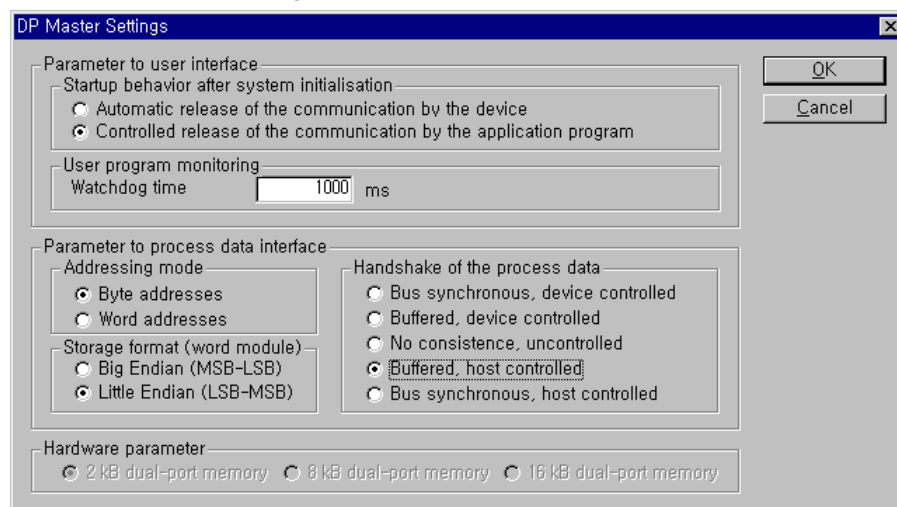




5.3.5

“Master Settings...”
 “Parameter to user interface”
 “Controlled release of the communication by the application program”
 “Storage format(word module)”
 “Little Endian(LSB-MSB)”
 “Handshake of the process data”
 “Buffered, host controlled”

Setting



5.3.6



(Insert Slave)

가

Slave

GPL-TR2A

“Available slaves”

“GPL-TR2A”

“Add”

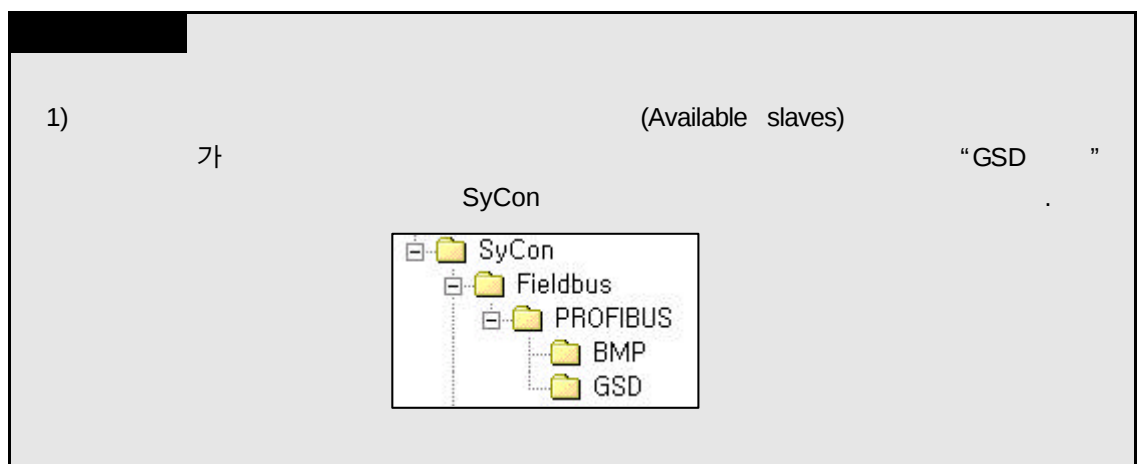
가

“Master”

, “Station address”

“Description”

“OK”



5.3.7 Slave Configuration

“Slave configuration”
.)

S

Slave Configuration

General

Device: GLOFA GM7 Station address: 2

Description: Slave2

☒ Activate device in actual configuration

☒ Enable watchdog control GSD file: LG_GM7.GSD

Max, length of in-/output data: 368 Byte Length of in-/output data: 6 Byte

Max, length of input data: 244 Byte Length of input data: 2 Byte

Max, length of output data: 244 Byte Length of output data: 4 Byte

Max, number of modules: 2 Number of modules: 2

Module	Inputs	Outputs	In/Out	Identifier
1 byte output (0x20)		1 Byte		0x20
2 byte output (0x21)		2 Byte		0x21
3 byte output (0x22)		3 Byte		0x22
4 byte output (0x23)		4 Byte		0x23
8 byte output (0x27)		8 Byte		0x27
10 byte output (0x29)		10 Byte		0x29

Slot	Idx	Module	Symbol	Type	I Addr.	I Len.	Type	O Addr.	O Len.
1	1	2 byte input (0x11)	Module1	IB	0	2			
2	1	4 byte output (0x23)	Module2				QB	0	4

Assigned master: Station address 1, Master1, 1 / COM-DPM / PKV20-DPI

Actual slave: Station address 2, Slave2, 2 / GLOFA GM7

Buttons: OK, Cancel, Parameter Data..., DPV1 Settings, Append Module, Remove Module, Insert Module, Predefined Modules, Symbolic Names

가

가

“Append Module”

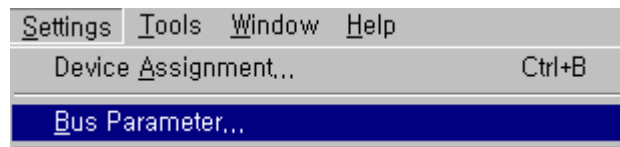
Input

Output

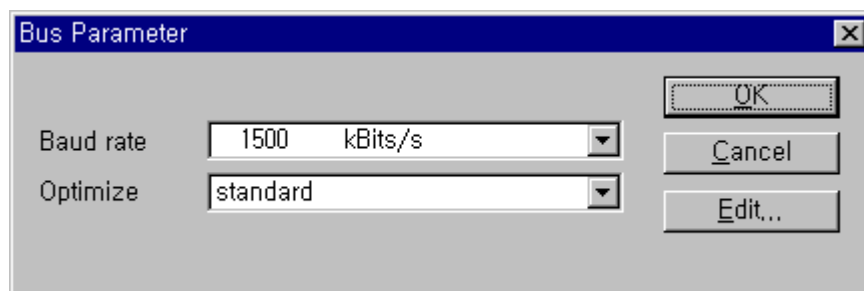
가

2

5.3.8



Profibus-DP "Settings/Bus
Parameter..." . Optimize " (Standard)" "
, (Baud rate) 9.6kbps ~ 12Mbps .
Baud rate 1.5Mbps, Optimize standard

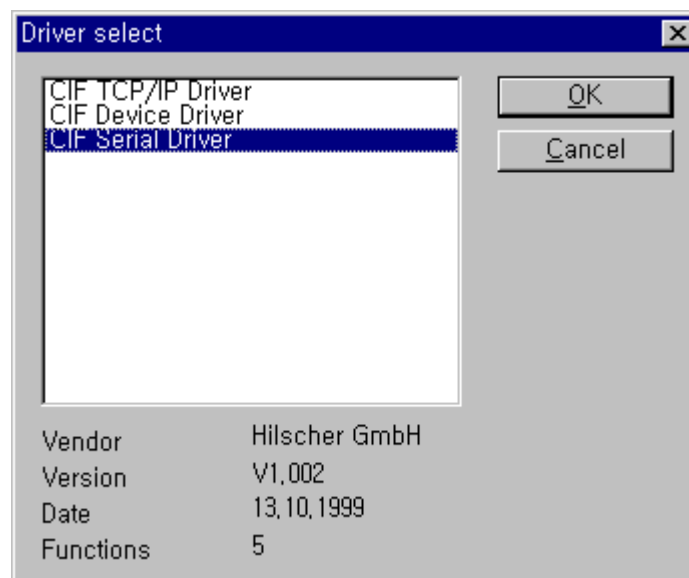
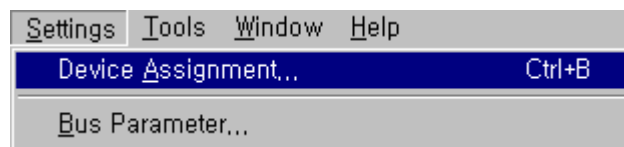


- 1) 가 .
- 2) 12Mbps .12Mbps .
- 3) 12Mbps 1m .
- 4) 12Mbps ()

5.3.9

Configuration

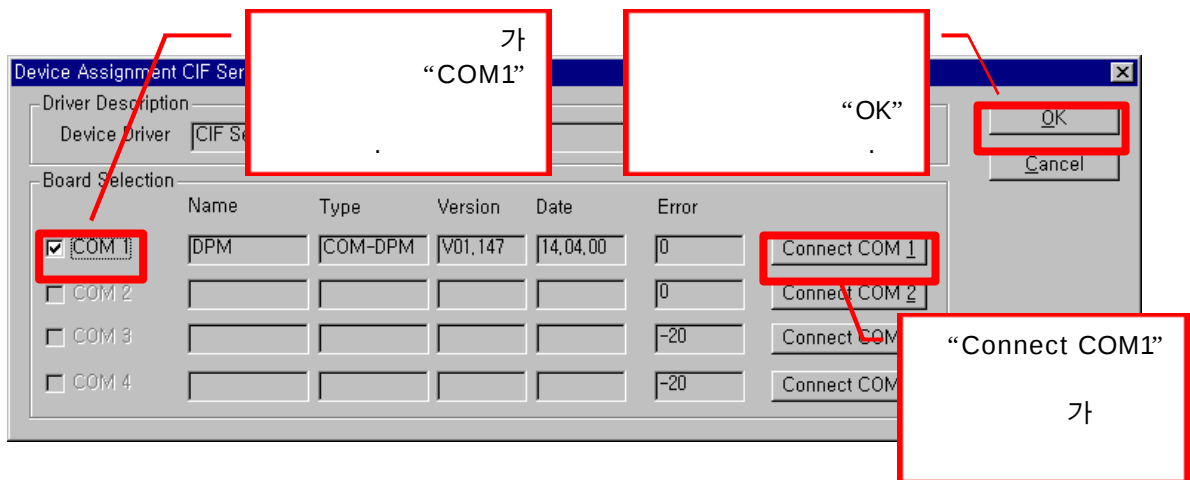
“Setting/Device Assignment...”



가 “CIF Serial Driver”

1) G3/4/6-PUEA/B	RS-232C
“CIF TCP/IP Driver”, “CIF Device Driver”	

CIF Serial Driver



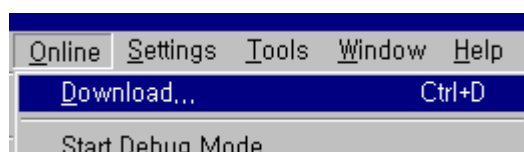
PC Profibus-DP Configuration Port ,
가 . PC PC “Connect
COM1”
“version” “Date”
“OK”

[illegible]

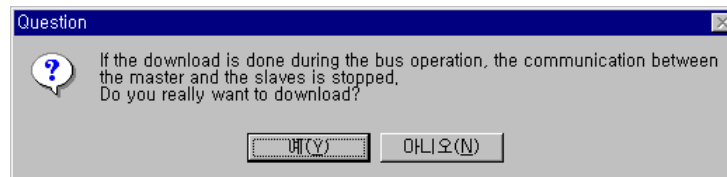
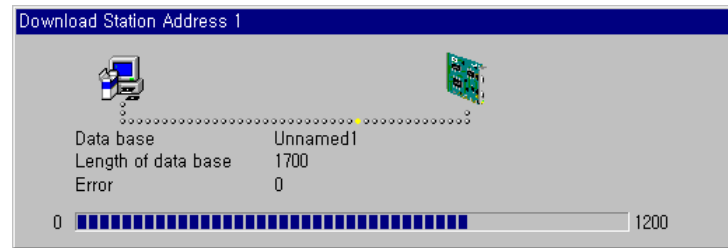
5.3.10 Configuration

“Online/Download”
“READY” LED
가
LED
“
”
가
“ (Y)”
가

Configuration



Download



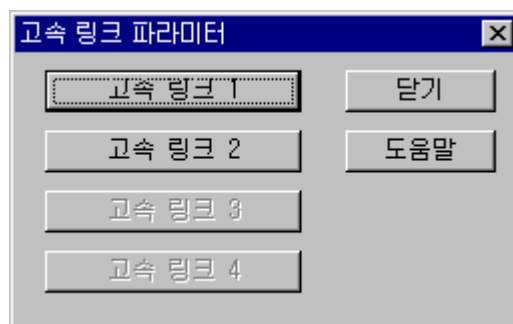
5.3.11 GMWIN

Configuration

. Configuration

GMWIN

1) GMWIN



PLC CPU

가

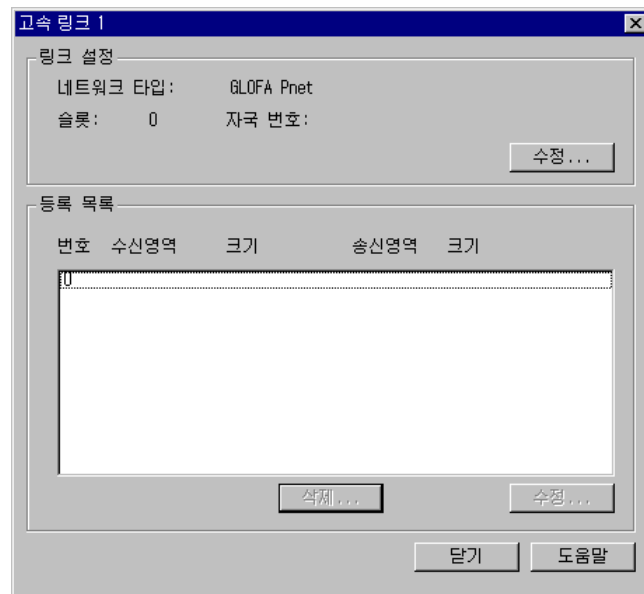
가

가

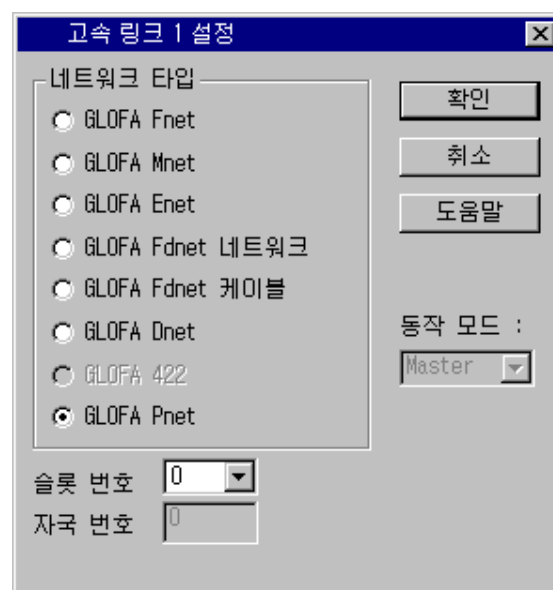
GLOFA-GM CPU

	가		
GM3-CPUA	G3L-PUEA, G3L-PUEB	4	가
GM4-CPUA/B	G4L-PUEA, G4L-PUEB	2 (A)/4 (B)	
GM6-CPUA/B/C	G6L-PUEA, G6L-PUEB	2	

2)



가)



- : , GLOFA Pnet
- : (0 ~ 7).
- : SyCon ,

)

'0

)

가

(

영역		주소	크기(바이트)
수신 영역	☒ %MW ☐ %QW	100	0
송신 영역	☒ %MW ☐ %IW ☐ %QW	200	32

확인 취소 도움말

()

링크 설정

네트워크 타입: GLOFA Pnet

슬롯: 0 자국 번호: 수정...

등록 목록

번호	수신영역	크기	송신영역	크기
0	%MW100	64	%MW200	32

삭제... 수정... 닫기 도움말

- : ,
- : 1 (byte)
G3/4/6L-PUEA 1kbytes , G3/4/6L-PUEB 7kbytes

- 1) SyCon
- 2) G4L-PUEA 1 GPL-TR2A(16), GPL-TR4A(32), GPL-D22A(16)
%MW0, %MW100
 - : %MW0
 - : %MW100
 - : 6 bytes()
 - : 2 bytes()
 - %MW0 -> GPL-TR2A
 - %MW1 ~ %MW2 -> GPL-TR4A
 - GPL-D22A -> %MW100
- 3) SyCon

5.3.12 GMWIN

1)

, 가

(_PHSxRLINK), - (_PHSxLTRBL)
_PHSxSTATE 가

PLC

가) - (_PHSxRLINK)

가

가

가

'On'

'Off'

'On'

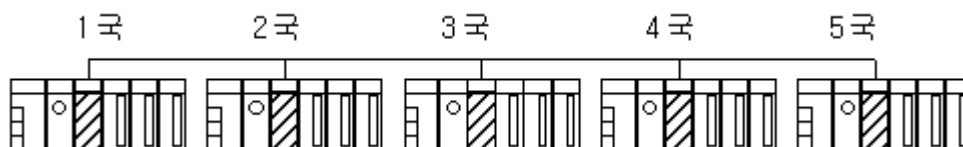
'On'

'On'

가

가 (RUN)

가



1	2	3	4	5
:2	:2	:2		
:2 (2)	:2 (1)	:2 (1)	:2	:2
:2 (3)	:2 (4)	:2 (5)		

- 가 'On'

. 5

1 - 가 'On'

(1) (Link-Enable) 'On' ,

(1) RUN ,

(1) 가 ,

(1) 가 ,

2,3 가 ,

(1) (2 ,3) 가

RUN , 가 ,

(1) (2,3) (4,5)

가 RUN 가 ,

7 1 - 'On' PLC 가

-

'On' (Link-Enable) 'Off'

'On'

) - (_PHSxLTRBL x= (1~2))

가 가

- 가 On - 가 On

가 On , Off .

) (_PHSxSTATE[0..127] x= (0~127))

127 가 Run , 가 On

Off .

2)

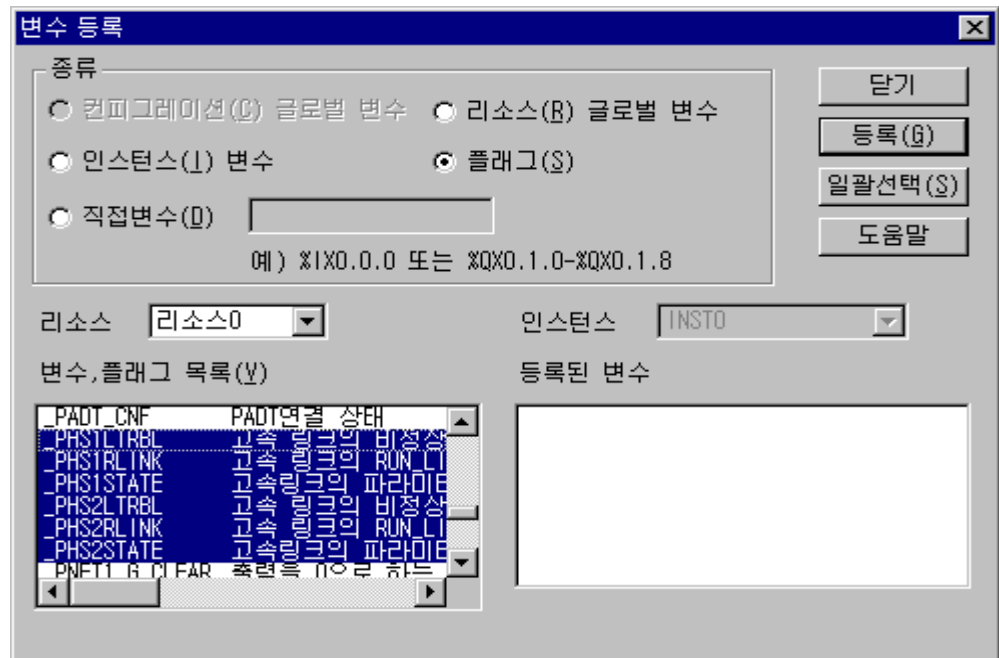
GMWIN ,

가

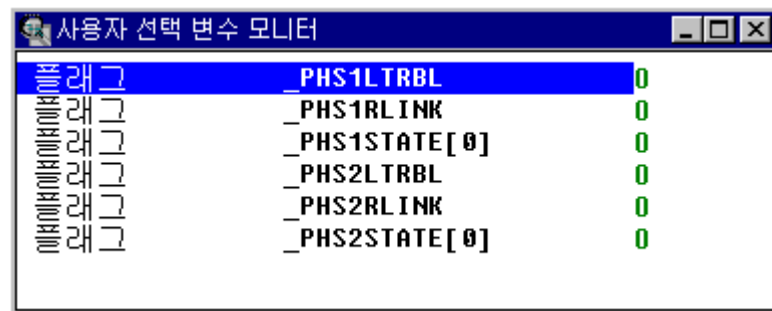
가)

GMWIN

_PHSxSTATE[n] Array 가
, 'X'
GM3 PLC CPU 1~4 , GM4 PLC CPU
1~2 , GM6 PLC CPU 1
Start

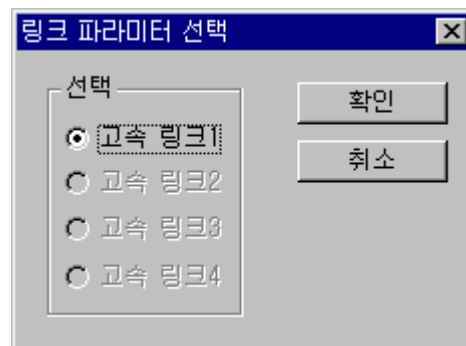


()



)

GMWIN



가

(), (),

고속 링크 파라미터 모니터								
런 링크: 0			링크 트러블: 0					
번호	타입	송수신주기	읽을영역	저장영역	크기	모드	통신	에러
0	%MW100	64	%MW200	32		0	0	0
1						0	0	0
2						0	0	0
3						0	0	0

가

가 ,

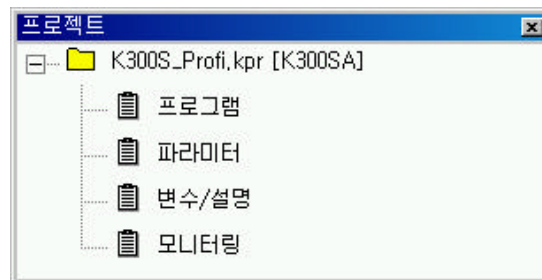
5.3.13 KGLWIN

MASTER-K Profibus-DP Configuration SyCon
 GLOFA-GM MASTER-K Configuration
 KGLWIN

1) KGLWIN

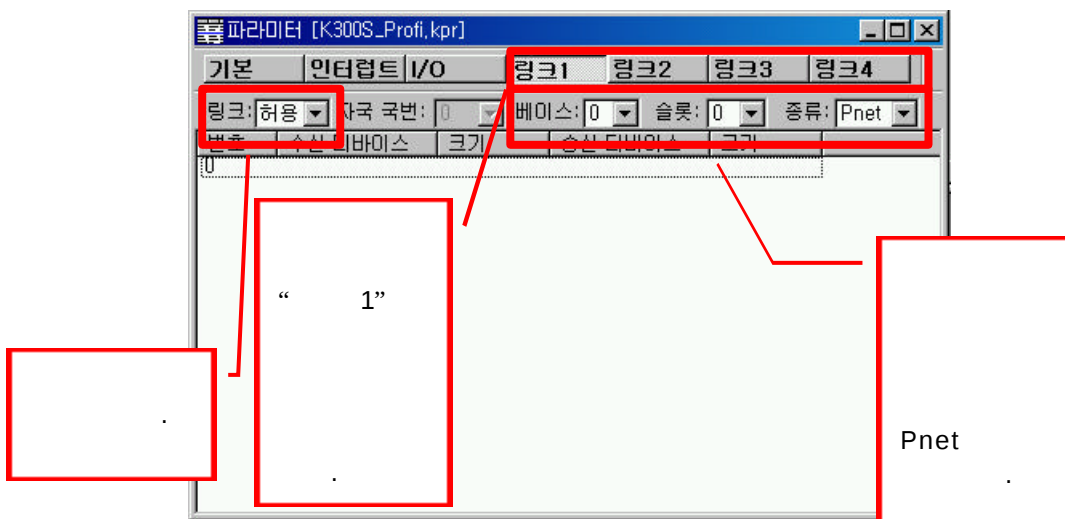
가

KGLWIN



2)

가)



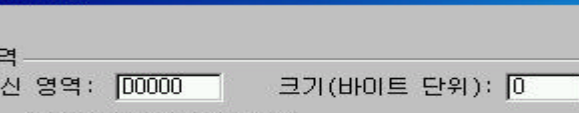
- MASTER-K CPU

	가		
K1000S CPU	G3L-PUEA, G3L-PUEB	4	
K300S CPU	G4L-PUEA, G4L-PUEB	2 /4 (Version 3.0)	

가 .

-
- The diagram illustrates a hierarchical or sequential structure with five levels. Each level is represented by a horizontal row of nodes, connected vertically from the level above.
- Level 1:** A single node labeled "SyCon".
 - Level 2:** Two nodes; the left one is labeled "Pnet" and the right one is labeled "(0 ~ 7)".
 - Level 3:** Three nodes; the leftmost is labeled "Pnet", and the next two are both labeled "(0 ~ 7)".
 - Level 4:** Four nodes; the leftmost is labeled "Pnet", and the remaining three are all labeled "(0 ~ 7)".
 - Level 5:** Five nodes; the leftmost is labeled "Pnet", and the remaining four are all labeled "(0 ~ 7)".

3)



파라미터 수정

영역

수신 영역: 00000 크기(바이트 단위): 0

(P,M,L,K,T,C,D,S 접점)

송신 영역: 00000 크기(바이트 단위): 0

(P,M,L,K,T,C,D,S 접점)

확인 취소 도움말

5 PROFIBUS-DP

- : ,
- : 1 (byte)
G3/4/6L-PUEA 1kbytes , G3/4/6L-PUEB 7kbytes

- 1) SyCon
- 2) G4L-PUEA 1 GPL-TR2A(16), GPL-TR4A(32), GPL-D22A(16)
P000, P010
 - : P000
 - : P010
 - : 6 bytes()
 - : 2 bytes()

,

 - P000 -> GPL-TR2A
 - P001~P002 -> GPL-TR4A
 - GPL-D22A -> P010
- 3) SyCon

6.1

- 가 가 .
- 1 63 ,
2,048 I/O 가 가 .
- T 가 .
- 가 .
- 가 .
- (MAC Address) 가 .(0 ~ 63).
- GLOFA-GM4 GM6 2 가 .
- 가 .
- I/O 가 .

6-1

6.2

1)

		125/250/500kbps
	(Thick) ^[1]	500/250/100m
	125 kbps	6m(156m)
	250 kbps	6m(78m)
	500 kbps	6m(39m)
		0~8 Byte
		• / • /
	[2]	• / • Peer-to-Peer • Poll, Strobe, COS/Cyclic
		64 MAC ID/MAC Identifier 32 I/O(2,048 I/O)
		On 가
		DC 24V
		Check/ /CRC Check

1) Smart I/O

, Thin

100m

2)

6.3

DeviceNet
가 (Scanlist)
가
(Scanlist) , Dnet I/F (Scanlist)
CPU
SMART I/O DeviceNet

GMWIN
1 256 (2,048) 가
5ms 10 가

SMART I/O	GDL-TR2A	16	64 (0-63)	
	GDL-TR4A	32	64 (0-63)	
	GDL-RY2A	16	64 (0-63)	
	GDL-DT4A	32	64 (0-63)	
	GDL-D22A	16	64 (0-63)	
	GDL-D24A	32	64 (0-63)	

* 1

1) DeviceNet

6 DEVICENET

6.3.1

GLOFA SMART I/O Dnet

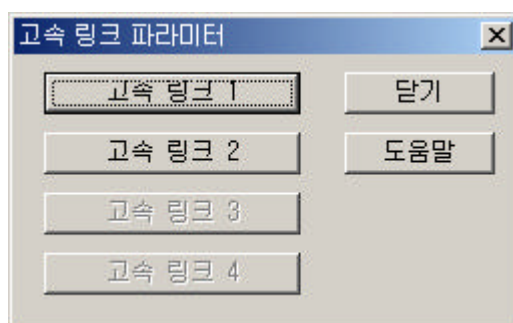
GMWIN 가

GLOFA-GM		GDL-TR2A	TR 16	Poll
		GDL-TR4A	TR 32	
		GDL-DT2A	DC/TR 16	Poll
		GDL-D22A	DC 16	Poll
		GDL-D24A	DC 32	
		GDL-RY2A	16	Poll
	OMRON	DRT1-OD08	TR 8	Poll
	A.B	1794-OB16	TR 16	Poll
		1794-OB16	DC 24V 16	

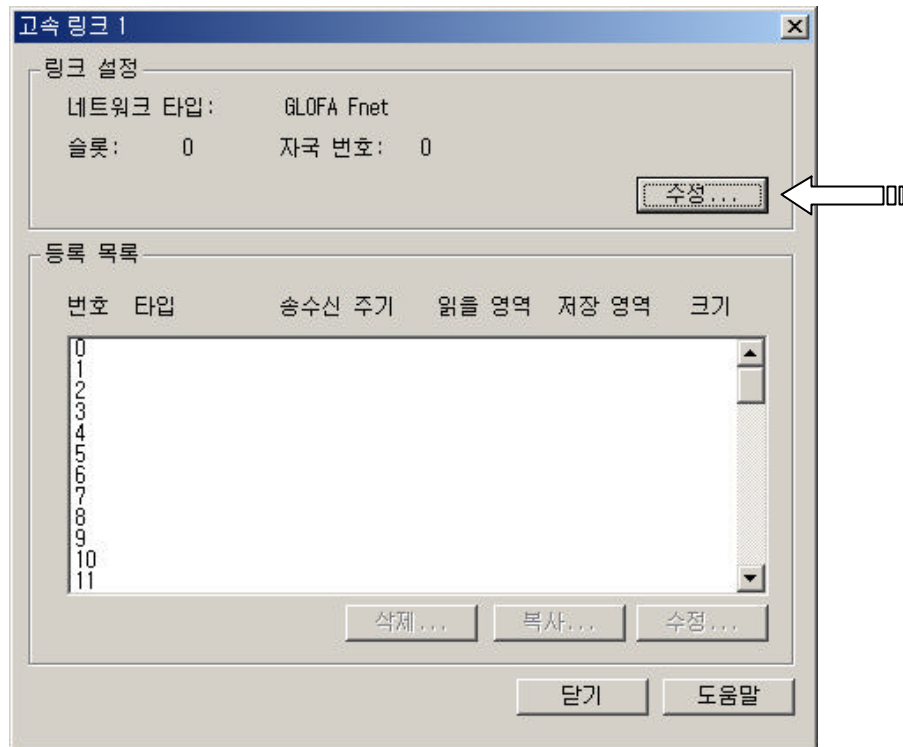
(ScanList) On Dnet
I/F 가

Dnet (ScanList)

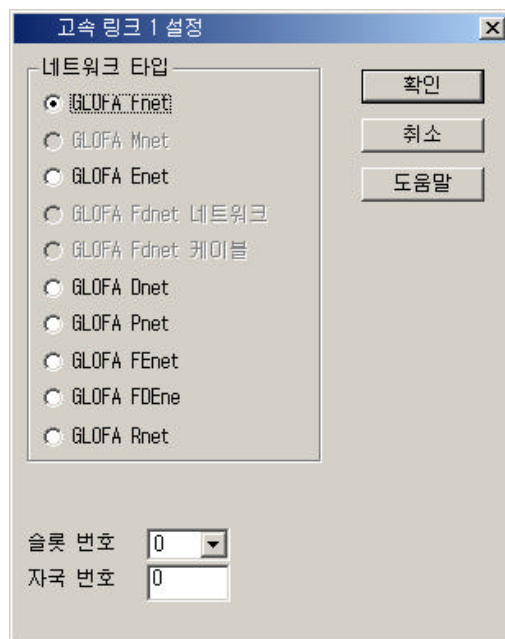
GMWIN CPU 1'



1 ' < Dnet I/F



1



[5.3] , Delay , 0
[5.3]
1~63 가

(GDL-TR2A)

고속 링크 1 항목 1 수정

모드

☒ 리모트 송신

☐ 리모트 수신

국 번호

1

통신 모드

☒ Poll

☐ Strobe

☐ COS

☐ Cyclic

영역

PLC 영역

☒ %MW

☐ %IW

☐ %QW

0

송수신 주기

50

크기(바이트수)

2

확인

취소

도움말

, Delay , 0
1~63 가

I/F , () , Poll, I/F
2

	' GLOFA Dnet'
	0~7 (CPU 0)
	10 0~63 가 ,

6 DEVICENET

	Dnet I/F (msec)
	Dnet I/F 2 Dnet I/F 2 Poll

GLOFA Dnet (GDL-TR2A/RV2A)

()

		SMART I/O
		SMART I/O
		I/F
	Poll	Poll
	Strobe	Strobe
	COS	COS
	Cyclic	Cyclic
		SMART I/O
		SMART I/O (%IW Disable)
(msec)		
()		2 가 ,

SMART I/O DeviceNet

(GDL-D22A)

Poll 1, 2

번호	스캔 타입	송신 주기 (msec)	PLC 영역	크기 (바이트 수)
0	L0 X SC			
1	R1 S PL	5	%MW0	2
2	R2 R PL	5	%MW100	2
3				
4				
5				
6				
7				
8				
9				
10				
11				

1) I/F GLOFA-GM

↑ DRT1-OD08 : 1

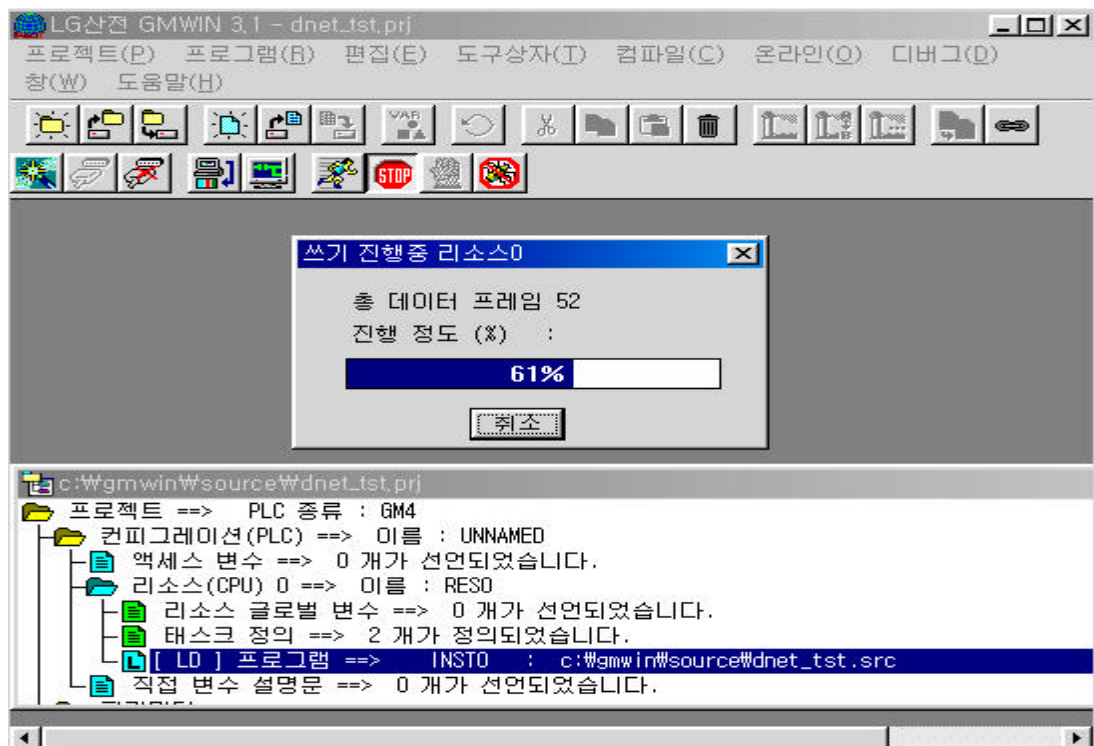
— 1794-OB16/IB16 : 4

Poll

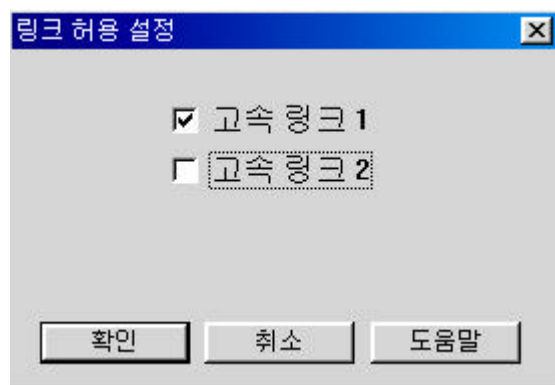
		PLC		
R1.S PL	5×1 = 5msec	%MW0	2	%MW0 2 1 Poll Request 5msec
R2.R PL	-	%MW100	2	1 Poll Response 2 %MW100

*

×



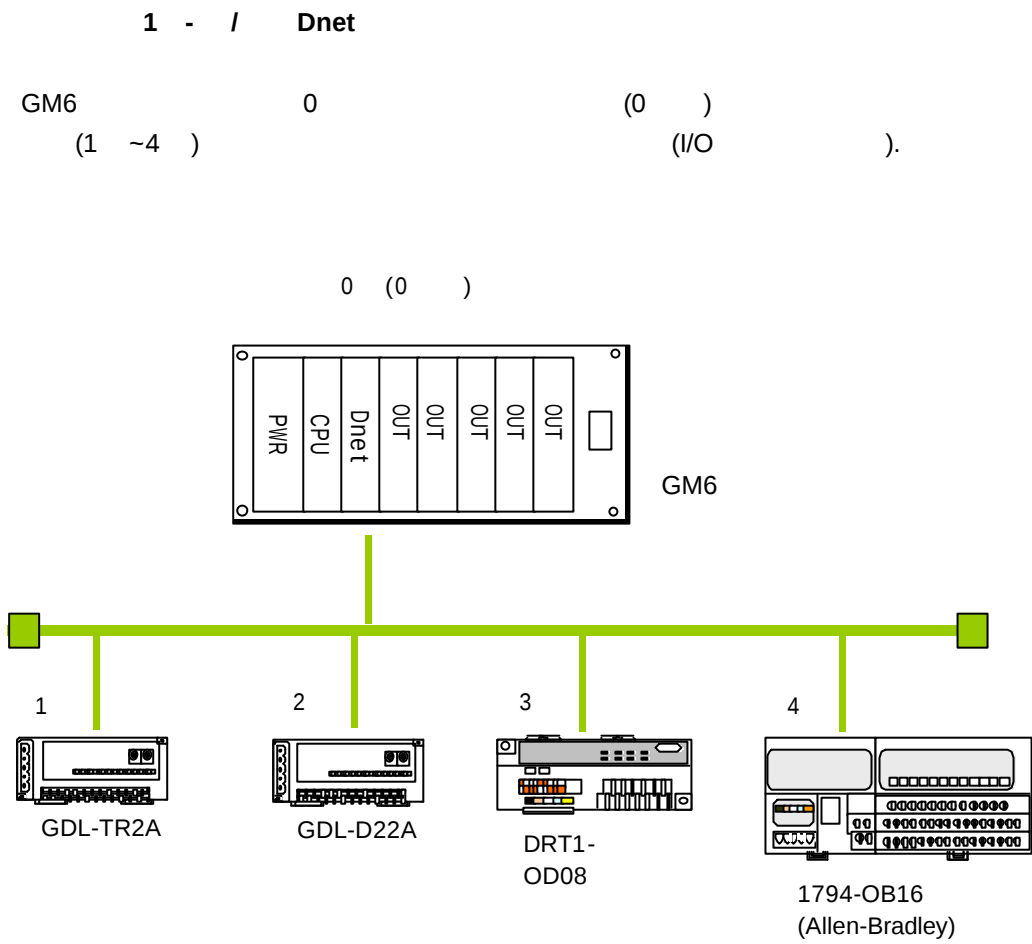
가 Dnet Smart I/O
GMWIN



가 CPU
가 가

6.4

6.4.1 GLOFA-GM



I/O

				()
GM6 (0) ()	:GDL-TR2A(1)	%MW0	-	2
	:GDL-D22A(2)	-	%QW0.1.0	2
	:DRT1-OD08(3)	%MW100	-	1
	:1794-OB16(4)	%MW200	-	4

1) GM6(0)

고속 링크 1 설정

네트워크 타입

- ☐ GLOFA Fnet
- ☐ GLOFA Mnet
- ☐ GLOFA Enet
- ☐ GLOFA Fdnet 네트워크
- ☐ GLOFA Fdnet 케이블
- ☒ GLOFA Onet
- ☐ GLOFA Pnet
- ☐ GLOFA FEnet
- ☐ GLOFA FDEne
- ☐ GLOFA Rnet

슬롯 번호 스캔 타임 msec

자국 번호 플레이트

확인 취소 도움말

1 (GDL-TR2A)

고속 링크 1 항목 1 수정

모드

- ☒ 리모트 송신
- ☐ 리모트 수신

국 번호

통신 모드

- ☒ Poll
- ☐ Strobe
- ☐ COS
- ☐ Cyclic

영역

PLC 영역 ☒ ☐ %IW ☐ %QW

송수신 주기

크기(바이트수)

확인 취소 도움말

I/F

GDL-TR2A
(2)

2 (GDL-D22A)

고속 링크 1 항목 2 수정

모드 ☐ 리모트 송신 ☒ 리모트 수신	국 번호 2	통신 모드 ☒ Poll ☐ Strobe ☐ COS ☐ Cyclic
영역 PLC 영역 ☐ %MW ☐ %IW ☒ %QW	0.1.0	송수신 크기 (바이트수) 50 2 ()

확인 취소 도움말

Callouts: GDL-D22A (Country Number), GDL-D22A (Mode), GDL-D22A (Area), GDL-D22A (Size)

3 (DRT1-OD08)

고속 링크 1 항목 3 수정

모드 ☒ 리모트 송신 ☐ 리모트 수신	국 번호 3	통신 모드 ☒ Poll ☐ Strobe ☐ COS ☐ Cyclic
영역 PLC 영역 ☒ %MW ☐ %IW ☐ %QW	100	송수신 주기 (1) 크기 (바이트수) 1

확인 취소 도움말

Callouts: DRT1-OD08 (Mode), DRT1-OD08 (Country Number), DRT1-OD08 (Period)

4 (1794-OB16)

고속 링크 1 항목 4 수정

모드: ☒ 리모트 송신 ☐ 리모트 수신

국 번호: 4

통신 모드: ☒ Poll ☐ Strobe ☐ COS ☐ Cyclic

영역: PLC 영역 ☒ %MW ☐ %IW ☐ %QW 200

크기 (바이트수): 4

1794-OB16 (4)

확인 취소 도움말

1'

고속 링크 1

링크 설정

네트워크 타입: GLOFA Dnet 스캔 타임: 5 msec

슬롯: 0 자국 번호: 0 플레이트: 1

수정...

등록 목록

번호	스캔 타입	송신 주기 (msec)	PLC 영역	크기 (비트 수)
0	L0.X SC			
1	R1.S PL	5	%MW0	2
2	R2.R PL	5	%QW0.1.0	2
3	R3.S PL	5	%MW100	1
4	R4.S PL	5	%MW200	4
5				
6				
7				
8				
9				
10				
11				

삭제... 복사... 수정...

닫기 도움말

7 FIELDBUS

7 FIELDBUS

7.1

Fieldbus(Rnet) / ,
 , 가 ()

Rnet GLOFA MASTER-K ,

Rnet V1.0 GLOFA Rnet MASTER-K Rnet

	Rnet V1.0	
(FMM)	G3L-RUEA	GM3/K1000S Rnet()
	G4L-RUEA	GM4/K300S Rnet()
	G6L-RUEA	GM6/K200S Rnet()
	G7L-RUEA	GM7/K80S Rnet()

7.2

I/O ()		
		1Mbps(Rnet)
		Manchester Biphase-L
	()	750m
	()	$750m * (6 + 1)$ $= 5.25km$
		$+ = 64$ ()
		256
		Circulated Token Passing
		Connection Oriented Connectionless
		$CRC\ 16 = X^{15} + X^{14} + X^{13} + \dots + X^2 + X + 1$

7 FIELDBUS

7.3

7.3.1

RNET 4
SMART I/O 가 .

Rnet 가
MASTER-K KGL-WIN GLOFA GMWIN,
가 , RNET V1.0 .

1) Rnet 가

(Rnet)

RNET	G3L-RUEA	3,840	1,920	64	(0-63)	60
	G4L-RUEA	3,840	1,920	64	(0-63)	60
	G6L-RUEA	3,840	1,920	64	(0-63)	60
	G7L-RUEA	3,840	1,920	64	(0-63)	60

SMART I/O

			GLOFA-GM	MASTER-K	
32	32	20ms ~ 10s	%QW, %IW	P	7.3.2

- 1) 가 SMART I/O 32 63
2,016 가 .
2) ' 4 ' .

7 FIELDBUS

7.3.2

1)

()
가 ,

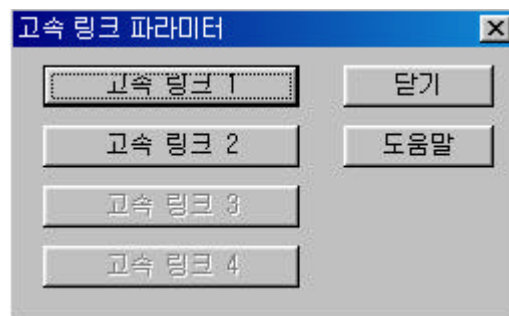
	-	- LINK_ TROUBLE	TRX_MODE	DEV_MODE	DEV_ERROR	HS_STATE
(□= 1,2,3,4)	_HS□LINK	_HS□LTRBL	_HS□TRX[n] (n= 0~63)	_HS□MOD[n] (n= 0~63)	_HS□ERR[n] (n= 0~63)	_HS□STATE[n] (n= 0~63)
	BIT	BIT	BIT-ARRAY	BIT-ARRAY	BIT-ARRAY	BIT-ARRAY
	가	가	가	가	가	가
	가	가	가	가	가	가

7.3.3 GMWIN

1) GMWIN

GMWIN

가



가

GM7

GM7 RNET

2)

1~4 PLC CPU

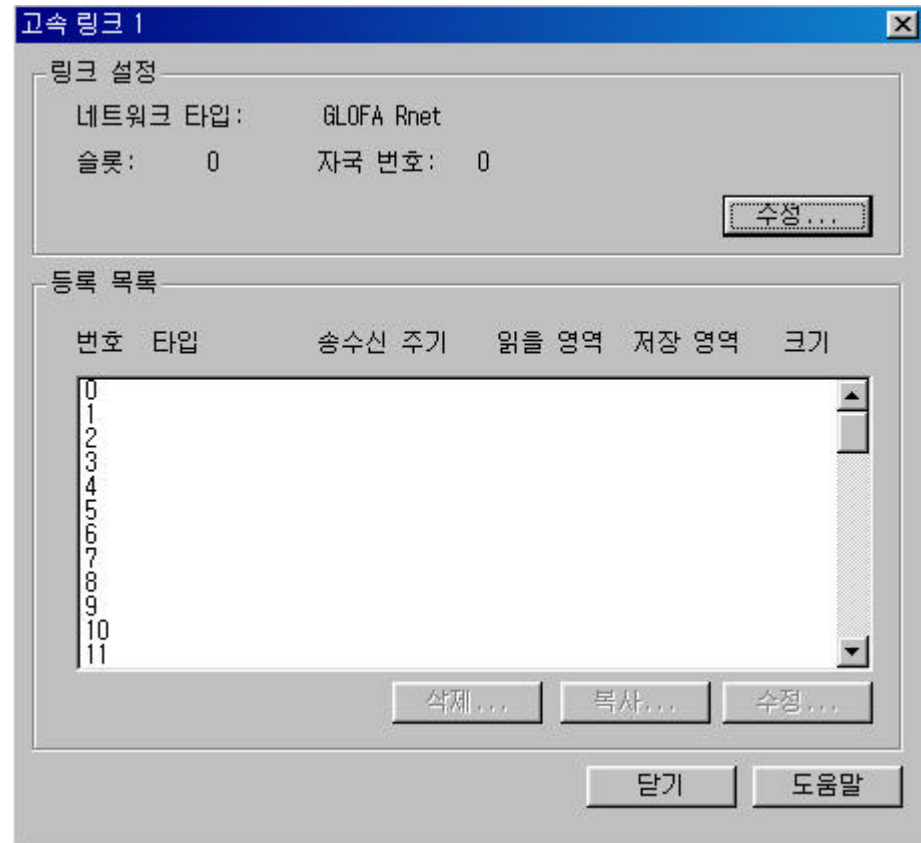
. GLOFA GM1/GM2/GM3 CPU/GM4-CPUB 4 , GLOFA GM4-CPUA/GM6
2 , GM7 1 가 .

CPU

	가	(1)
GLOFA-GM3	G3L-RUEA	4
GLOFA-GM4-CPUA	G4L-RUEA	2
GLOFA-GM4-CPUB	G4L-RUEA	4
GLOFA-GM6	G6L-RUEA	2
GLOFA-GM7	G7L-RUEA	1

3)

가 ,



(1)



: Rnet
 : '0' '7'
 :
 : Rnet
 0

(2) G7L-RUEA



7 FIELDBUS

: GLOFA Rnet

:

: 0

(3)

4

1) Smart I/O

GRL-DT4A

가

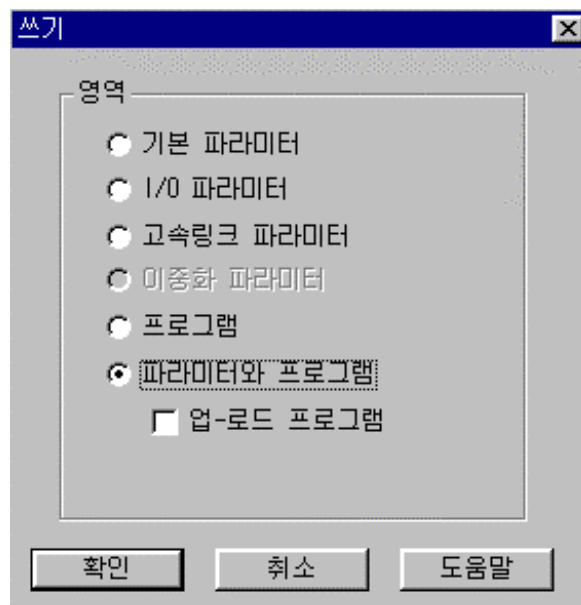
4)

GMWIN

PLC

CPU

(1)



7 FIELDBUS

가

GMWIN
PLC

, GMWIN

(2)



(3) G7L-RUEA



가
PLC

가

PLC
PLC CPU

(4)

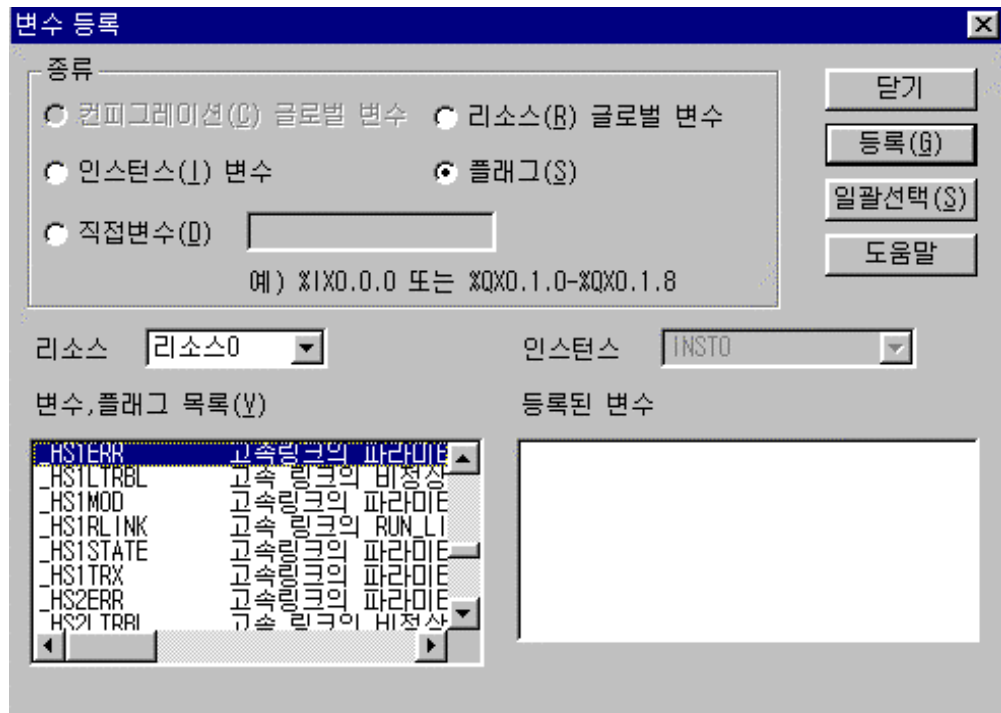
GMWIN

가

GMWIN

,
 (_HSxSTATE[n], _HSxERR[n],
 _HSxMOD[n],_HSxTRX[n] ARRAY 가
).

1) 'x' GM1/GM2/GM3/GM4-CPUB PLC 1~4
 ,GM4-CPUA,GM6 PLC 1~2, GM7 1
 [n] (0~63)



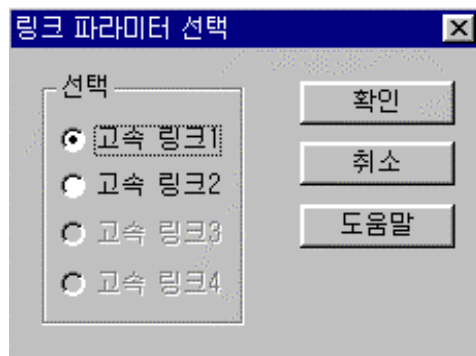
()



RNET

GMWIN

가



가

(), (),
가

()

고속 링크 파라미터 모니터

런 링크: 1 링크 트러블: 0

번호	타입	송수신주기	읽을영역	저장영역	크기	모드	통신	에러
0	리모트2.송신2	A(20ms)	%MW0	%QW0.0.0	1	1	1	0
1	리모트3.송신3	A(20ms)	%MW0	%QW0.0.0	1	1	1	0
2					0	0	0	0
3					0	0	0	0
4					0	0	0	0
5					0	0	0	0
6					0	0	0	0
7					0	0	0	0
8					0	0	0	0
9					0	0	0	0
10					0	0	0	0
11					0	0	0	0
12					0	0	0	0
13					0	0	0	0
14					0	0	0	0
15					0	0	0	0
16					0	0	0	0
17					0	0	0	0
18					0	0	0	0
19					0	0	0	0
20					0	0	0	0
21					0	0	0	0
22					0	0	0	0

4

1)

Smart I/O GRL-TR4A 가

' 0 '

7.3.4 KGLWIN

1) KGLWIN

KGL-WIN

(1) KGL-WIN

KGL-WIN (K200S)

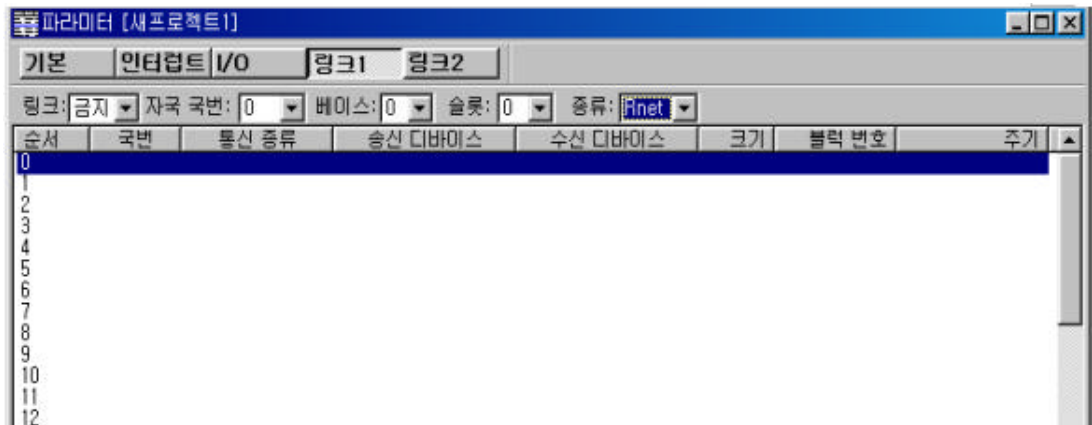


(2)

KGL-WIN

1

1



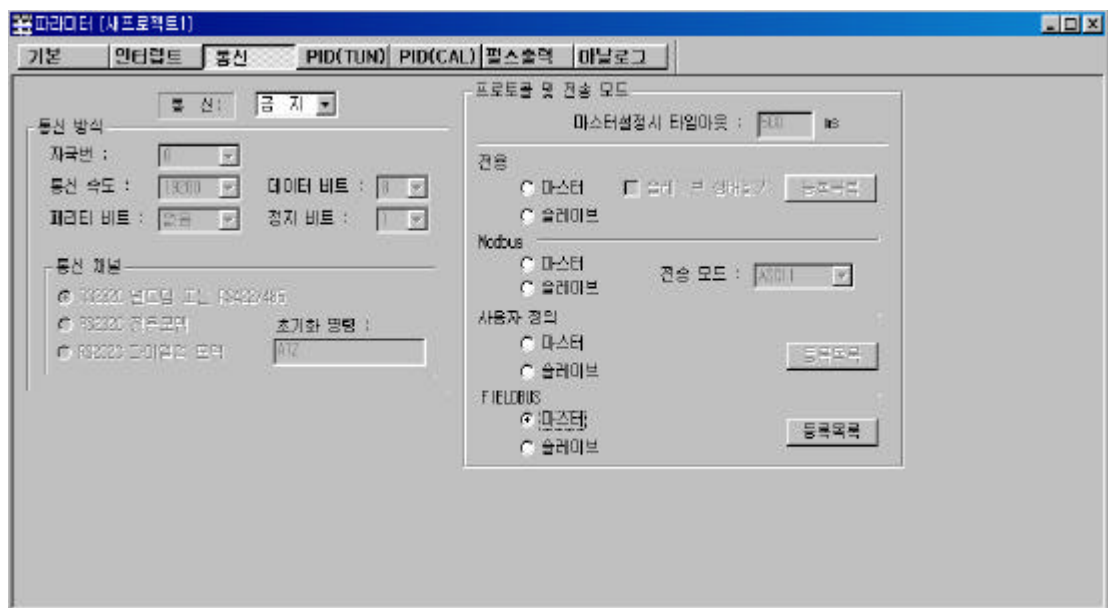
7 FIELDBUS

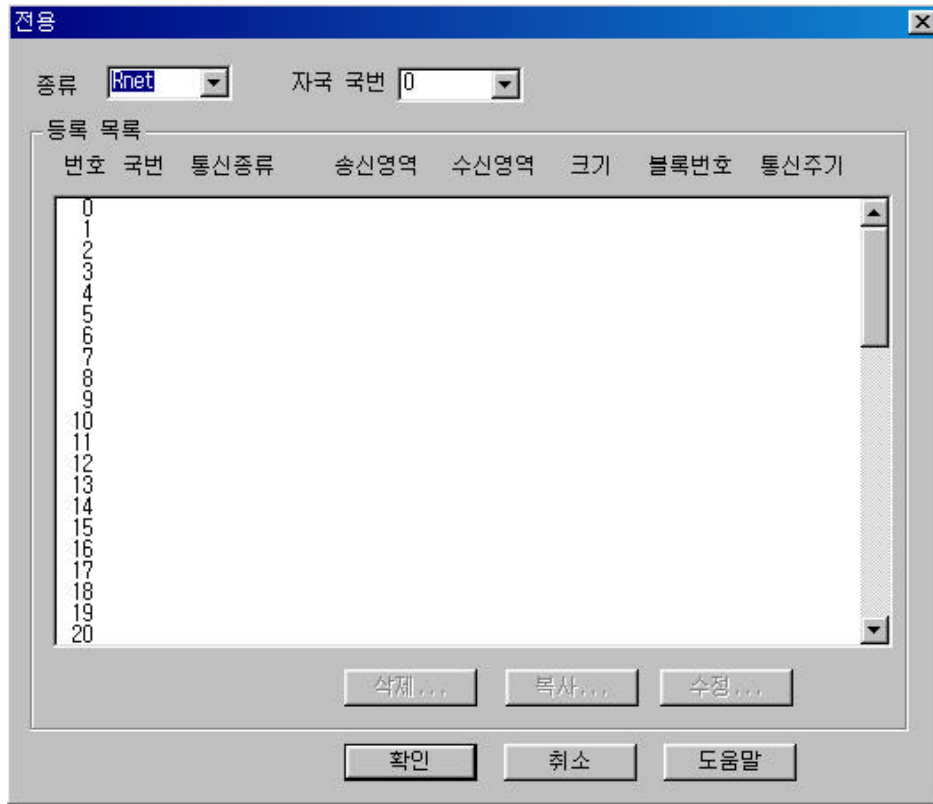
(3) K80S K80S



KGL-WIN

FIELDBUS





1 : K1000S CPU 4 , K300S/K200S 2
 , K80S 1 .
 가 ,
 가 . [6.6.1(A)] CPU 가

CPU

K1000S	G3L-RUEA	4	가
K300S(v2.2)	G4L-RUEA	2	
K300S(v2.2)	G4L-RUEA	4	
K200S	G6L-RUEA	2	
K80S	G7L-RUEA	1	

: .(,)
 : '0'
 : , '0' '7'
 .

: 가
'0' '63' , 63
32 가 .

(4)

No.0 ,

(1 0)

링크 항목 수정			
국번: 0	송신 디바이스: P000	확인	
블럭 번호: 0	수신 디바이스: P000	취소	
	크기: 1		
통신 종류: 리모트송신	통신 주기: 20 msec		

: .

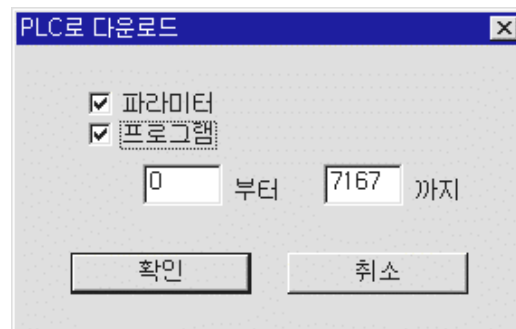
	()	1~63

: /

SMART I/O 32 31 31
, 32 31
64
2 가
2 가 RNET
64
가 .

		가	
		P,M,L,K,F,D,T,C	
		P	
		P	
		P,M,L,K,D,T,C	

(5)



2)

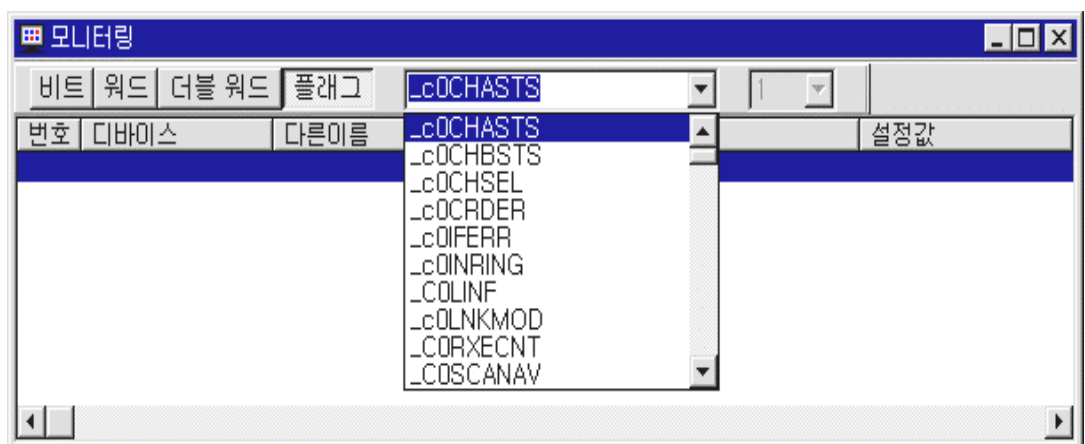
KGL-WIN

가

KGLWIN []→[]

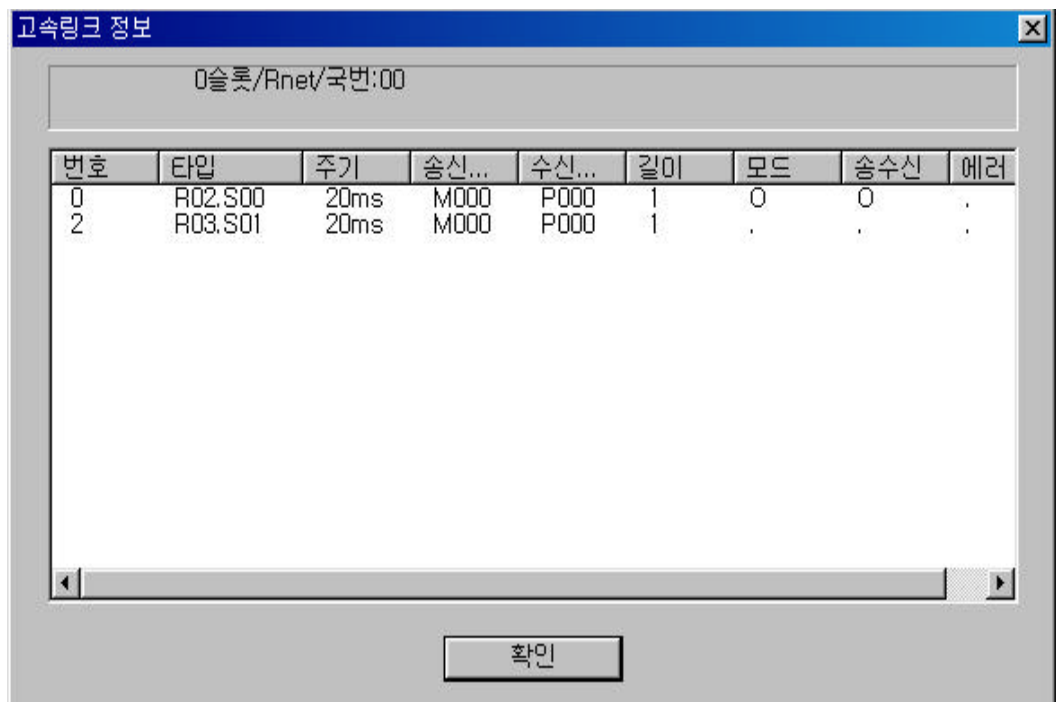
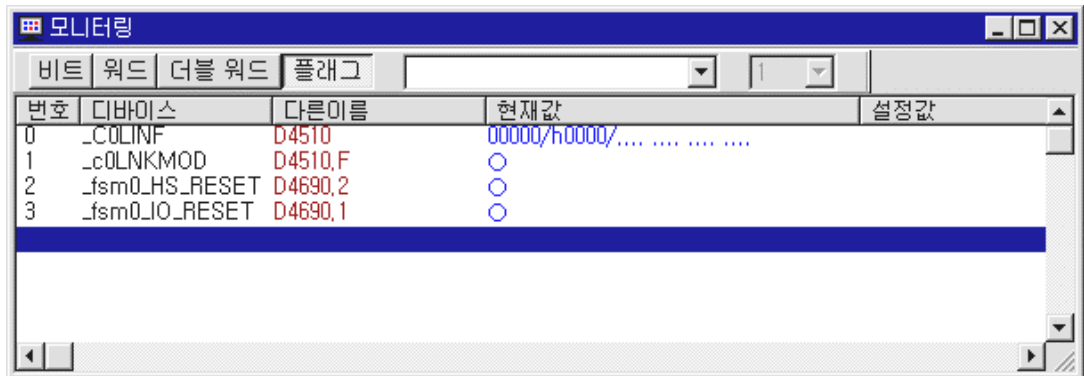
(▼)

가

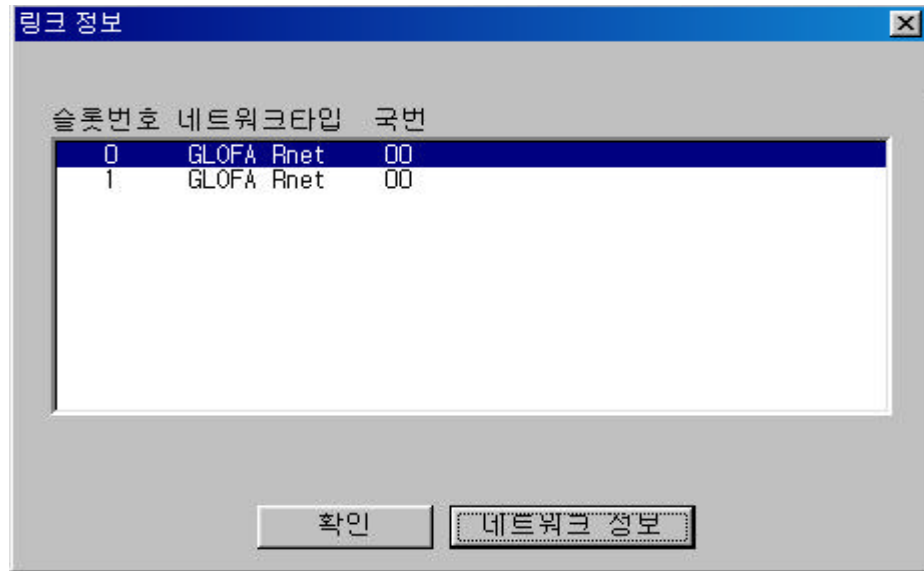


7 FIELDBUS

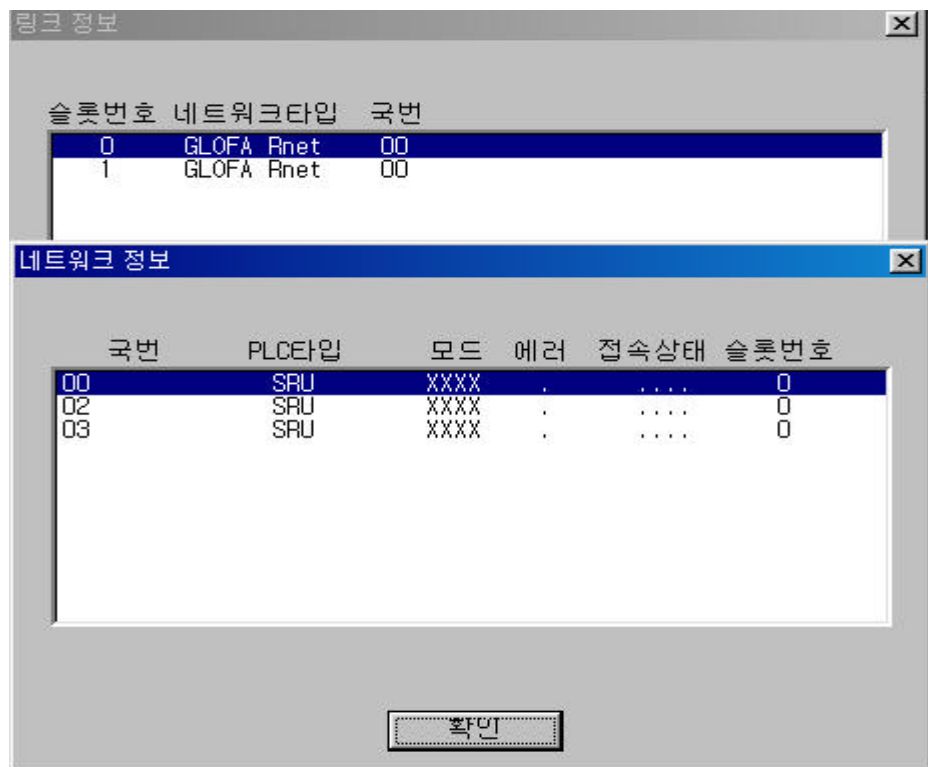
(가)



R02.R03 (Smart I/O) 2 3 SOO,S01
, (M000) 0 (Smart I/O) 2 (P000)
. R03 (M000) 1
(Smart I/O) 3 (P000)



RNET (K80S)



7 FIELDBUS

3)

L (0) x : , n :

NETx LIV[n]	L0001 ~ L003F	L0001 ~ L000F(1~15)	Alive , 가 .(가)
		L0010 ~ L001F(16~31)	
		L0020 ~ L002F(32~47)	
		L0030 ~ L003F(48~63)	
		L0050 ~ L005F(16~31)	
		L0060 ~ L006F(32~47)	
		L0070 ~ L007F(48~63)	

x : K1000S=9, K300S/K200S=4 m :

	Type	Bit		
_HSmRLI NK	Bit	Dx600.0	RUN_LINK	ON 1. RUN , 가 2. 3. 가 - ON ON
_HSmLT RBL	Bit	Dx600.1	(LINK_TROUBLE)	_HSmRLINK가 ON 가 ON 1. RUN 가 2. 가 3. 가 1,2,3 ON , 가 OFF
_HSmST ATE[k] (k=0~63)	Bit Array	Dx601.0 ~ Dx604.15	k	_HSmSTATE[k] = _HSmMOD[k] & _HSmTRX[k] & _HSmERR[k]

7 FIELDBUS

	Type	Bit		
_HSmMO D[k] (k=0~63)	Bit Array	Dx605.0 ~ Dx608.15	(RUN = 1, = 0)	k
_HSmTR X[k] (k=0~63)	Bit Array	Dx609.0 ~ Dx612.15	(=1, =0)	k 가
_HSmER R[k] (k=0~63)	Bit Array	Dx613.0 ~ Dx616.15	k (=1, =0)	k 가

m=1~3

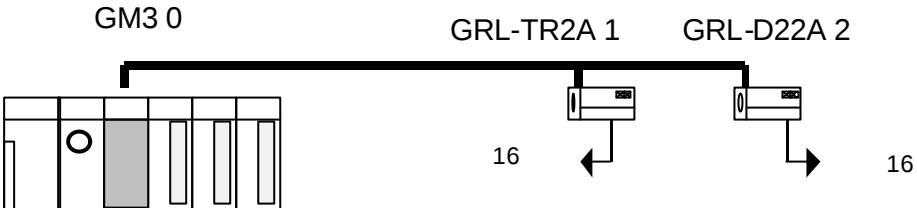
	D	
High Speed Link2 (m=1)	Dx620 ~ Dx633	$[A1.2]_{m=0} \quad m=1 \sim 3 \quad D$ $: m=1 \sim 3 \quad D = [A3.3] + 20^m$
High Speed Link3 (m=2)	Dx640 ~ Dx653	
High Speed Link4 (m=3)	Dx660 ~ Dx673	

7.4

7.4.1 GLOFA-GM

1

GM3 0 (G3L-RUEA), 1 32 , 2
32 , GM3 %IW0.2.0 1 , 2
GM3 %QW0.1.0 .



I/O

CPU

I/O

GM3→1	%IW0.2.0	%QW0.0.0	0	1
GM3←2	%IW0.0.0	%QW0.1.0	1	1

- 1)
- 2) ()
- 3)
- 4) GMWIN
- 5)
- 6)
- 7)
- 8)
- 9) 가 On
- 10) 1)

GM3 0 1

GM3 0 2

1)

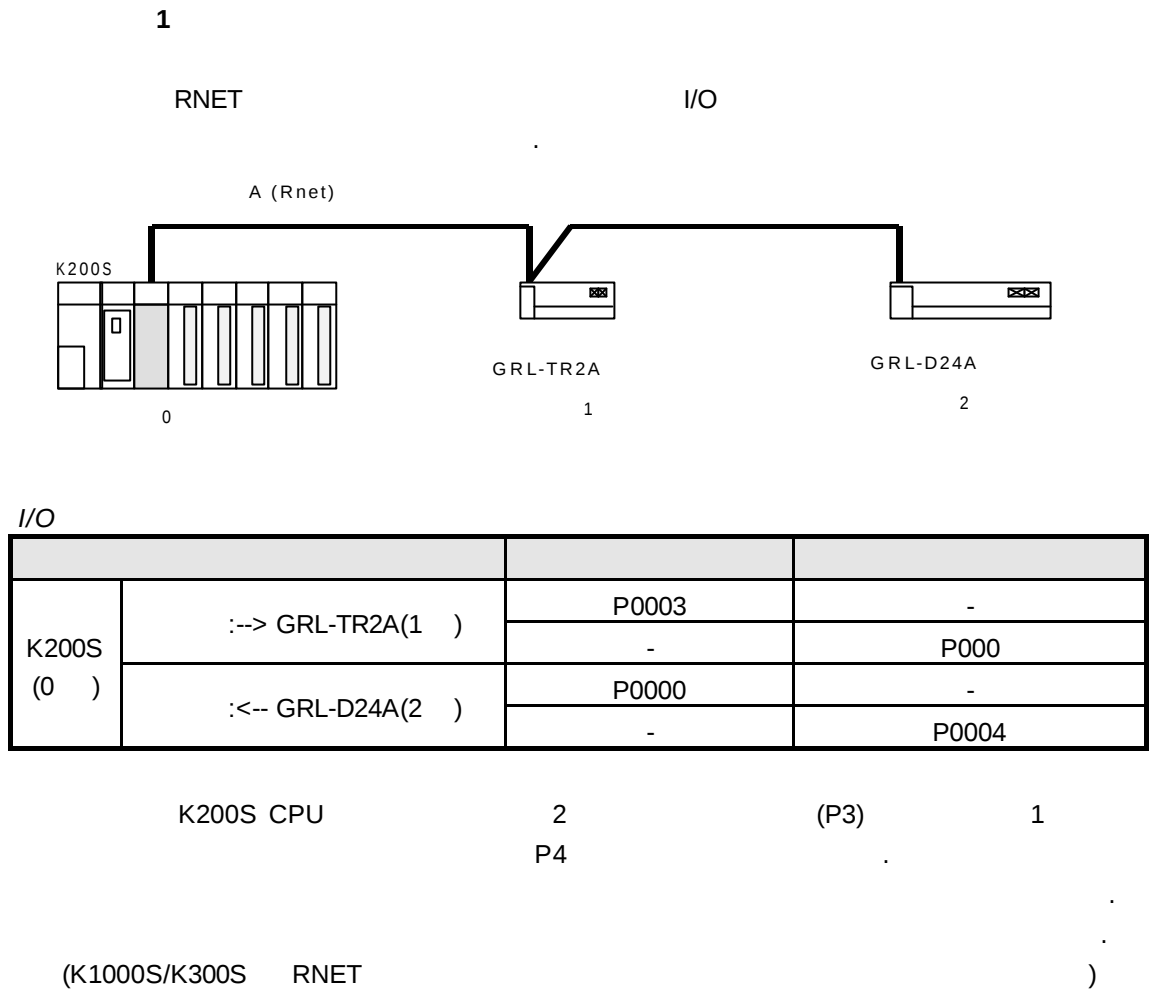
2

2

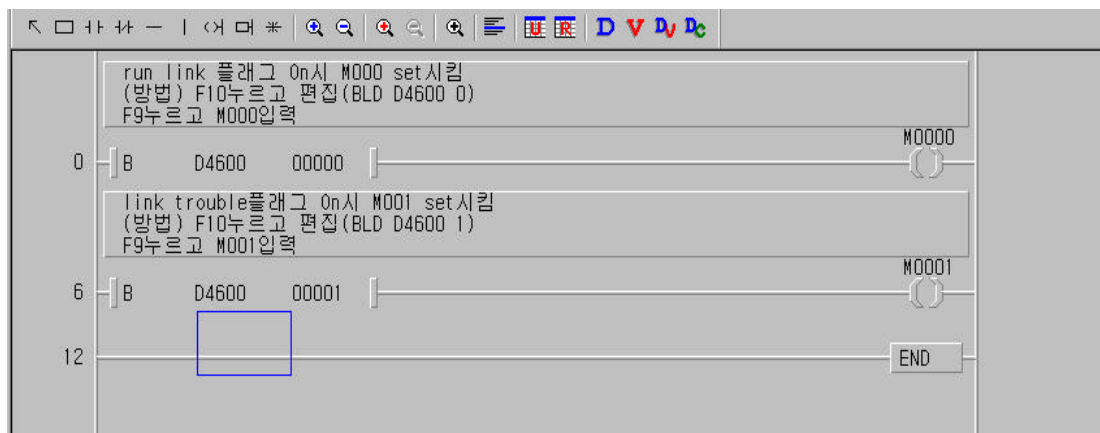
.

7 FIELDBUS

7.4.2 MASTER-K



1)



7 FIELDBUS

Run Link 가 On M0000 Set Link Trouble 가
On M0001 Set

()

0,1,2

PLC

1)

2)

()

3)

4) KGL-WIN

5)

6)

7)

8)

1)

K200S (0)

파라미터 [박길환.kpr]							
기본 인터럽트 I/O 링크 링크2							
링크:	허용	자국 국번:	0	베이스:	0	슬롯:	0
				종류:	Rnet		
순서	국번	통신 종류	송신 디바이스	수신 디바이스	크기	블럭 번호	주기
0	1	리모트송신	P003	P000	1	0	20 msec
1	2	리모트수신	P000	P004	1	1	20 msec
2							
3							
4							
5							
6							
7							

()

1)

3

1

7 FIELDBUS

$$St = P_scanA + C_scan$$

$$St =$$

$$P_scanA = PLC\ A$$

$$C_scan =$$

P_scanA, PLC 3ms 가
(KGL-WIN - -PLC 가)

$$C_scan = n1 \times 180us + n2 \times 828us + 1,000us \text{ -----[7-1]}$$

n1 :

n2 :

$$C_scan = 1 \times 180 + 1 \times 828 + 1,000 = 2,008us$$

$$St = P_scanA(=3ms) + Cscan(2ms) = 5ms$$

5ms .

8 MODBUS

8 MODBUS

8.1

SMART I/O GM3/4/6/7
(G3L-CUEA/G4L-CUEA/G6L-CUEC/G7L-CUEC)
(ASCII : American Standard Code for Information Interchange)
(HEX) RTU(Remote Terminal Unit)
01, 02,
03, 04, 05, 06, 15, 16
'Modicon Modbus Protocol Reference Guide'
(<http://www.modicon.com/techpubs/toc7.html>)

8.2

1)

- (1)
- (2) ':(Colon) : H3A), CRLF(
- (Carriage Return-Line Feed) : HOD HOA)
- (3) (Character) 1 (Interval)
- (4) LRC
- (5) ()

					LRC	(CR LF)
	1	2	2	n	2	2

2) RTU

- (1)
- (2) (Address) CRC
- (3) 3.5 (Character Time) (Interval) 가
- (4) (Character) 1.5 (Character Time)
- (5) 16 CRC
- (6) ()

				CRC
	1	1	n	2

1) 1 1 , 1 8 , 1

2) 1 1
 1) 2,400 bps 1
 2,400 bps 2,400 1 가 1
 $1() \div 2,400() = 0.41(\text{ms})$
 $0.41(\text{ms}) \times 8() = 3.28(\text{ms})$
 584, 984A/B/X 가 LRC 1 (Interval)

3) (Address)

(1) SMART I/O 0 ~ 31
 (2) 0 (Broadcast)
 가 SMART I/O

4) (Function Code)

(1) SMART I/O Modicon 01, 02, 03, 04, 05, 06, 15, 16
 (2) Confirm+ (ACK)
 (3) Confirm- (NCK) 8 1

) 가 03
 가

[Request] 0000 0011 (H03)

[Confirm+] 0000 0011 (H03)

[Confirm-] 1000 0011 (H83)

Request 8
 1

5) (Data)

(1) () (RTU)
 (2) 가
 (3)

8 MODBUS

6) (LRC Check/CRC Check)

(1) LRC(Longitudinal Redundancy Check) : /

2

(2) CRC(Cyclical Redundancy Check) : RTU 2 CRC

16	, 10	, 2
10	7, 10	
16	: H07, H0A	16#07, 16#0A
10	: 7, 10	
2	: 2#0111, 2#1010	

7) (Function Code)

		Modicon PLC	SMART I/O	
01	(Read Coil Status)	0XXXX(-)	%QX0~%QX31	
02	(Read Input Status)	1XXXX(-)	%IX0~%IX31	
03	(Read Holding Registers)	4XXXX(-)	%QW0~%QW3	
04	(Read Input Registers)	3XXXX(-)	%IW0~%IW3	
05	1 (Force Single Coil)	0XXXX(-)	%QX0~%QX31	
06	1 (Preset Single Register)	4XXXX(-)	%QW0~%QW3	
15	(Force Multiple Coils)	0XXXX(-)	%QX0~%QX31	
16	(Preset Multiple Register)	4XXXX(-)	%QW0~%QW3	

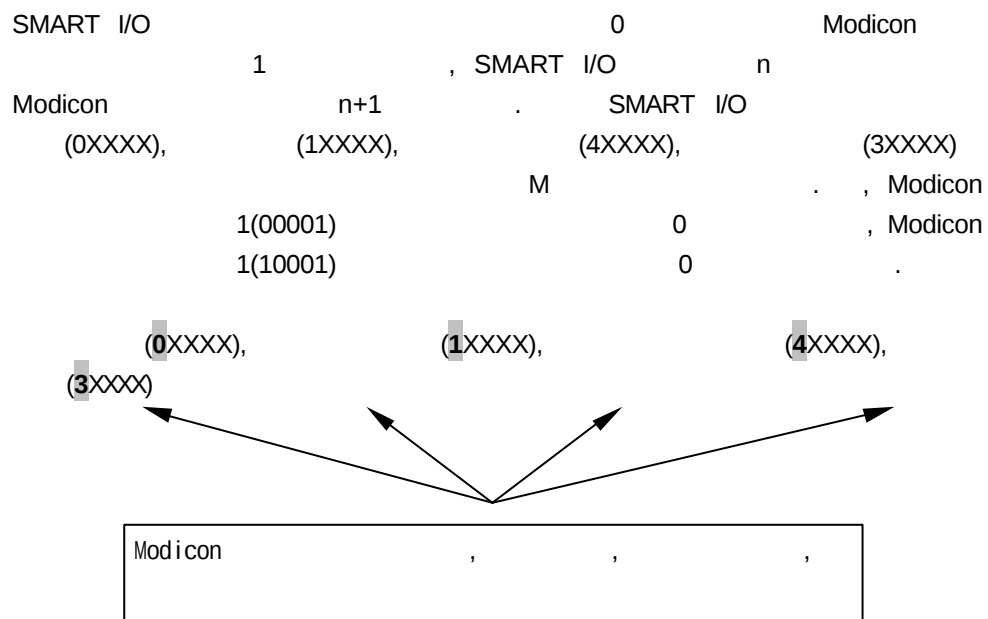
8 MODBUS

MASTER-K

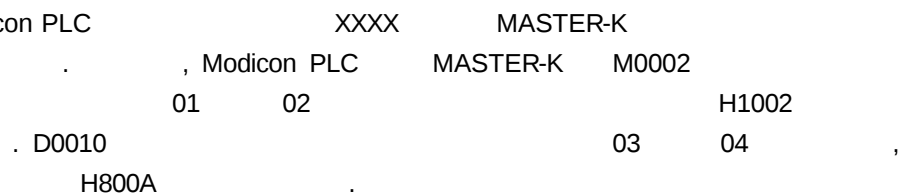
h0000	P	h0000	P
H1000	M	H1000	M
H2000	L	H2000	L
H3000	K	H3000	K
H4000	F	H4000	F
H5000	T ()	H5000	T ()
H6000	C ()	H6000	C ()
		H7000	S
		H8000,H9000	D

8)

(1) GLOFA-GM
SMART I/O

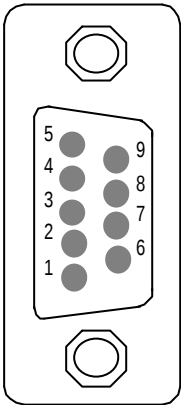


(2) MASTER-K
Modicon PLC



9) SMART I/O 128 , RTU
256 Modicon 가 'Modicon
Modbus Protocol Reference Guide'

10)

 (Male Type)	PIN	SMART I/O(9PIN)
	1	
	2	(1) 1 : (OS Upgrade)
	3	(2) 2 : (OS Upgrade)
	4	(3) 3 : RX-
	5	(4) 4 : RX+
	6	(5) 5 : GND
	7	(6) 6 /7 :
	8	(7) 8 : TX-
	9	(8) 9 : TX+

8.3

8.3.1 GLOFA-GM

8.3.1.1 GM3/4/6

1)

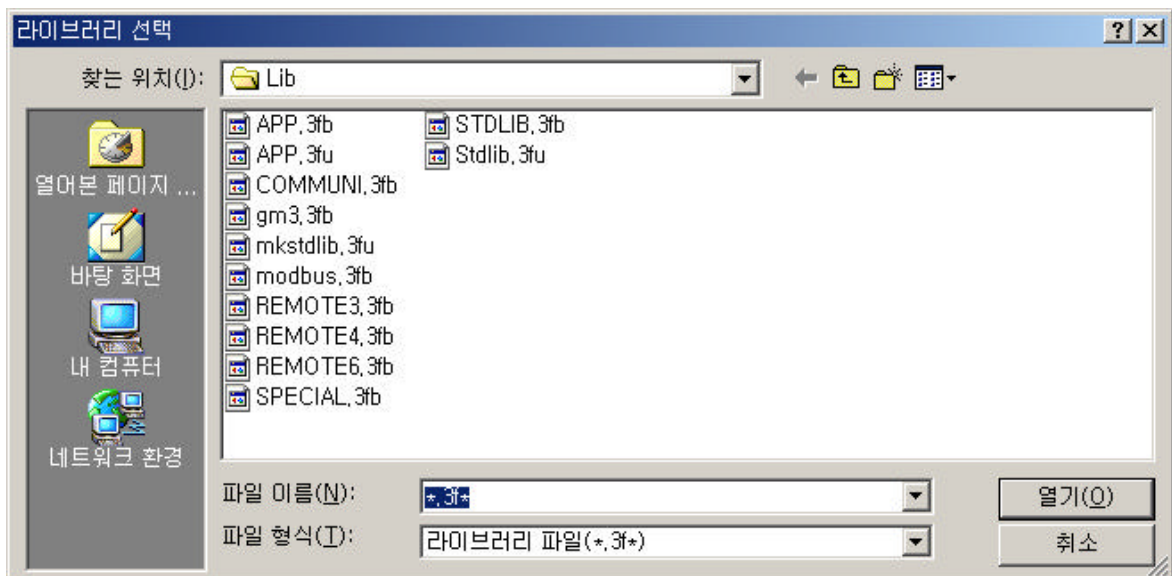
GM3/4/6

SMART I/O

GMWIN

GM3	G3L-CUEA	MODBUS.3FB	GMWIN 가
GM4	G4L-CUEA	MODBUS.4FB	
GM6	G6L-CUEA	MODBUS.6FB	

(1) GMWIN



(2) GMWIN

7.4

- RTU_WR
- RTU_RD

8.3.1.2 GM7

1)

(1) GMWIN

- PLC GM7
-

(2) GMWIN

통신파라미터

통신 방식

자국번 : 0

통신 속도 : 19200 데이터 비트 : 8

패리티 비트 : 없음 정지 비트 : 1

통신 채널

☒ RS232C 널모뎀 또는 RS422/485

☐ RS232C 전용모뎀 초기화 명령 :

☐ RS232C 다이얼업 모뎀 ATZ

프로토콜 및 전송 모드

마스터설정시 타임아웃 : 500 ms

전용

☐ 마스터 ☐ 슬레이브 상태읽기 등록목록

☐ 슬레이브

Modbus

☐ 마스터 전송 모드 : ASCII

☒ 슬레이브

사용자 정의

☐ 마스터 등록목록

☐ 슬레이브

FIELD BUS

☐ 마스터 등록목록

☐ 슬레이브

확인 취소 도움말

8 MODBUS

3)

	1 31 . (0 .)
	2400, 4800, 9600, 19200, 38400 bps
	7 8 . : 7 . RTU : 8 .
	, Even, Odd
	1 2 . 가 : 1 . 가 : 2 .
	• RS-232C RS422/485 : GM7 Cnet I/F (G7L-CUEC) • RS-232C : Cnet I/F (G7L-CUEB) • RS-232C : Cnet I/F (G7L-CUEB)) RS-232C RS-232C RS-232C Cnet I/F (G7L-CUEB) RS-422/485 Cnet I/F (G7L-CUEC)
	• GM7 • 500ms • PLC •
Modbus /	가
	RTU

8.3.2 MASTER-K

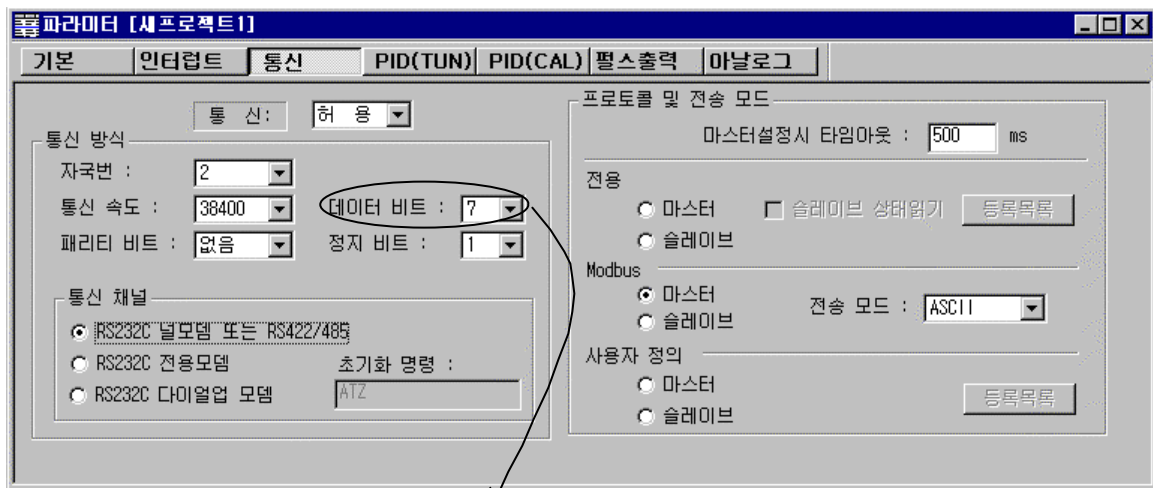
8.3.1.1 K80S

1)

(1) KGLWIN

- PLC MK80S
-

(2) KGLWIN



가 ASCII

7

1	31 . (0 .)
	2400, 4800, 9600, 19200, 38400 bps
7	8 .
	: 7 .
RTU	: 8 .
	, Even, Odd .
1	2 .
	가 : 1 .
	가 : 2 .

8 MODBUS

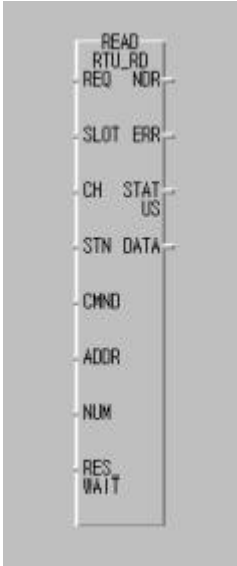
	• RS-232C RS-422/485 : MK80S Cnet I/F (G7L-CUEC) • RS-232C : Cnet I/F (G7L-CUEB) • RS-232C : Cnet I/F (G7L-CUEB)) RS-232C RS232C RS-232C Cnet I/F (G7L-CUEB) RS-422/485 Cnet I/F (G7L-CUEC)
	• MK80S • 500ms • PLC •
Modbus /	가
	RTU

8.4

8.4.1 GLOFA-GM

8.4.1.1 GM3/4/6

1) RTU_RD

	IN / OUT	Type	Description														
	REQ	BOOL	Function Block (Rising edge) - 0 1, 1														
	SLOT	USINT	Cnet (0 ~ 7)														
	CH	USINT	(0 : RS-232C, 1 : RS-422/485)														
	STN	USINT	(0 ~ 32)														
	CMND	USINT	Modbus Command (1 ~4) 1 : Read coil status (Bit) 2 : Read input status (Bit) 3 : Read holding register (Word) 4 : Read input register (Word)														
	ADDR	INT	Read (1 ~ 9999)														
	NUM	USINT	Read Data (1 ~ 64)														
	RES_WAIT	TIME	Response wait time (CPU Cnet Data)														
	NDR	BOOL	1Scan ' ON' .														
	ERR	BOOL	1Scan ' ON' .														
	STATUS	USINT	code (Error code) 0 : , 0 : Error code														
	DATA	USINT ARRAY (256)	<table><tr><td>Array [0] :</td><td>Word</td><td>High Byte</td></tr><tr><td>Array [1] :</td><td>Word</td><td>Low Byte</td></tr><tr><td>Array [2] :</td><td>Word</td><td>High Byte</td></tr><tr><td>Array [3] :</td><td>Word</td><td>Low Byte</td></tr><tr><td colspan="3">...</td></tr></table>	Array [0] :	Word	High Byte	Array [1] :	Word	Low Byte	Array [2] :	Word	High Byte	Array [3] :	Word	Low Byte	...	
Array [0] :	Word	High Byte															
Array [1] :	Word	Low Byte															
Array [2] :	Word	High Byte															
Array [3] :	Word	Low Byte															
...																	

(1)

01, 02, 03, 04

/ 01
(Coil Status) 02

8 MODBUS

(Input Status)

03

(Holding Registers)

04

(Input Registers)

(2)

STATUS

Response Wait Time

1) CPU Cnet

2) Error(code 74 code 10)가

Function Block Cnet

Protocol Mode'

3) Response Wait Time / (Baudrate),

4) Error 가

	1 ~ 16 word	17 ~ 32 word	33 ~ 48 word	49 ~ 64 word
4800 bps	150ms	250ms	330ms	400ms
9600 bps	100ms	180ms	230ms	280ms
19200 bps	80ms	150ms	180ms	230ms

2) RTU_WR

	IN / OUT	Type	Description														
	REQ	BOOL	Function Block (Rising edge) - 0 1 , 1														
	SLOT	USINT	Cnet (0 ~ 7)														
	CH	USINT	(0 : RS-232C, 1 : RS-422/485)														
	STN	USINT	(0 ~ 32)														
	CMND	USINT	Modbus Command (15 ~ 16) 15 : Force Multiple coils(Bit) 16 : Preset Multiple register (Word)														
	ADDR	INT	Write (1 ~ 9999)														
	NUM	USINT	Write Data (1 ~ 64)														
	RES_WAIT	TIME	Response wait time (CPU Cnet Data)														
	NDR	BOOL	1Scan ' ON' .														
	ERR	BOOL	1Scan ' ON' .														
	STATUS	USINT	code (Error code) 0 : , 0 : Error code														
	DATA	USINT ARRY (256)	Write <table border="1"><tr><td>Array [0] :</td><td>Word</td><td>High Byte</td></tr><tr><td>Array [1] :</td><td>Word</td><td>Low Byte</td></tr><tr><td>Array [2] :</td><td>Word</td><td>High Byte</td></tr><tr><td>Array [3] :</td><td>Word</td><td>Low Byte</td></tr><tr><td colspan="3">...</td></tr></table>	Array [0] :	Word	High Byte	Array [1] :	Word	Low Byte	Array [2] :	Word	High Byte	Array [3] :	Word	Low Byte	...	
Array [0] :	Word	High Byte															
Array [1] :	Word	Low Byte															
Array [2] :	Word	High Byte															
Array [3] :	Word	Low Byte															
...																	

(1)

15 16

1 (15), 1 (

16) . 15 (Coil) 1

16 (Holding Registers)

1 .

(2)

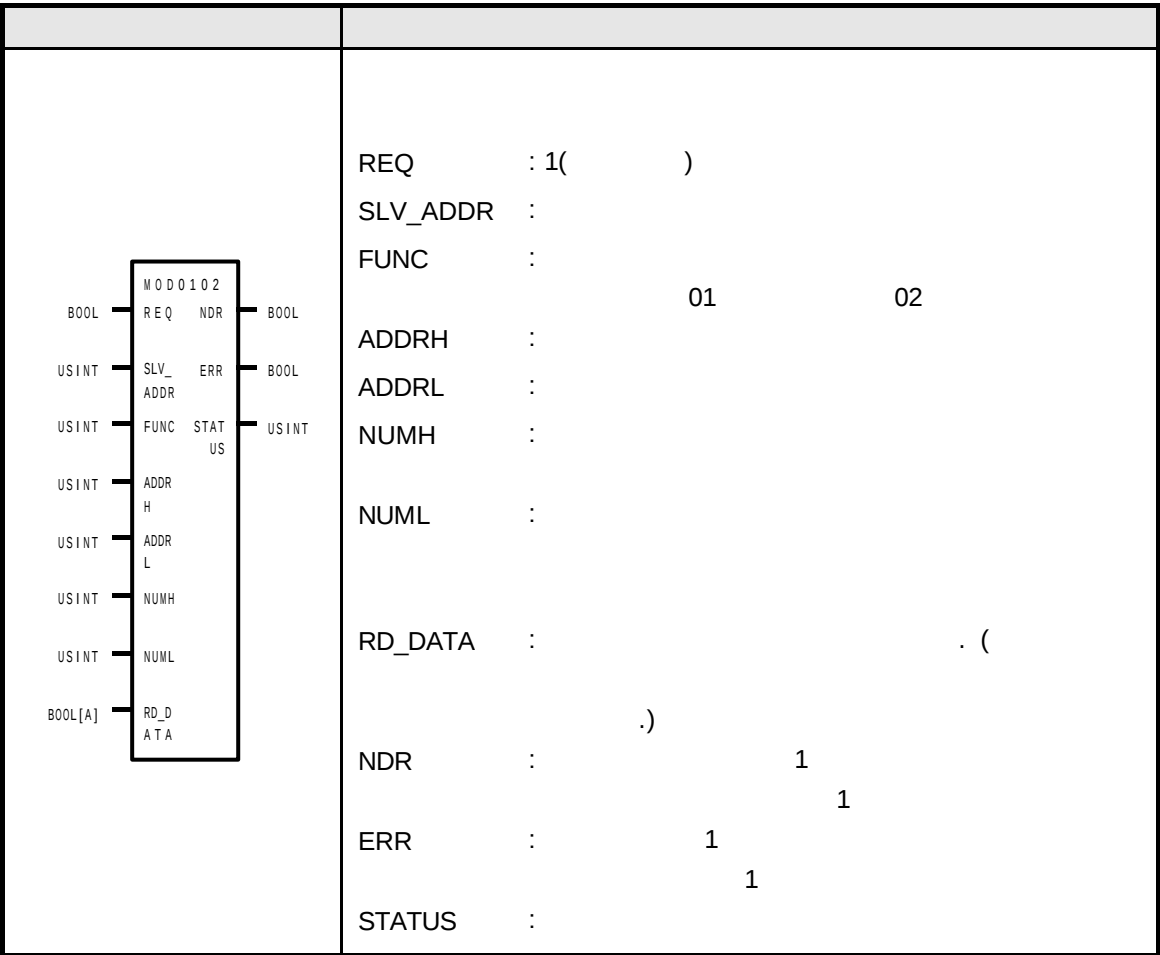
STATUS .

- 1) Function Block Cnet 'Protocol Mode'
- 2) (Baudrate, Data bit, Stop bit, Parity check, Station No.) Cnet
가
- 3) Modbus RTU Protocol (ASCII Protocol)
- 4) Function Block Cnet
 - Cnet : v2.0 (GMWin 가)
 - Cnet Flash Rom OS version : v1.01 (Cnet Editor 가)
 - Modbus.Nfb (Modbus Function Block , N=3,4,6)

Status code (Dec)			
0		No error	
1	Illegal command (Slave Command Master)	Slave 가 Command FB Command	Slave
2	Illegal address (Slave Address Master)	Slave 가 Address FB Address	
3	Illegal data value (Slave Data Master)	Slave 가 Data FB Data	
4	Slave device failure (Slave Master 가)	Slave	
6	Slave device busy	Master	
10	Frame CRC error	1. Frame Response Wait Time 2. Cable Noise	Function Block
16	Cnet module I/F error (Cnet)	FB Slot	
64	Channel(232c/422) stop	Cnet RUN ()	
74	Time out error	1. (/) 2. Slave () 3. Cable	
115	Communication mode error	Cnet	

8.4.1.2 GM7

1) MOD0102



(3)

01 02
02 01
(Coil
(Input Status)

(4)

STATUS

(5)

GM7

17

Modicon

● 17 (Coil) 00020 ~ 00056

가 RD_DB0 가 40 (BOOL)

RD_DB0

	59	58	57	56	55	54	53	52	51	50	49	48	47	46	45	44	43	42	41	40
	X	X	X	1	1	0	1	1	0	0	0	0	1	1	1	0	1	0	1	1
	1				B				0				E				B			
	39	38	37	36	35	34	33	32	31	30	29	28	27	26	25	24	23	22	21	20
	0	0	1	0	0	1	1	0	1	0	1	1	1	1	0	0	1	1	0	1
	2				6				B				C				D			

<Modicon

00020~00059

>

● 57, 58, 59 (Redundancy)

●

0

1)

1) CD 6B B2 0E 1B

REQ	
SLV_ADDR	16#11 17 :
FUNC	16#01 1 : ' 1'
ADDRH	16#00 0 :
ADDRL	16#13 19 : 00020 8) 19 FUNC

8 MODBUS

NUMH	16#00 0 :
NUML	16#25 37 : - 00020 ~ 00056 37 . 37 H0025 NUMH H00 NUML H25 .

●

RD_DB0[0]	1	RD_DB0[10]	0	RD_DB0[20]	1	RD_DB0[30]	0
RD_DB0[1]	0	RD_DB0[11]	1	RD_DB0[21]	1	RD_DB0[31]	0
RD_DB0[2]	1	RD_DB0[12]	0	RD_DB0[22]	0	RD_DB0[32]	1
RD_DB0[3]	1	RD_DB0[13]	1	RD_DB0[23]	1	RD_DB0[33]	1
RD_DB0[4]	0	RD_DB0[14]	1	RD_DB0[24]	0	RD_DB0[34]	0
RD_DB0[5]	0	RD_DB0[15]	0	RD_DB0[25]	1	RD_DB0[35]	1
RD_DB0[6]	1	RD_DB0[16]	0	RD_DB0[26]	1	RD_DB0[36]	1
RD_DB0[7]	1	RD_DB0[17]	1	RD_DB0[27]	1	RD_DB0[37]	X
RD_DB0[8]	1	RD_DB0[18]	0	RD_DB0[28]	0	RD_DB0[38]	X
RD_DB0[9]	1	RD_DB0[19]	0	RD_DB0[29]	0	RD_DB0[39]	X

●

가

STATUS

RD_DB0[0]

●

●

가

GM7

17

Modicon

●

17

(Input) 10197 ~ 10218

가

가 24

(BOOL)

RD_DB1

8 MODBUS

	10220	10219	10218	10217	10216	10215	10214	10213	10212	10211	10210	10209
	X	X	1	1	0	1	0	1	1	1	0	1
	3				5				D			
	10208	10207	10206	10205	10204	10203	10202	10201	10200	10199	10198	10197
	1	0	1	1	1	0	1	0	1	1	0	0
	B				A				C			

- 10219, 10220 (Redundancy)
-

0

1)

2) AC DB 35

REQ	
SLV_ADDR	16#11 17 :
FUNC	16#02 2 : ' 2'
ADDRH	16#00 0 :
ADDRL	16#C4 196 : - 10197 8) 196 FUNC
NUMH	16#00 0 :
NUML	16#16 22 : - 10197 ~ 10218 22 . 22 H0016 NUMH H00 NUML H16

8 MODBUS

●

RD_DB1[0]	0	RD_DB1[6]	0	RD_DB1[12]	1	RD_DB1[18]	1
RD_DB1[1]	0	RD_DB1[7]	1	RD_DB1[13]	0	RD_DB1[19]	0
RD_DB1[2]	1	RD_DB1[8]	1	RD_DB1[14]	1	RD_DB1[20]	1
RD_DB1[3]	1	RD_DB1[9]	1	RD_DB1[15]	1	RD_DB1[21]	1
RD_DB1[4]	0	RD_DB1[10]	0	RD_DB1[16]	1	RD_DB1[22]	X
RD_DB1[5]	1	RD_DB1[11]	1	RD_DB1[17]	0	RD_DB1[23]	X

●

가

STATUS

●

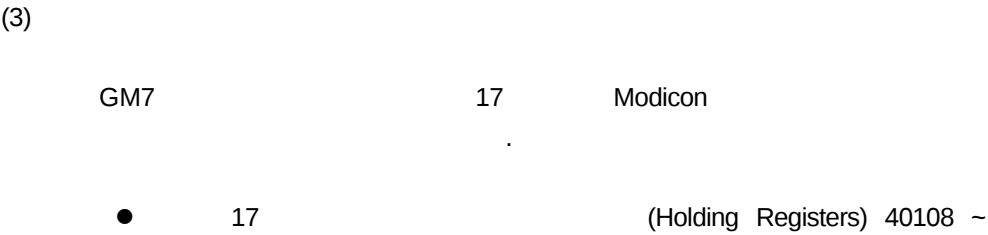
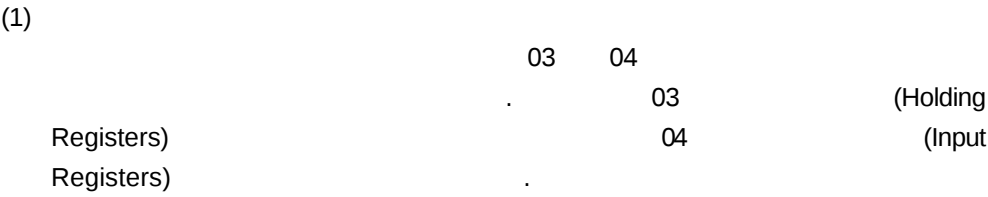
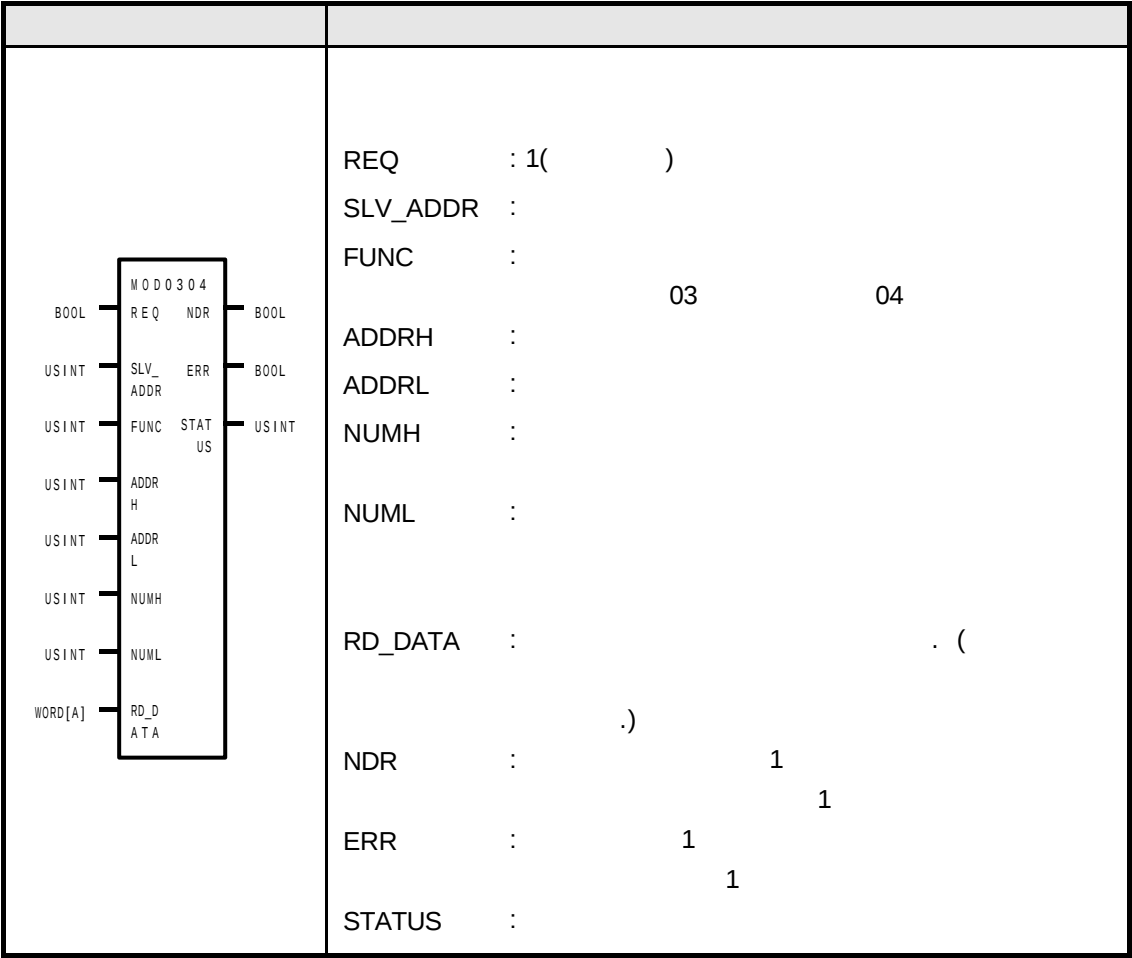
RD_DB1[0]

●

가

8 MODBUS

2) MOD0304



8 MODBUS

40110

가

가 4

RD_DW0

	40110	40109	40108
	H0064	H0000	H022B

1)

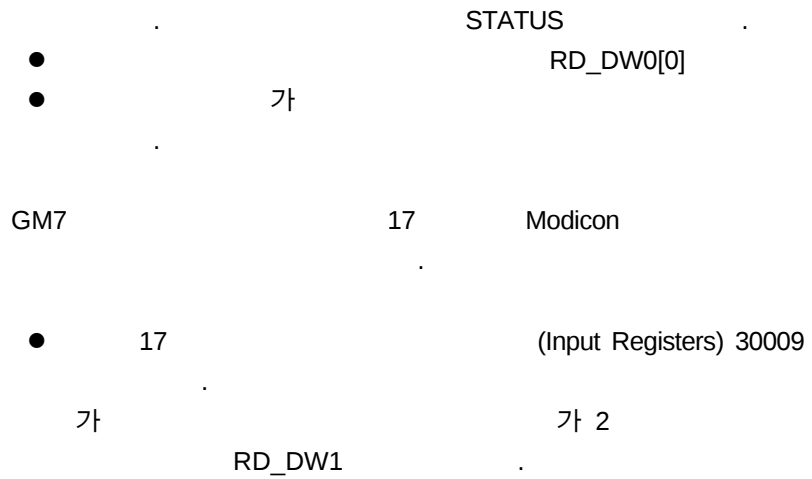
1) 02 2B 00 00 00 64

REQ	
SLV_ADDR	16#11 17 :
FUNC	16#03 3 : '3'
ADDRH	16#00 0 :
ADDRL	16#6B 107 : - 40108 8) 107 FUNC
NUMH	16#00 0 :
NUML	16#03 3 : - 40108 ~ 40110 3 3 H0003 NUMH H00 NUML H03

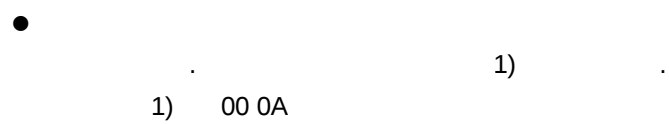
RD_DW0[0]	H002B 555
RD_DW0[1]	H0000 0
RD_DW0[2]	H0064 100
RD_DW0[3]	X

가

8 MODBUS



	30009
	H000A



REQ	
SLV_ADDR	16#11 17 :
FUNC	16#04 4 : ' 4'
ADDRH	16#00 0 :
ADDRL	16#08 8 : - 30009 8) 8 FUNC
NUMH	16#00 0 :
NUML	16#01 1 : - 30009 1 1 H0001 NUMH H00 NUML H01

•

RD_DW1[0]	H000A 10
RD_DW1[1]	X

•

가

STATUS

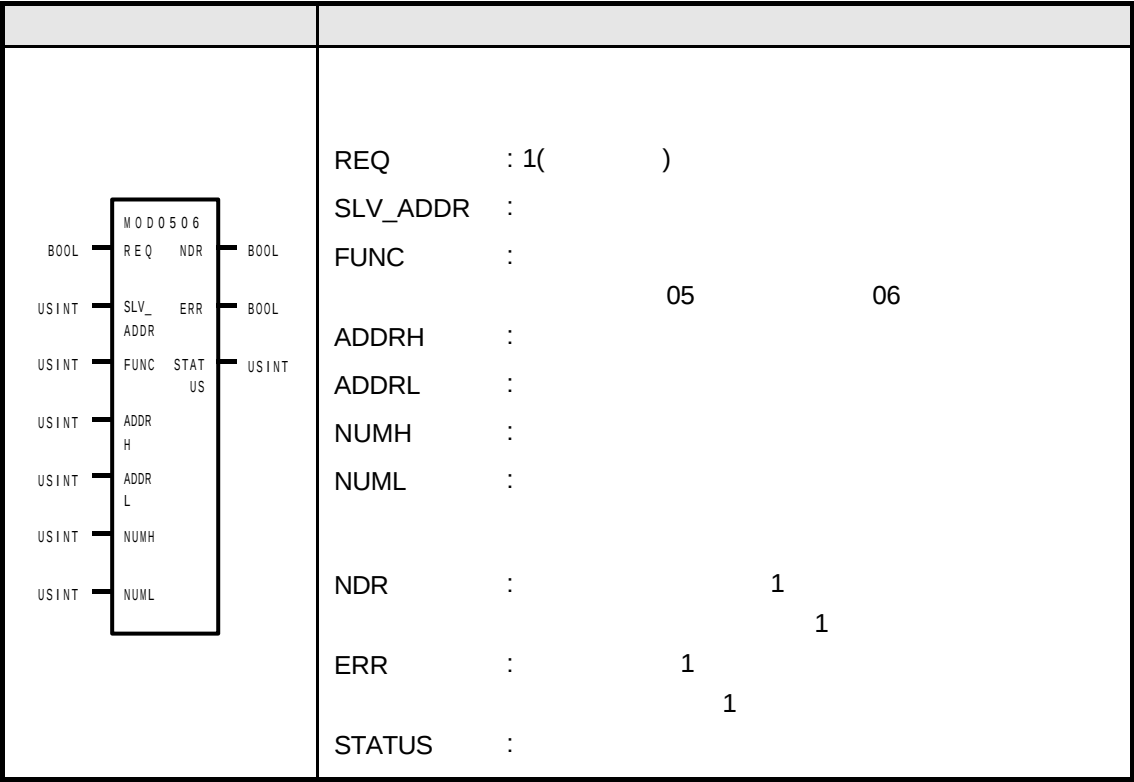
•

RD_DW1[0]

•

가

3) MOD0506



(1)

05 06
1 (05), 1 (06)
05 (Coil) 1
NUMH 255(HFF), NUML 0(H00)
1 NUMH 0(H00), NUML
0(H00) 0 06
(Holding Registers) 1

(2)

STATUS

8 MODBUS

(3)

GM7 17 Modicon 1

● 17 (Coil) 00173 1

REQ	
SLV_ADDR	16#11 17 :
FUNC	16#05 5 : 1 ' 5'
ADDRH	16#00 0 :
ADDRL	16#AC 172 : - 00173 8) 172 FUNC
NUMH	16#FF 255 : - ' 0' → H00 - ' 1' → HFF
NUML	16#00 0 : - 1 HUMH HFF NUML H00

● : 00173 ON (GM7
M 1 .)

	00173
	1

GM7 17 Modicon 1

● 17 (Holding Registers) 40002
3

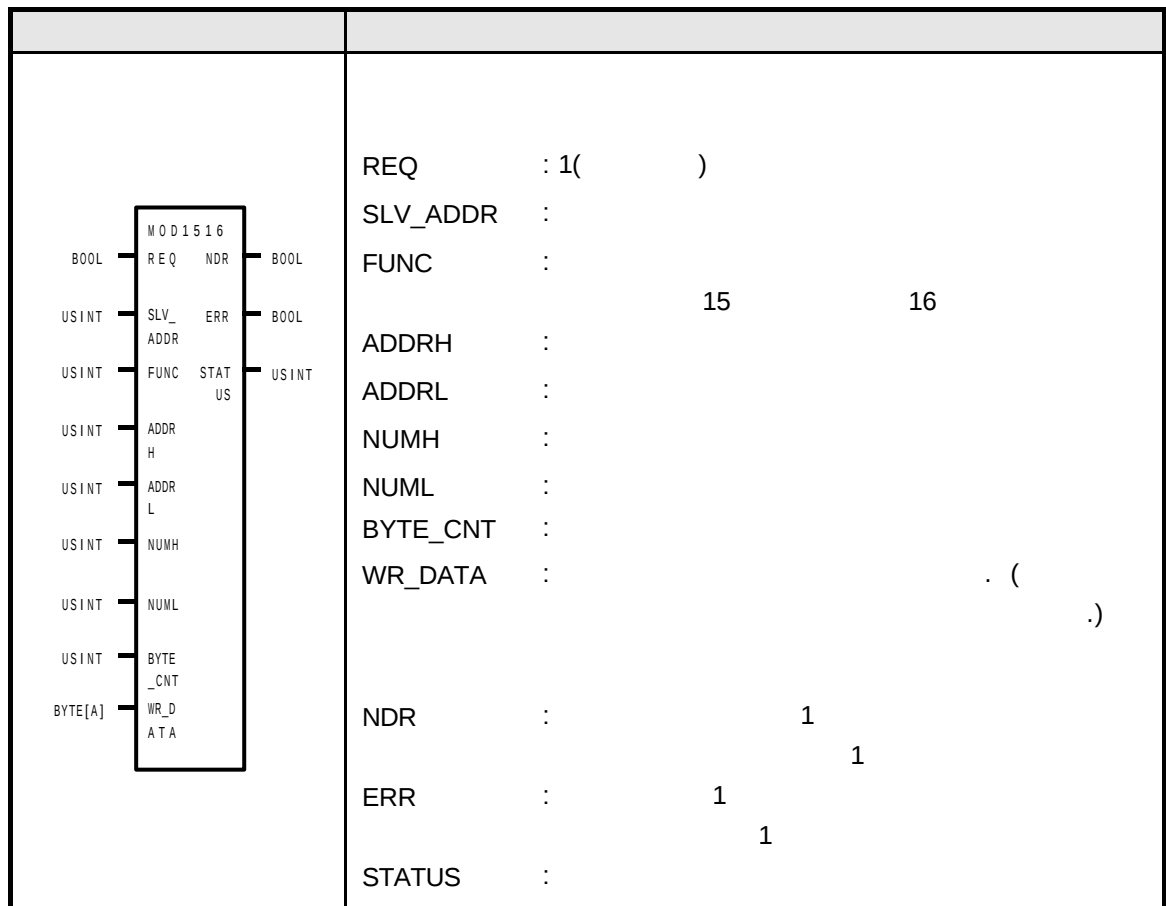
8 MODBUS

REQ	.
SLV_ADDR	16#11 17 :
FUNC	16#06 6 : 1 ' 6'
ADDRH	16#00 0 :
ADDRL	16#01 1 : - 40002 8) 1 FUNC
NUMH	16#00 0 :
NUML	16#03 3 : - 3 H0003 HUMH H00 NUML H03 .

● : 40002 H0003 .(GM7
M H0003 .)

	40002
	H0003

4) MOD1516



(3)

15 16
1 (15), 1 (16)
(Coil) 1
(Holding Registers)
1 .

(4)

STATUS

8 MODBUS

(5)

GM7

17

Modicon

- 17 (Coil) 00020 10
0111001101 1
가 2 (BYTE) WR_DB0

WR_DB0[0]	2#11001101	16#CD
WR_DB0[1]	2#10000001	16#81

- BYTE_CNT
1 10 1
2
6 0
BYTE_CNT 2
● WR_DB0 1000 0001 1100 1101 가
가 10 (01 1100
1101) 6 0
10 10
0
●
1)
1) CD 01

8 MODBUS

REQ	
SLV_ADDR	16#11 17 :
FUNC	16#0F 15 : ' 15'
ADDRH	16#00 0 :
ADDRL	16#13 19 : 00020 8) 19 FUNC
NUMH	16#00 0 :
NUML	16#0A 10 : 00020 10 H000A NUMH H00 NUML H0A
BYTE_CNT	16#02 2 : 10 2 BYTE_CNT H02

●

2 (16)
10

	00029	00028	00027	00026	00025	00024	00023	00022	00021	00020
	0	1	1	1	0	0	1	1	0	1

GM7

17

Modicon

●

17 (Holding Registers) 40002
000A 0102 가 4
(BYTE) WR_DB1

8 MODBUS

WR_DB1[0]	2#00001010	16#0A
WR_DB1[1]	2#00000000	16#00
WR_DB1[2]	2#00000010	16#02
WR_DB1[3]	2#00000001	16#01

- BYTE_CNT

2 4
 BYTE_CNT 4

-

1)

1) 00 0A 01 02

REQ	
SLV_ADDR	16#11 17 :
FUNC	16#10 16 : ' 16
ADDRH	16#00 0 :
ADDRL	16#01 1 : - 40002 8) 1 FUNC
NUMH	16#00 0 :
NUML	16#02 2 : - 40002 2 2 2 H0002 NUMH H00 NUML H02
BYTE_CNT	16#04 4 : - 2 4 BYTE_CNT H04

8 MODBUS

●

	40003	40002
	H0102	H000A

5)

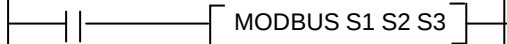
CODE		
01	Illegal Function	
02	Illegal Address	,
03	Illegal Data Value	,
04	Slave Device Failure	
05	Acknowledge	가
06	Slave Device Busy	
07	Time Out	
08	Number Error	가 0 , 256 , Number BYTE_CNT 가
09	Parameter Error	(, /)
10	Station Error	

8 MODBUS

8.4.2 MASTER-K

1) MODBUS

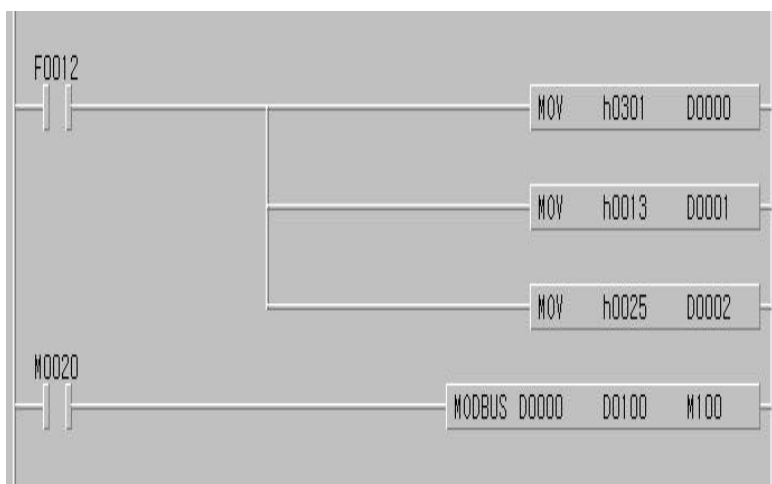
		가															
		M	P	K		L	F	T	C	S	D	# D			(F110)	(F111)	(F112)
MODBUS	S1	0	0	0	0	0	0	0	0		0	0		7	0		
	S2	0	0	0	0	0		0	0		0	0					
	S3	0	0	0	0	0		0	0		0	0					

			
	F110	#D	On
	S1	가	
	S2	가	
	S3		

(1)

- S1 MODBUS
- S2 (3Word) 가
- S1 가
- S3 가

(2)



()

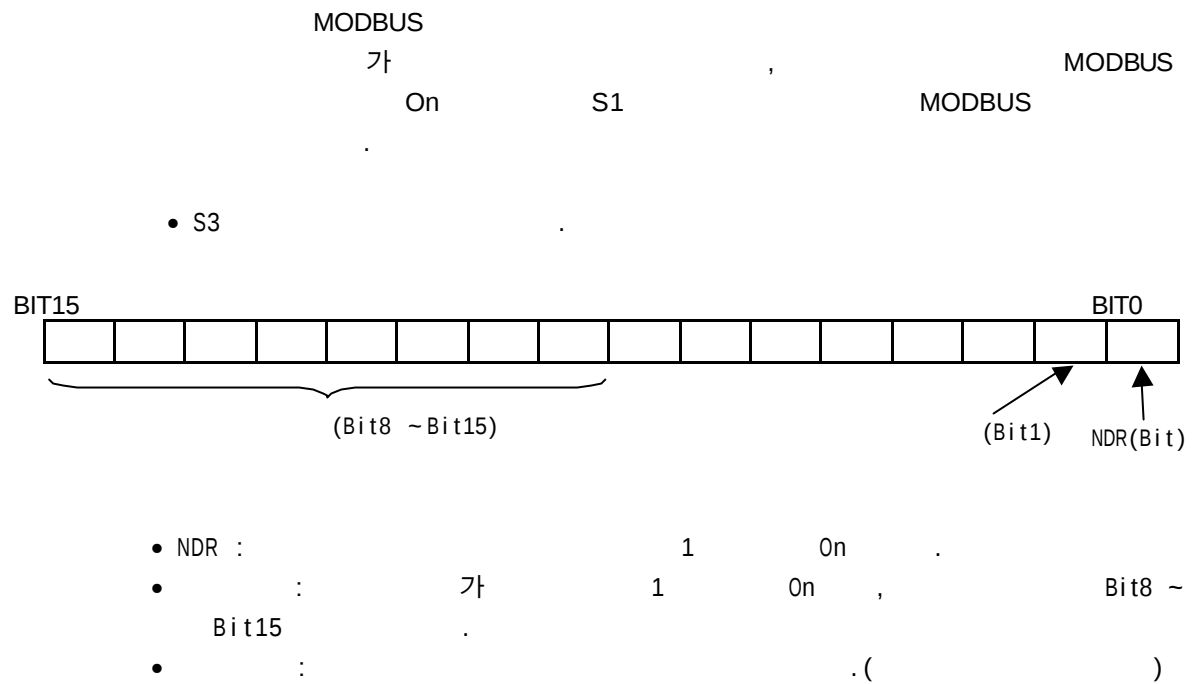
()

M0020 On D0000 MODBUS

D1000

M100 가

8 MODBUS



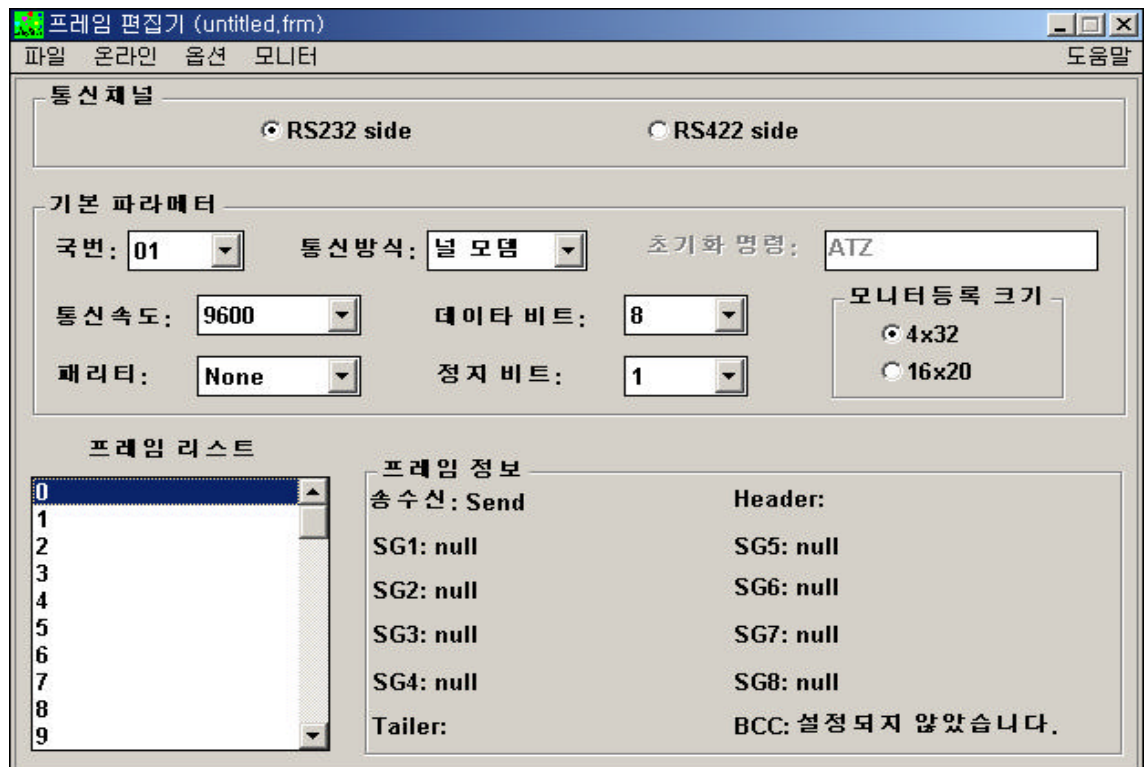
Code		
01	Illegal Function	
02	Illegal Address	Address
03	Illegal Data Value	
04	Slave Device Failure	가 .
05	Acknowledge	가 .
06	Slave Device Busy	가 .
07	Time Out	Time Out
08	Number Error	가 0 256Byte .
09	Parameter Error	
10	Station Error	MODBUS 가

8.5

8.5.1 GLOFA-GM

8.5.1.1 가 GM3/4/6

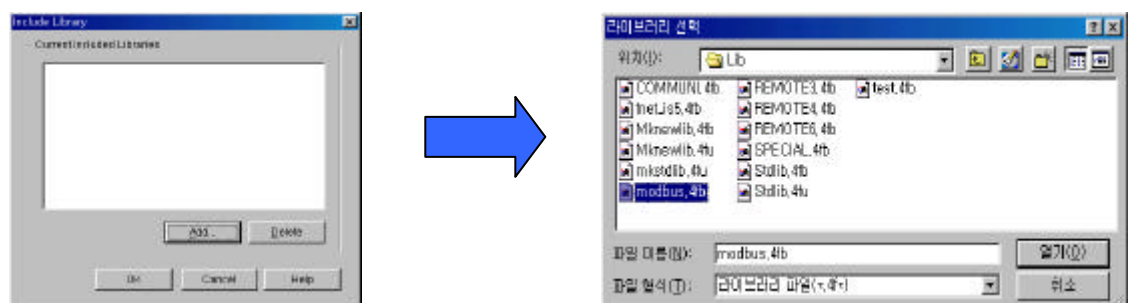
1) PLC Cnet

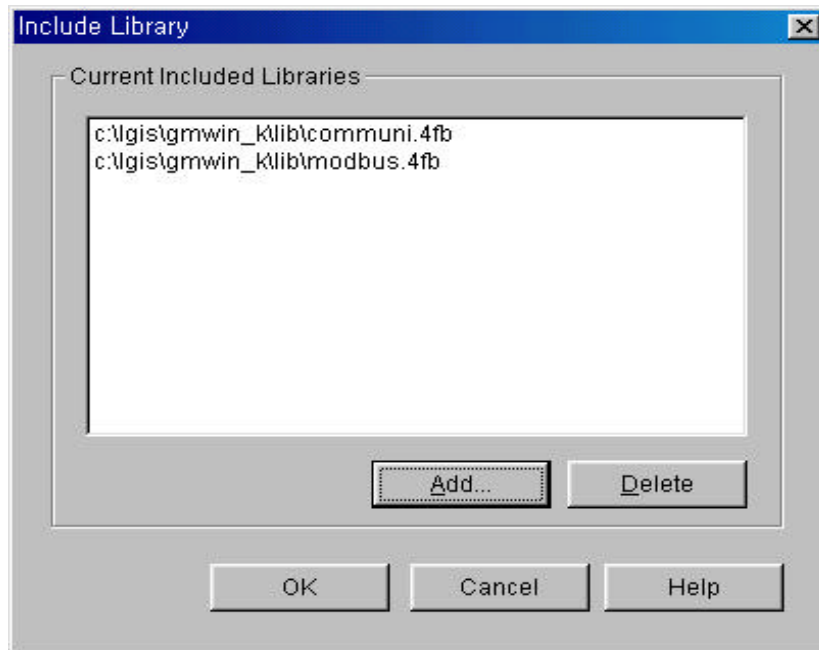


- (Baudrate, Parity, Data bit, Stop bit)

-

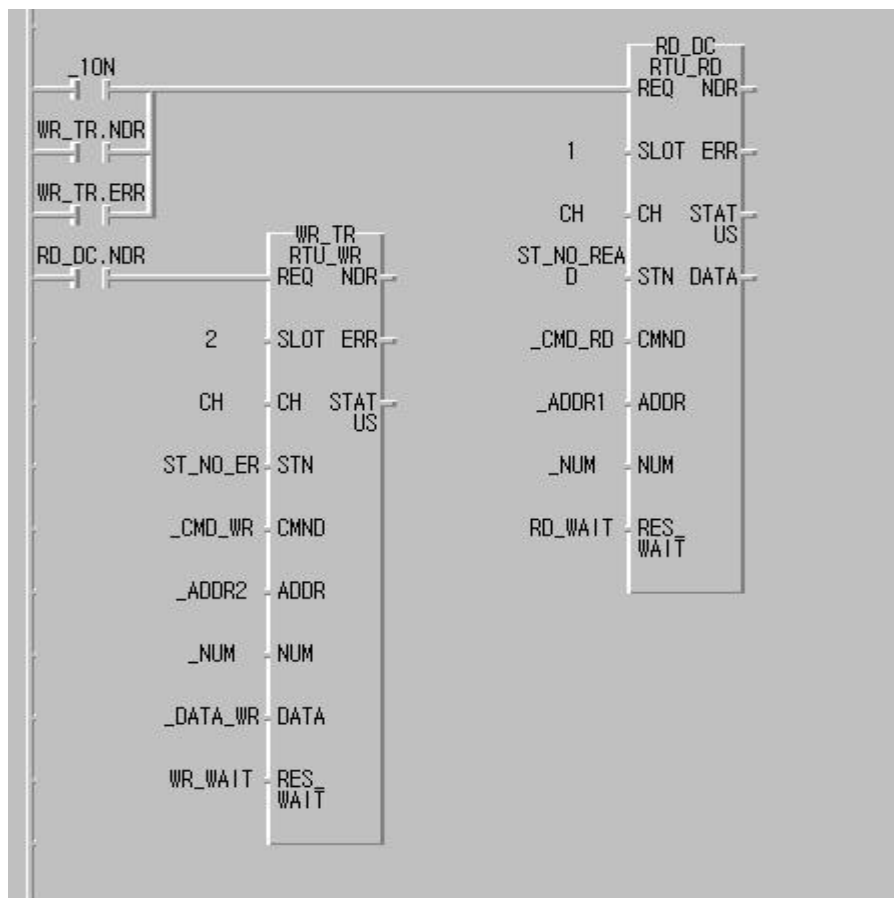
2)





3) GMWIN

LOAD



8.5.1.2 가 GM7

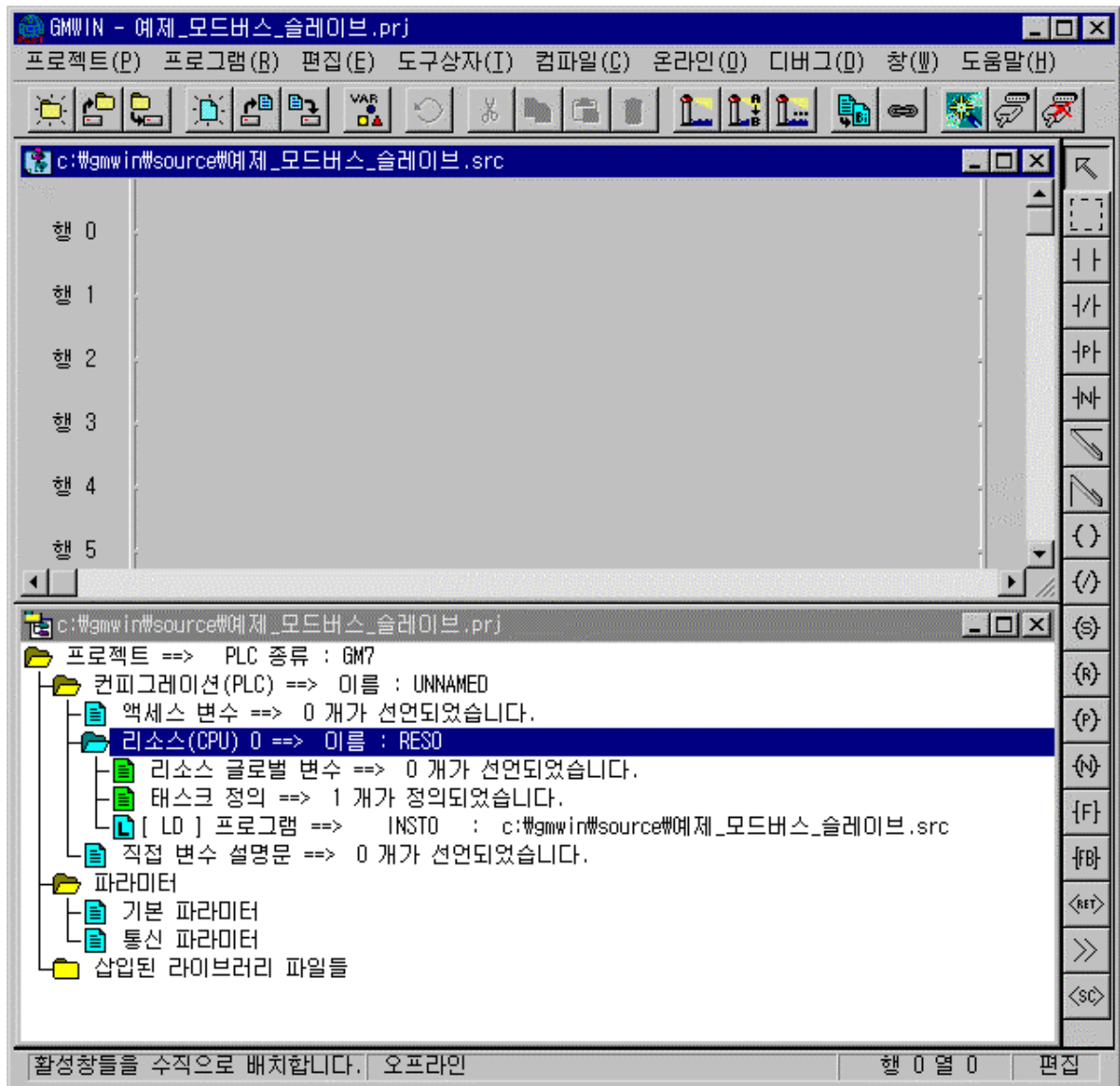
```

- : M
- : MOD0506( 06) %MW0(%MX0 ~ %MX15
    %MB0 ~ %MB1 .) 16#FF( 255)
    MOD0102( 01) %MX0
    MOD1516( 15) %MX0 ~ %MX9 0
    MOD0304( 03) %MW0
- 1:1

```

1)

(1)



(2) GMWIN

통신파라미터

통신 방식

자국번 : 17

통신 속도 : 2400

데이터 비트 : 7

패리티 비트 : Even

정지 비트 : 1

통신 채널

☒ RS232C 널모뎀 또는 RS422/485

☐ RS232C 전용모뎀

☐ RS232C 다이얼업 모뎀

초기화 명령 : ATZ

프로토콜 및 전송 모드

마스터설정시 타임아웃 : 500 ms

전용

☐ 마스터

☐ 슬레이브

☐ 슬레이브 상태읽기

등록목록

Modbus

☐ 마스터

☒ 슬레이브

전송 모드 : ASCII

사용자 정의

☐ 마스터

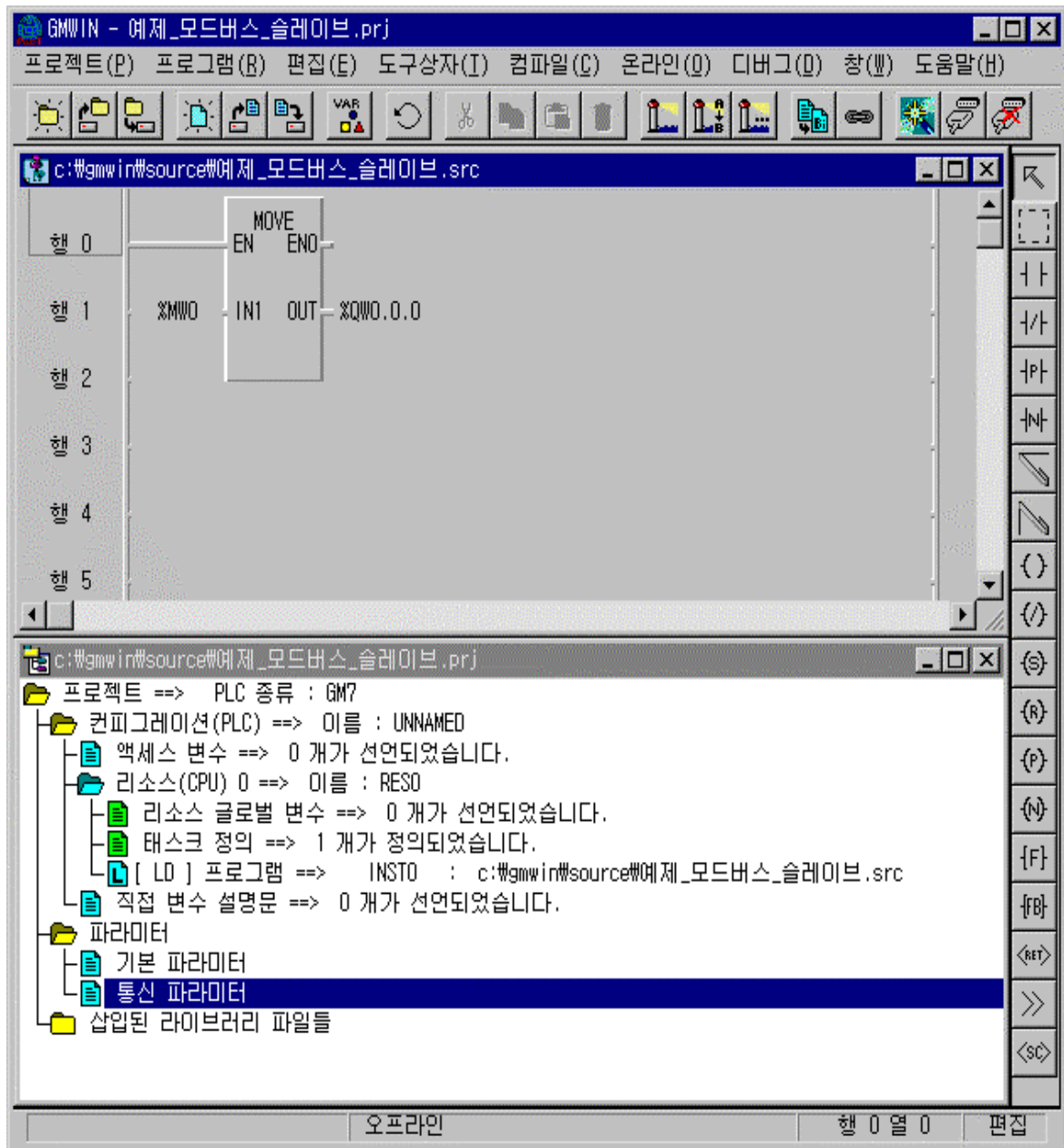
☐ 슬레이브

등록목록

확인 취소 도움말

						Modbus	
17	2400	7	EVEN	1	RS232C		
					RS422/485		

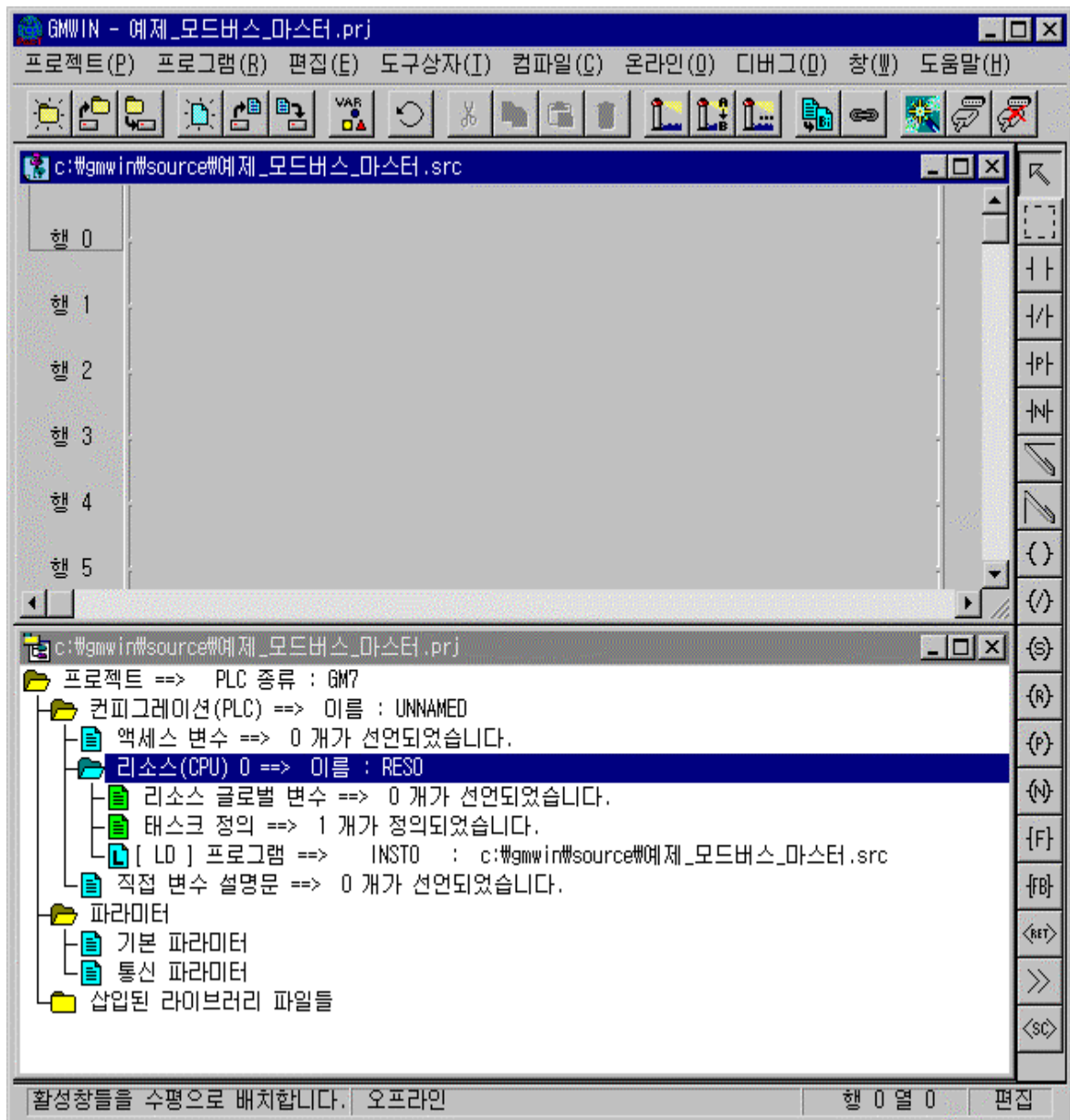
(3)

GM7
GMWIN

M

2)

(1)



(2) GMWIN

통신파라미터
✕

통신 방식

자국번 :

통신 속도 : 데이터 비트 :

패리티 비트 : 정지 비트 :

통신 채널

☒ RS232C 널모뎀 또는 RS422/485
☐ RS232C 전용모뎀 초기화 명령 :
☐ RS232C 다이얼업 모뎀

프로토콜 및 전송 모드

마스터설정시 타임아웃 : ms

전용

☐ 마스터 ☐ 슬레이브 상태읽기
☐ 슬레이브

Modbus

☒ 마스터 전송 모드 :
☐ 슬레이브

사용자 정의

☐ 마스터
☐ 슬레이브

						Modbus	
1	2400	7	EVEN	1	RS232C RS422/485		

(3)

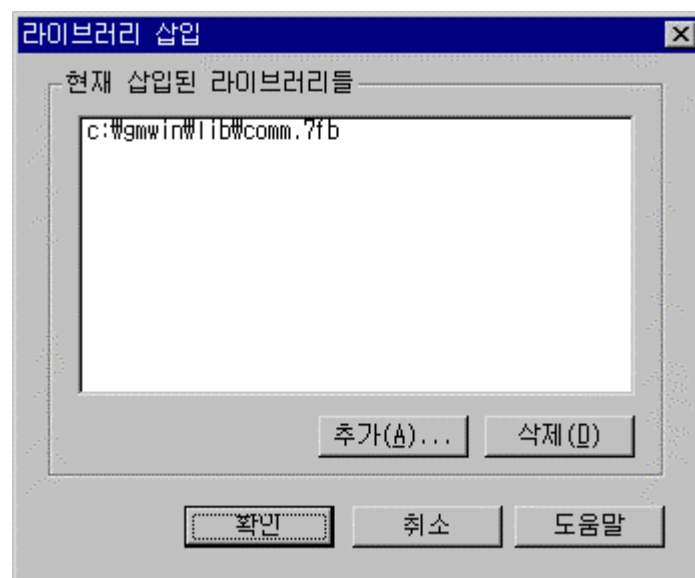
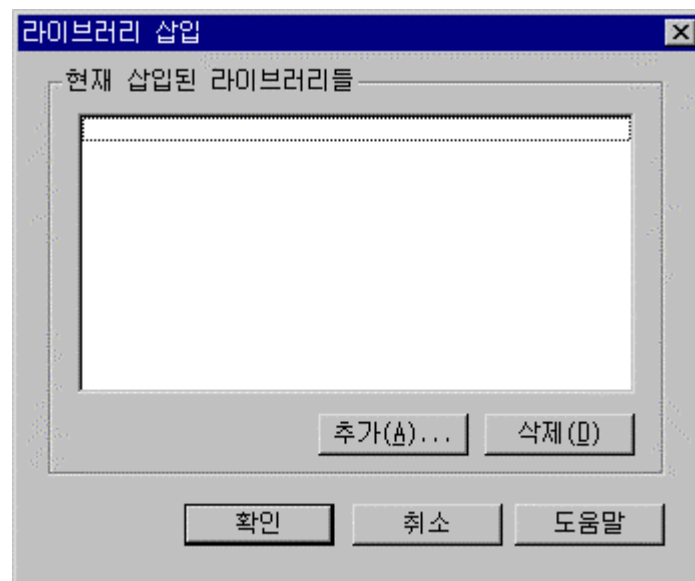
GM7
GMWIN

GMWIN

가(A)...

COMM.7FB

가



8 MODBUS

```

-          MOD0506(          06)    %MW0(%MX0 ~ %MX15    %MB0
~    %MB1          .)    16#FF(    255)
          MOD0102(          01)          %MX0
MOD1516(          15)          %MX0 ~ %MX9    0
          MOD0304(          03)          %MW0
-          8    LED 가 ON/OFF
-
    _RD_DB, _RD_DW
-
    .NDR,          .ERR,          .STATUS
-
    _1ON          1          ON
-
    REQ
-
    _BYTE_CNT
-
    가
-

```

_SLV_ADDR	USINT	17(H11)	_NH0102	USINT	0(H00)
_FUNC0102	USINT	1(H01)	_NH0304	USINT	0(H00)
_FUNC0304	USINT	3(H03)	_NH0506	USINT	0(H00)
_FUNC0506	USINT	6(H06)	_NH1516	USINT	0(H00)
_FUNC1516	USINT	15(H0F)	_NL0102	USINT	1(H01)
_AH0102	USINT	0(H00)	_NL0304	USINT	255(HFF)
_AH0304	USINT	0(H00)	_NL0506	USINT	1(H01)
_AH0506	USINT	0(H00)	_NL1516	USINT	10(H0A)
_AH1516	USINT	0(H00)	_RD_DB	ARRAY[40]	{0,0,...0}
_AL0102	USINT	0(H00)	_RD_DW	ARRAY[4]	{0,0,0,0}
_AL0304	USINT	0(H00)	_WR_DBW	ARRAY[4]	{0,0,0,0}
_AL0506	USINT	0(H00)	_BYTE_CNT	USINT	2(H02)
_AL1516	USINT	0(H00)			

8 MODBUS

8.5.2 MASTER-K

1

- 17 (Coil) 00020 ~ 00056

- 가

D1000

	59	58	57	56	55	54	53	52	51	50	49	48	47	46	45	44	43	42	41	40
	X	X	X	1	1	0	1	1	0	0	0	0	1	1	1	0	1	0	1	1
	1				B				0				E				B			
	39	38	37	36	35	34	33	32	31	30	29	28	27	26	25	24	23	22	21	20
	0	0	1	0	0	1	1	0	1	0	1	1	1	1	0	0	1	1	0	1
	2				6				B				C				D			

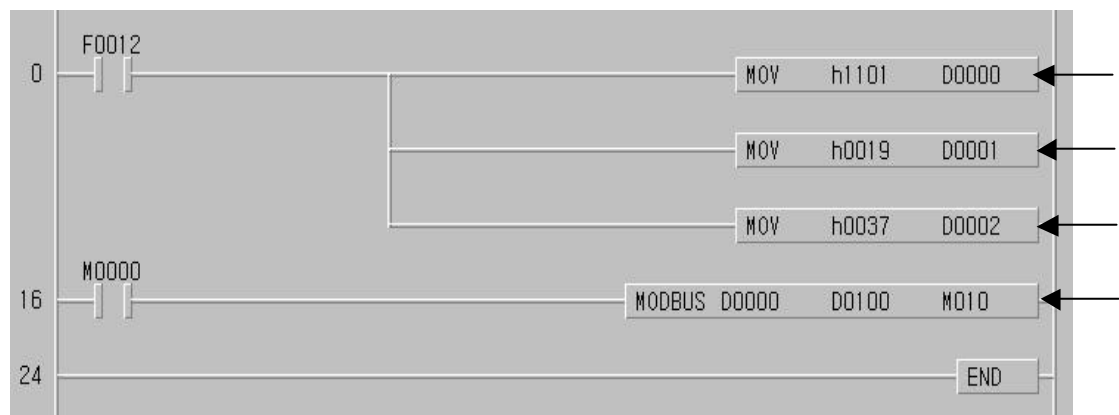
1) 57, 58, 59 (Redundancy)

2)

0

1)

3) CD 6B B2 0E 1B



17 = h11 = h01

MODBUS 0 1
20 19

20 56 37
MODBUS
D000 ~ D002 D1000

8 MODBUS

0

1)

1) CD 6B B2 0E 1B

D1000	h CD 6B
D1001	h B2 CE
D1002	h 00 1B

2

17

(Input) 10197 ~ 10218

가

M15

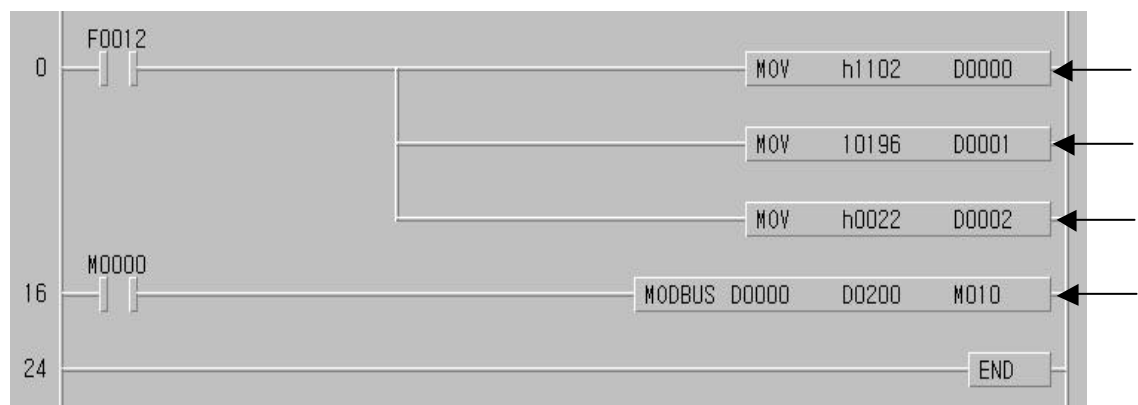
	10220	10219	10218	10217	10216	10215	10214	10213	10212	10211	10210	10209
	X	X	1	1	0	1	0	1	1	1	0	1
	3				5				D			
	10208	10207	10206	10205	10204	10203	10202	10201	10200	10199	10198	10197
	1	0	1	1	1	0	1	0	1	1	0	0
	B				A				C			

1) 10219, 10220 (Redundancy)

2)

0

1)



17 = h11 = h02

2) AC DB 35

8-46

9.1

9.1.1

가 .

1)

- (1) 가 .
- (2) 가 .
- (3) .
- (4) .
- (5) 가 0 ~ 55°C .
- (6) 가 5 ~ 95% .
- (7) 가 가 가 가 .

2)

- (1) 가 PLC 가 가 .
- (2) .
- (3) (Panel) .
- (4) 50mm .
- (5) .

3)

- (1) PLC PLC .
- , 가 PLC .
- (2) , .

9.1.2 PROFIBUS-DP

	Profibus-DP Smart I/O	32	
(1)			
(2)		,	
(3)			
(4)			Run
(5)			
(6)			
(7)			
(8)		,	
(9)			
(10)	LED	' 10	A/S
(11)	PLC		
(12)		GLOFA	LED GMWIN, MASTER-K KG-
(13)	WIN SMART I/O		' Off
	' On	,	
	Start	가	, 가
			.()

9.1.3 DeviceNet

DeviceNet Smart I/O

63

(1)

(2)

(3) Auto boaurate

가 ,

(4)

. PLC

(5)

(6)

(GDL-DT4A)

2

32 (, GDL-DT4A)

(7)

125k,250k,500kbps ,

Off

가

(8)

LED

GLOFA

GMWIN

1)

	Dnet I/F
	Thick /Thin
/	4,8 , :121Ω, 1%, 1/4W
24V	
	(Phoenix) 5 Female

2)

(1) 가

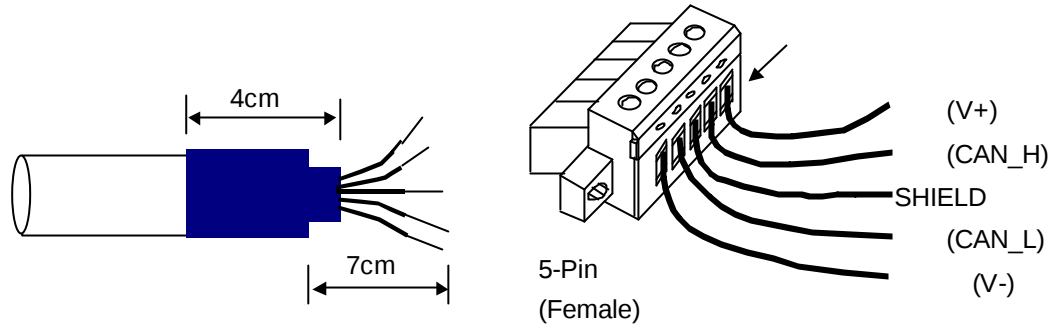
(2)

(3)

Off

(4) 가

3)



(1)

7cm 가

(2)

(3)

4cm

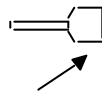
(4)

3mm 가

(

가

.)



(,)

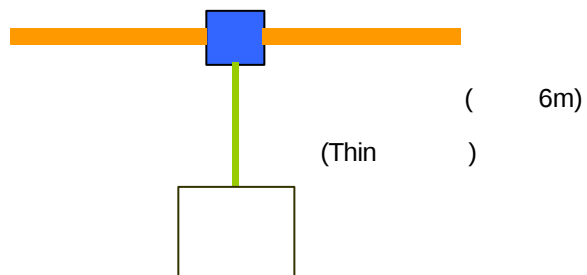
(5)

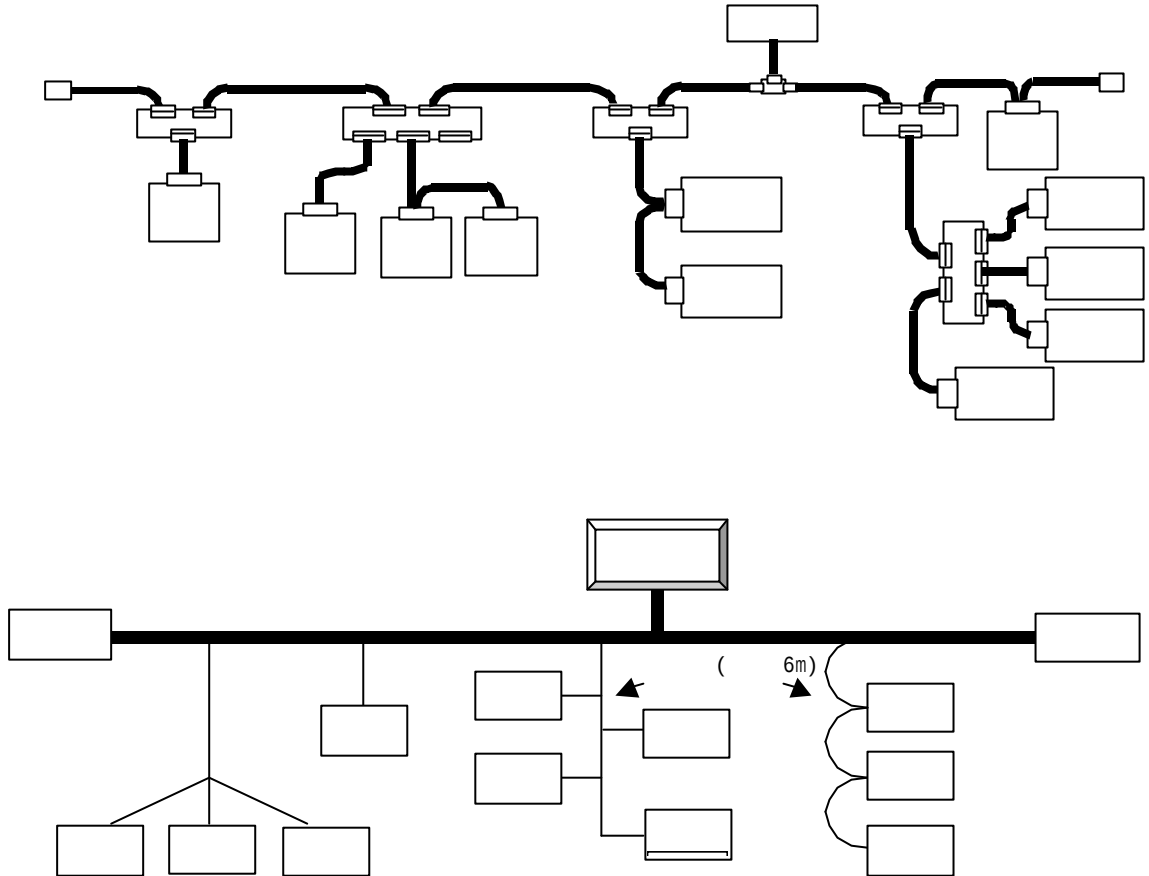
.(

)

DC 24V

Smart I/O

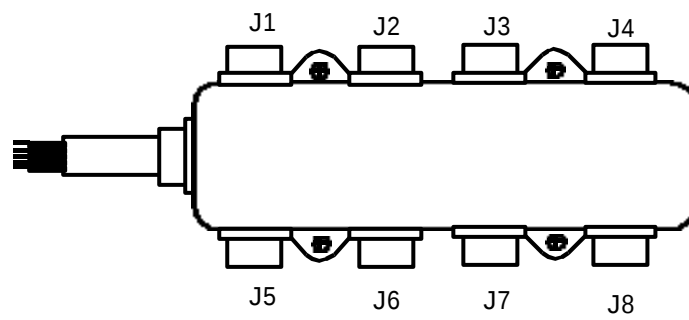




4) (8-Port)

8

가 가



- (1) Thick Thin
가 Open-Style 3 가
- Pluggable screw
- Hard-wired screw
- Soldered
- (2) 가

(3)

THICK	500 m
THIN	100 m

500 kbps	$L_{THICK} + L_{THIN} \leq 100 \text{ m}$
250 kbps	$L_{THICK} + 2.5 * L_{THIN} \leq 250 \text{ m}$
125 kbps	$L_{THICK} + 5 * L_{THIN} \leq 500 \text{ m}$

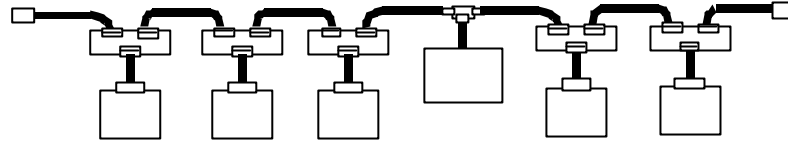
$L_{THICK}:THICK$ (8A), $L_{THIN}:THIN$ (3A)

	THICK	THIN
500 kbps	100 m	100 m
250 kbps	250 m	
125 kbps	500 m	

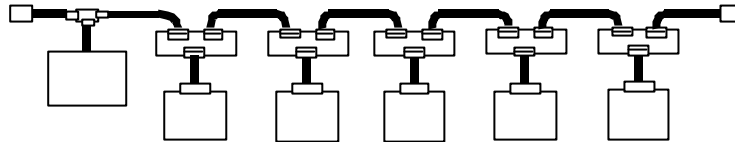
가 500kbps , 6m
39m , 250kbps , 6m ,
78m , 125kbps ,
가 6m , 156m .

5)

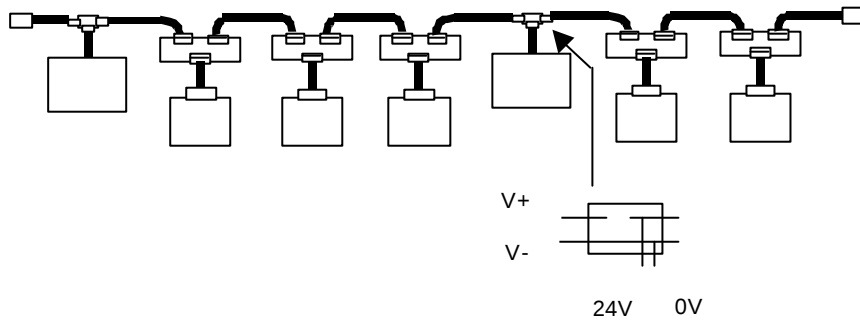
1)



2)

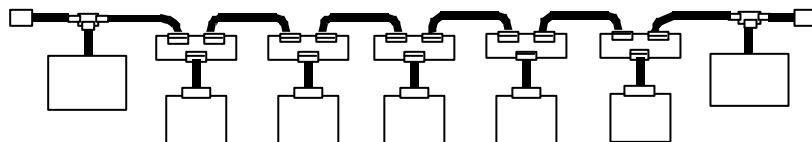


3)



4)

2



3m

9.1.4 FIELDBUS

- FIELDBUS Smart I/O 63 가 .
- 1)
 - 2) Run
 - 3)
 - 4)
 - 5)
 - 6)
 - 7) (GRL-DT4A) 2
31 (, GRL-DT4A)
 - 8)
 - 9) () 9
 - 10) LED ' 10
A/S
 - 11) PLC
 - 12) LED
GLOFA GMWIN, MASTER-K KGL-WIN
 - 13) SMART I/O ' Off ' On
Start
가 , 가
()

9.1.5 MODBUS

Modbus Smart I/O 32

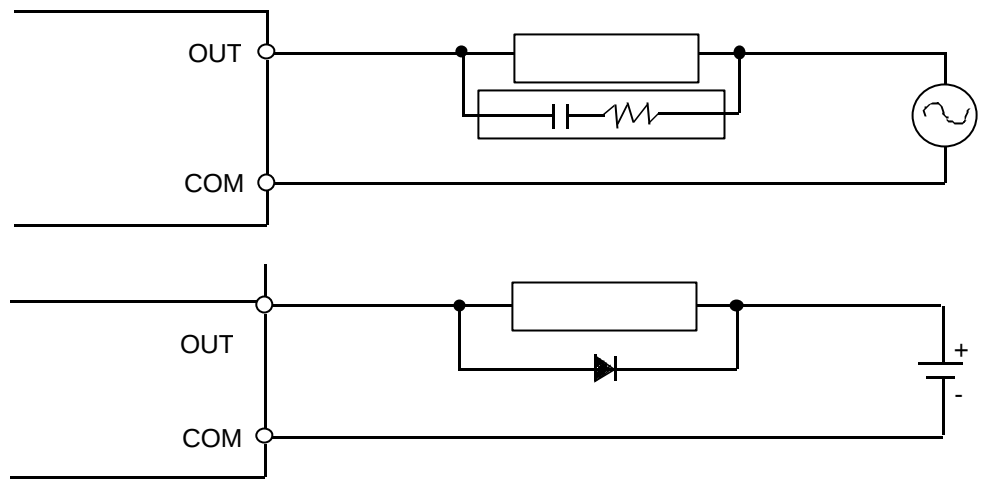
- 1) 가 Cnet I/F ,
가
- 2) RS-422/485
Modbus RS-422
- 3)
- 4)
- 5)
- 6) RS-422/485 TX/RX
TX RX가 TX TX
RX RX (RS-422)
- 7) RS-485 Cnet I/F TX RX
- 8)
- 9)
- 10) LED ' 11
A/S
- 11) SMART I/O 'Off 'On
Start 가
가
()

9.1.6

-
- PCB
- 가

1)

- (1)
가 ,
- (2)
AWG24(0.18mm²)
- (3)
가 ,
- (4)
가
DC24V AC
DeviceNet 24V
가
- (5)
 -
 - (LED) 가
 - (
 - 가 (Surge Killer) +



(6)

가

가 PLC

(7)

가

, PCB

2)

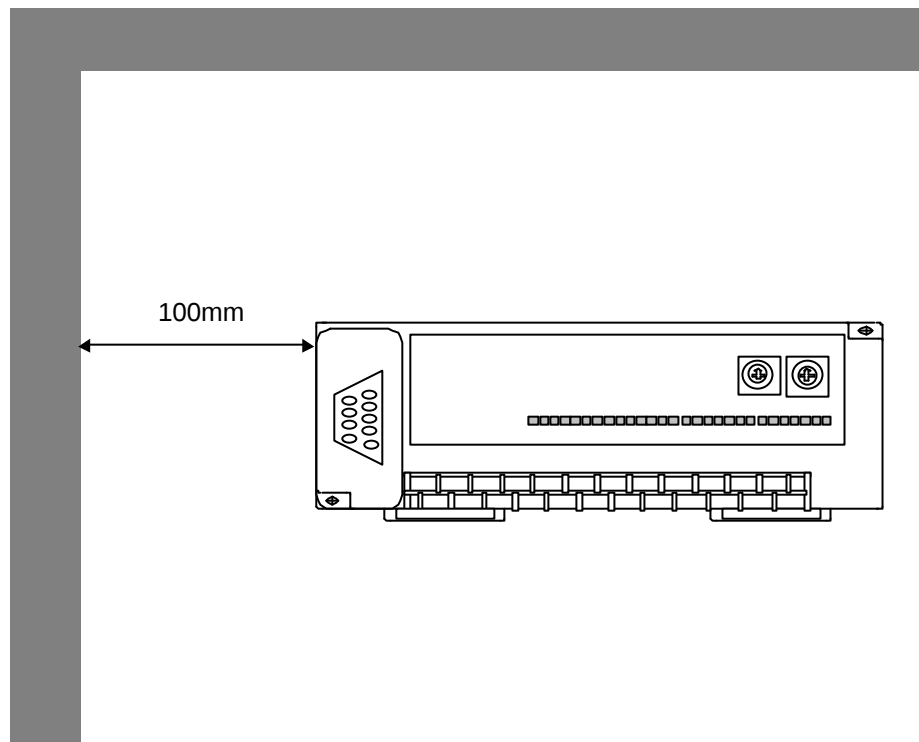
PLC

(1)

(3)

100mm

(2)



(3)

(Panel)

(4)

, PLC

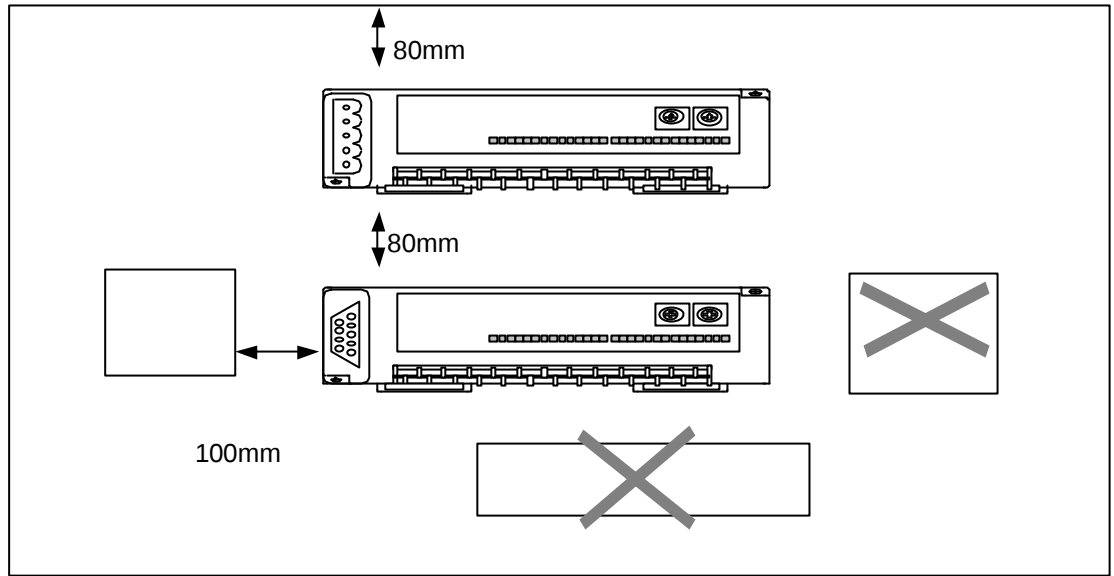
가

- PLC

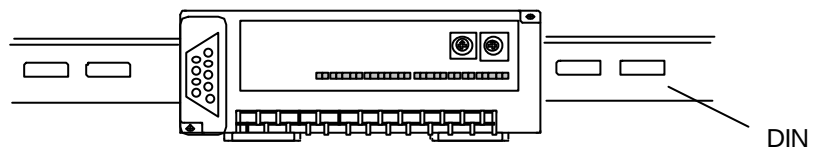
50mm

- PLC

- (5) () 100mm 100mm PLC 가



- (6) Smart I/O DIN(35mm) (Hook) DIN 가 가

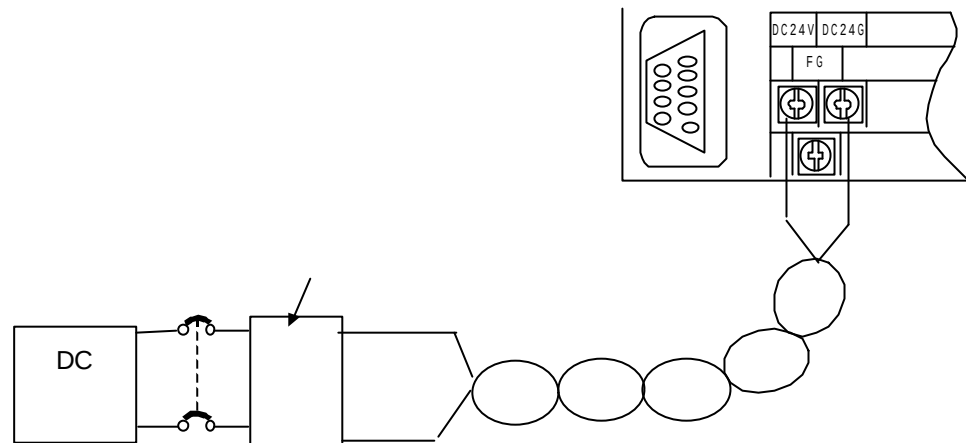


9.2

9.2.1

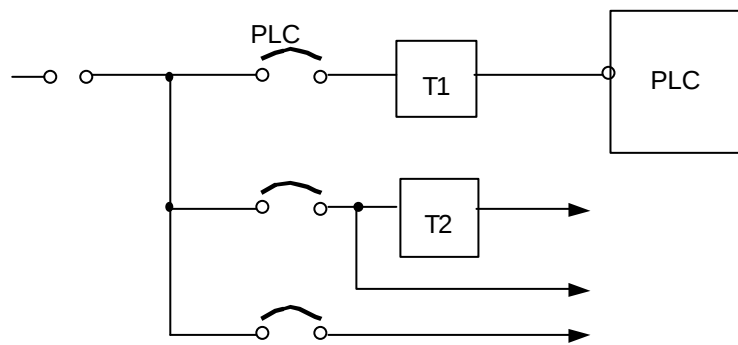
- 1) DC 24V
- 2)
- 3)

가



- 4) (가 가 .)

- 5) PLC

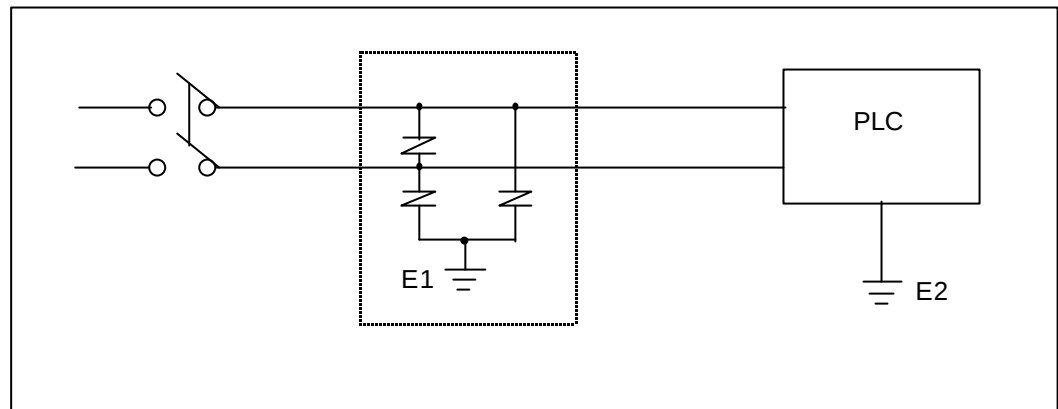


T1,T2:

6) 가 (2mm^2)

7) DC24V (,) ,
가 80mm

8)



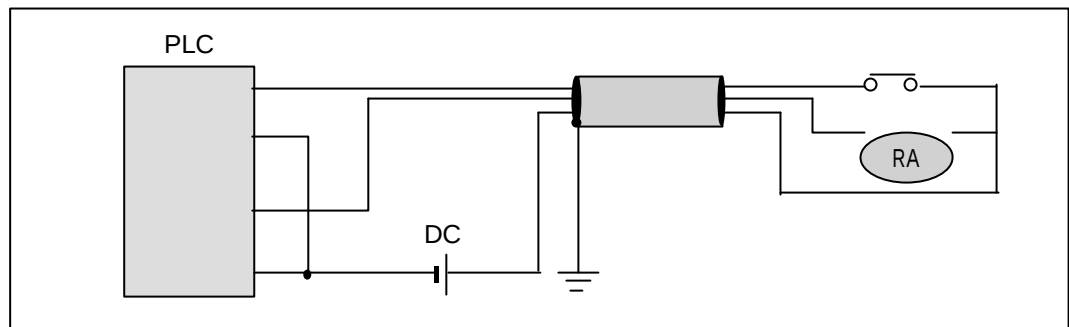
- 1) (E1) PLC (E2)
- 2)

9) 가

10)

9.2.2

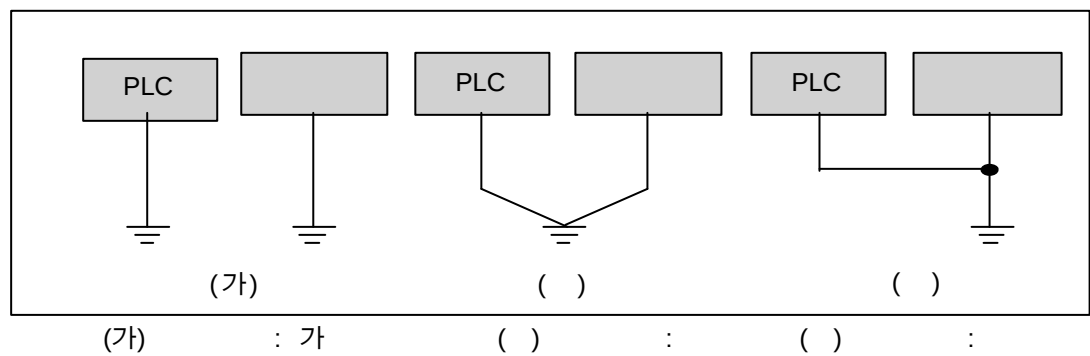
- 1) $0.18 \sim 2 \text{ mm}^2$, (0.5 mm^2)
 2) .
 3) 80mm
 4) ,
 PLC

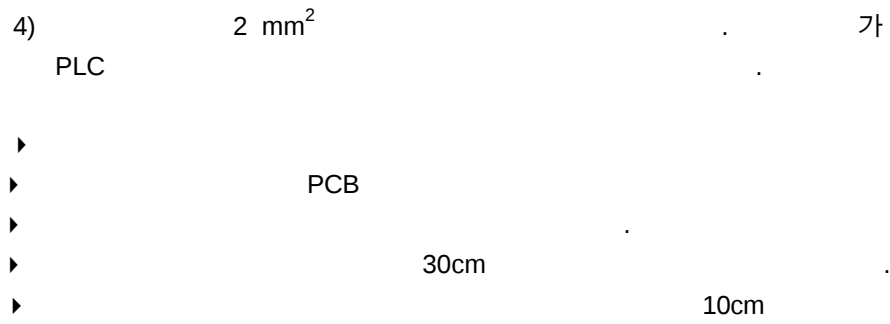


- 5)
 6) DC24V AC110V AC220V
 7) 200m

9.2.3

- 1) PLC , 가
 2) 가
 3 (80Ω)
 3))





9.2.4

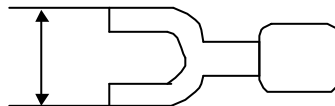
	(mm ²)	
	0.18 (AWG24)	1.5 (AWG16)
	0.18 (AWG24)	2.0 (AWG14)
	0.18 (AWG24)	1.5 (AWG16)
	0.18 (AWG24)	1.5 (AWG16)
	1.5 (AWG16)	2.5 (AWG12)
	1.5 (AWG16)	2.5 (AWG12)

Smart I/O

- M3
- 6 ~ 9 kg · cm
-

()

6.2mm



PLC

10.1

6 1~2

		0 ~ +55℃	가
		5 ~ 95%RH	
		- 15% / +10%	

10.2

SMART I/O

1) Profibus-DP

LED	PWR LED		3
	TX LED	()	3
	RX LED	(Smart I/O)	3

2) DeviceNet

LED	PWR LED		3
	MS LED	()	3
	NS LED	(Smart I/O)	3

3) Fieldbus

LED	PWR LED		3
	TX LED	(	3
	RX LED	Smart I/O (Smart I/O)	3

4) Modbus

LED	PWR LED		3
	TX LED	(	3
	RX LED	(Smart I/O)	3

10.3

6 1~2

		/	0 ~ 55	(
			5 ~ 95%RH	
		가	가 가	
	,			
	,			
			AC 85 ~ 132V AC 170 ~ 264V	

11

11.1

가

가

1)

- (,)
- 가
-
- (,)
- (POWER LED, Run LED, ERR. LED, LED)
- PLC

2)

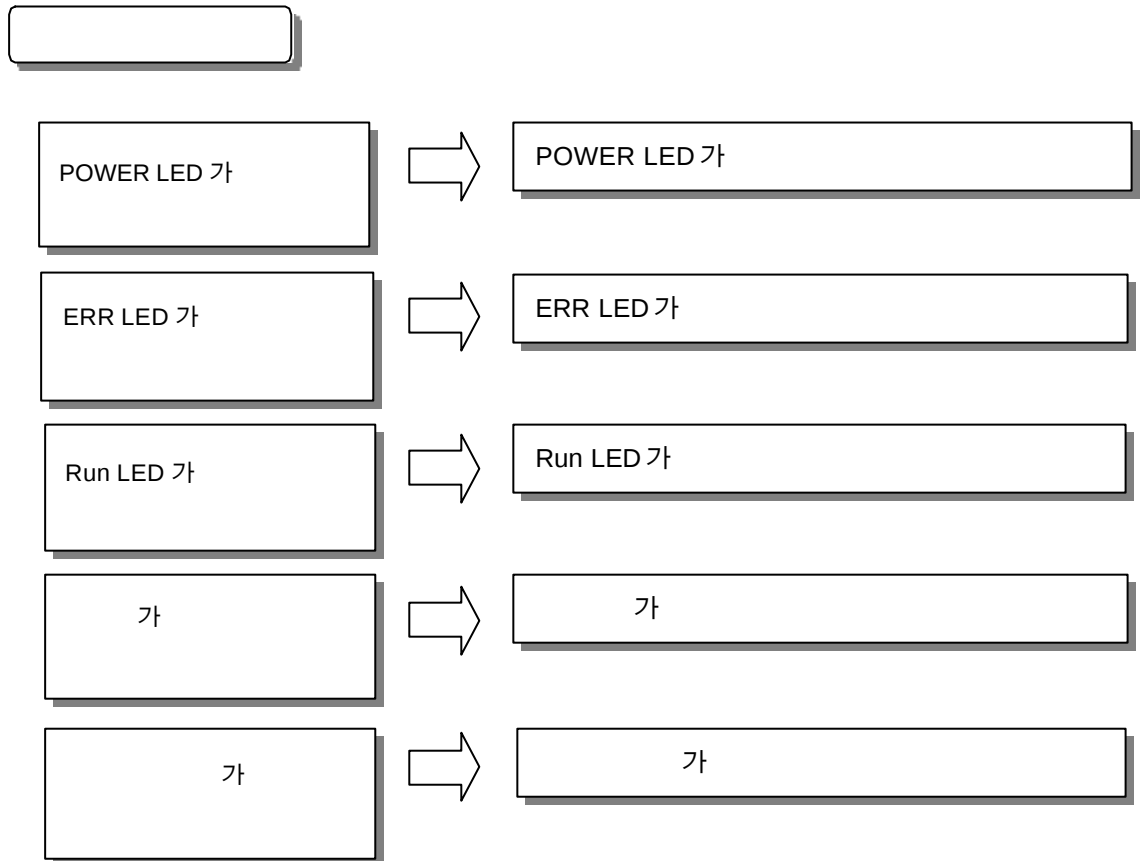
가

- Stop On / Off

3)

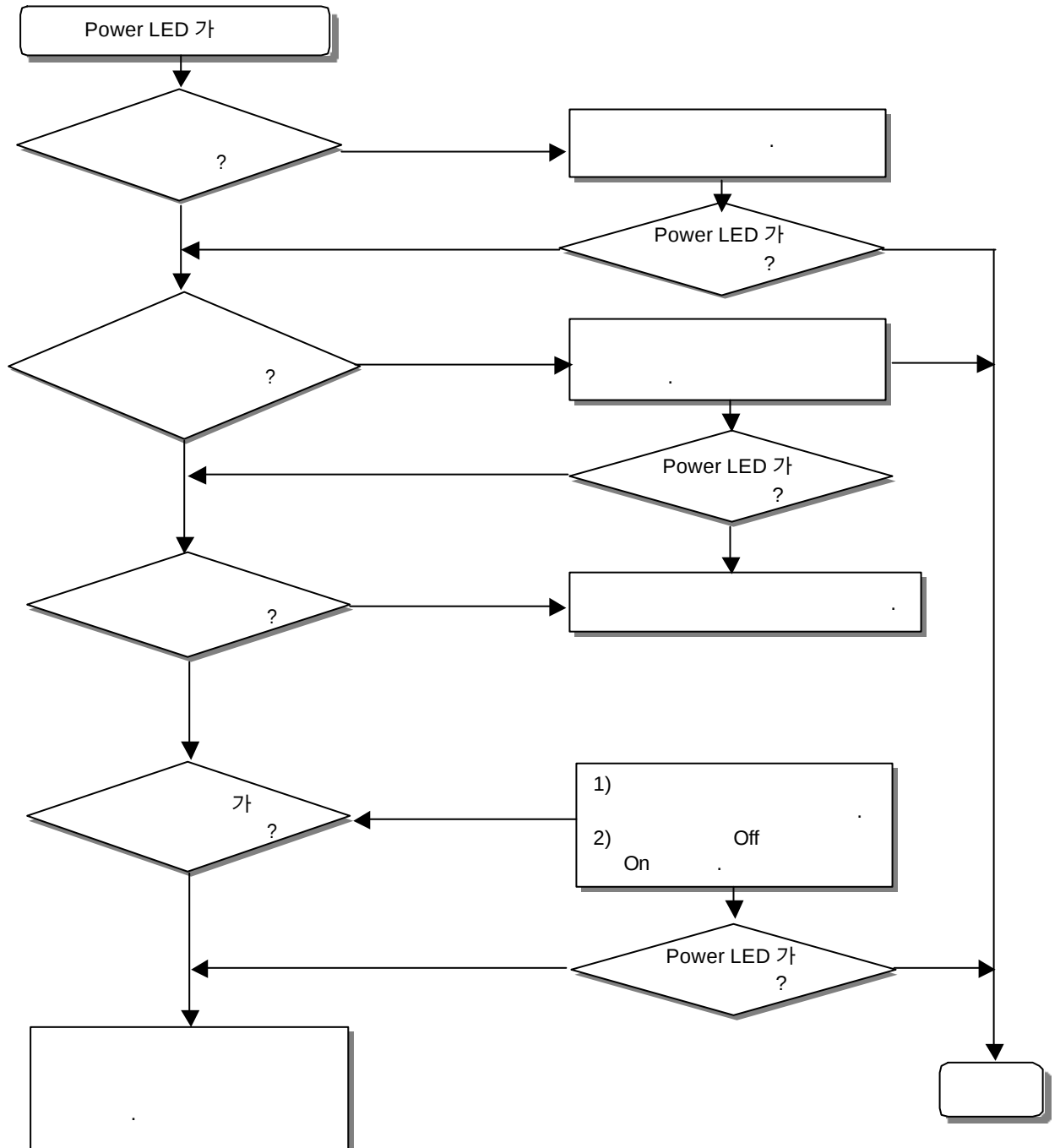
가

- PLC 가? 가?
- 가? 가?
- PLC 가?



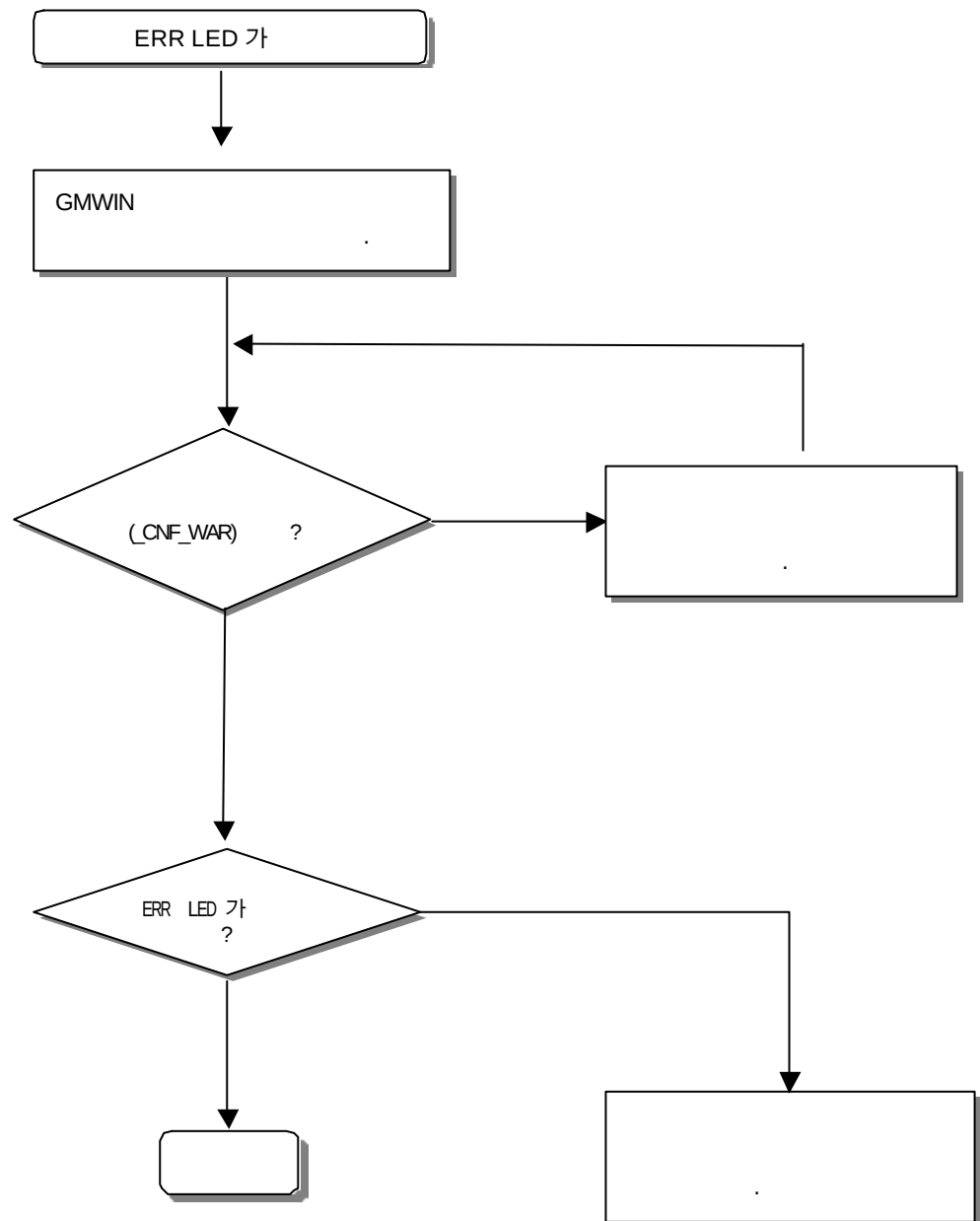
11.2.1 POWER LED 가

Power LED 가



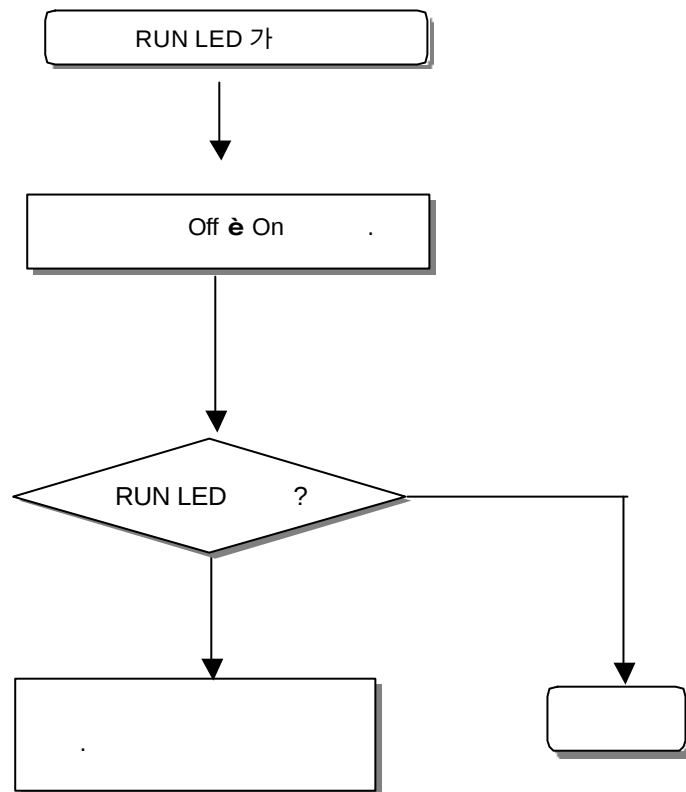
11.2.2 ERR LED 가

ERR LED 가

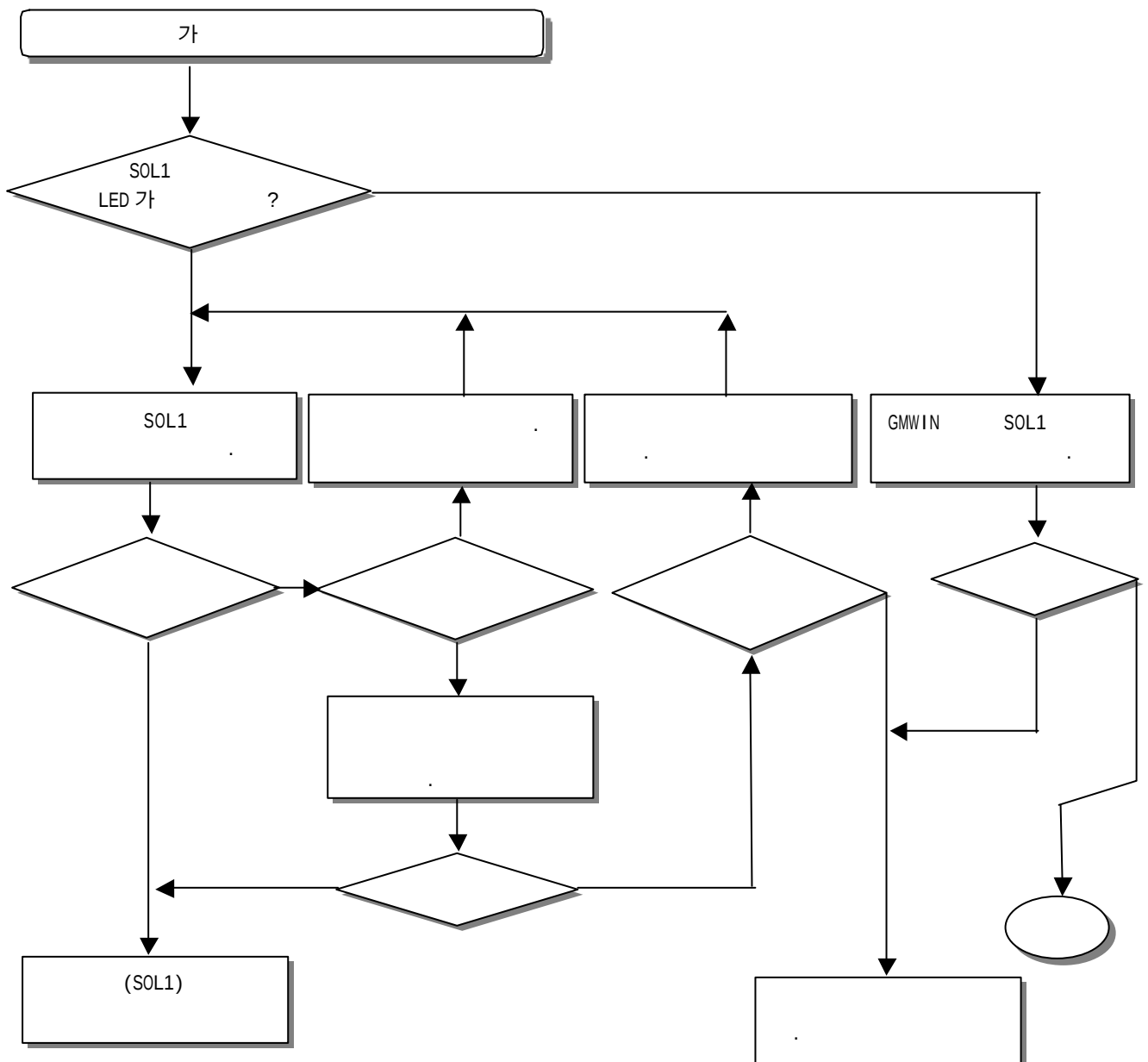


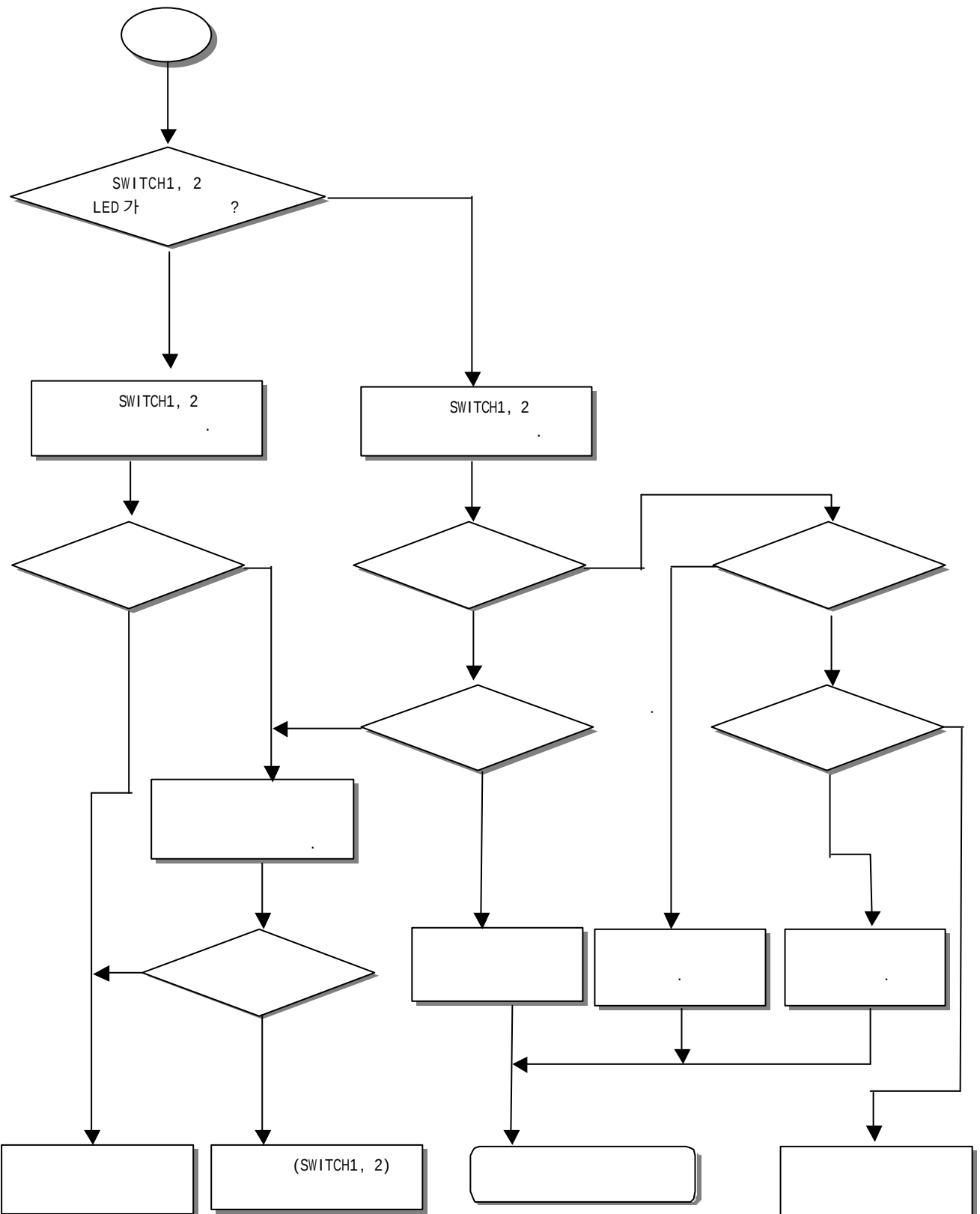
11.2.3 RUN LED 가

, RUN LED 가



가



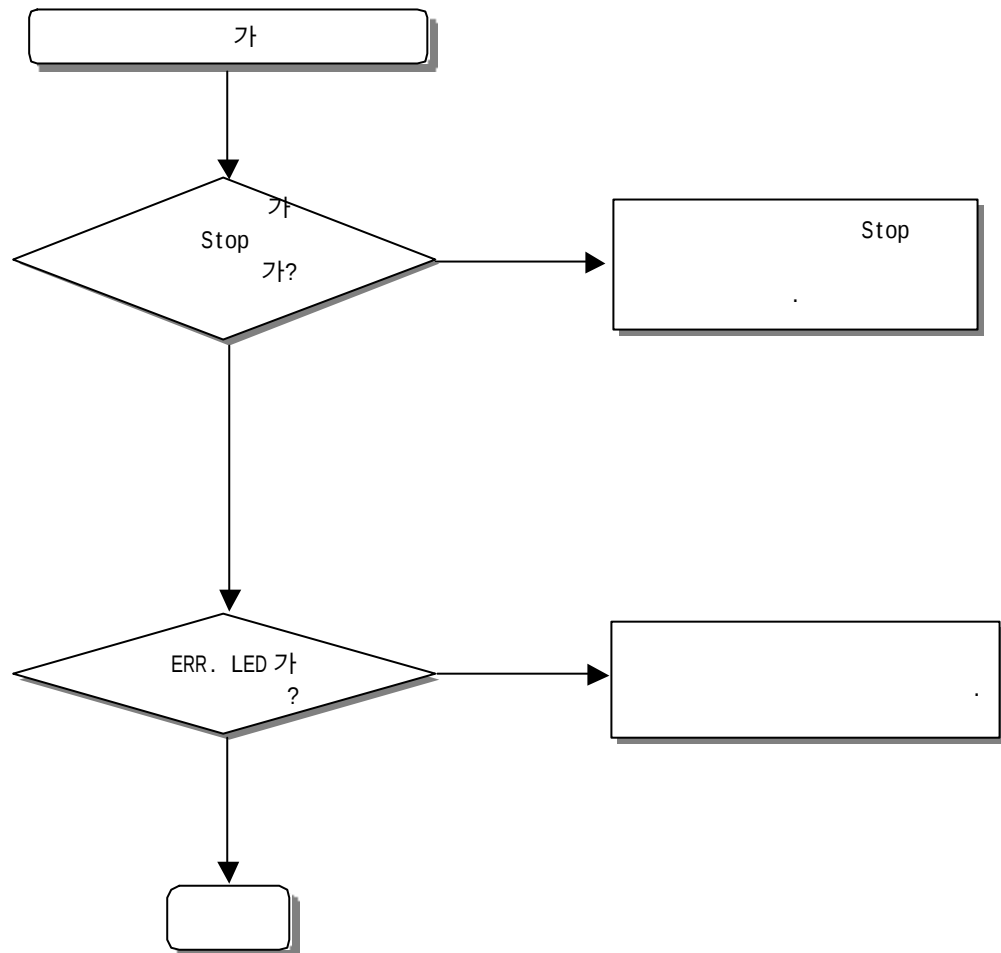


11.2.5

가

CPU

가



11.3

SMART I/O
FAX

- , .

1. :) _____
FAX) _____
2. : () _____
3.
– : – OS (), –
()
– GMWIN : ()
4. :

5. :
6. ERR LED ? Yes(), No()
7. GMWIN :
8. 7 :
9. :
10.
• () : (), ()
()
• () : :
12. :

13. :